

FIG. 1

FIG. 2

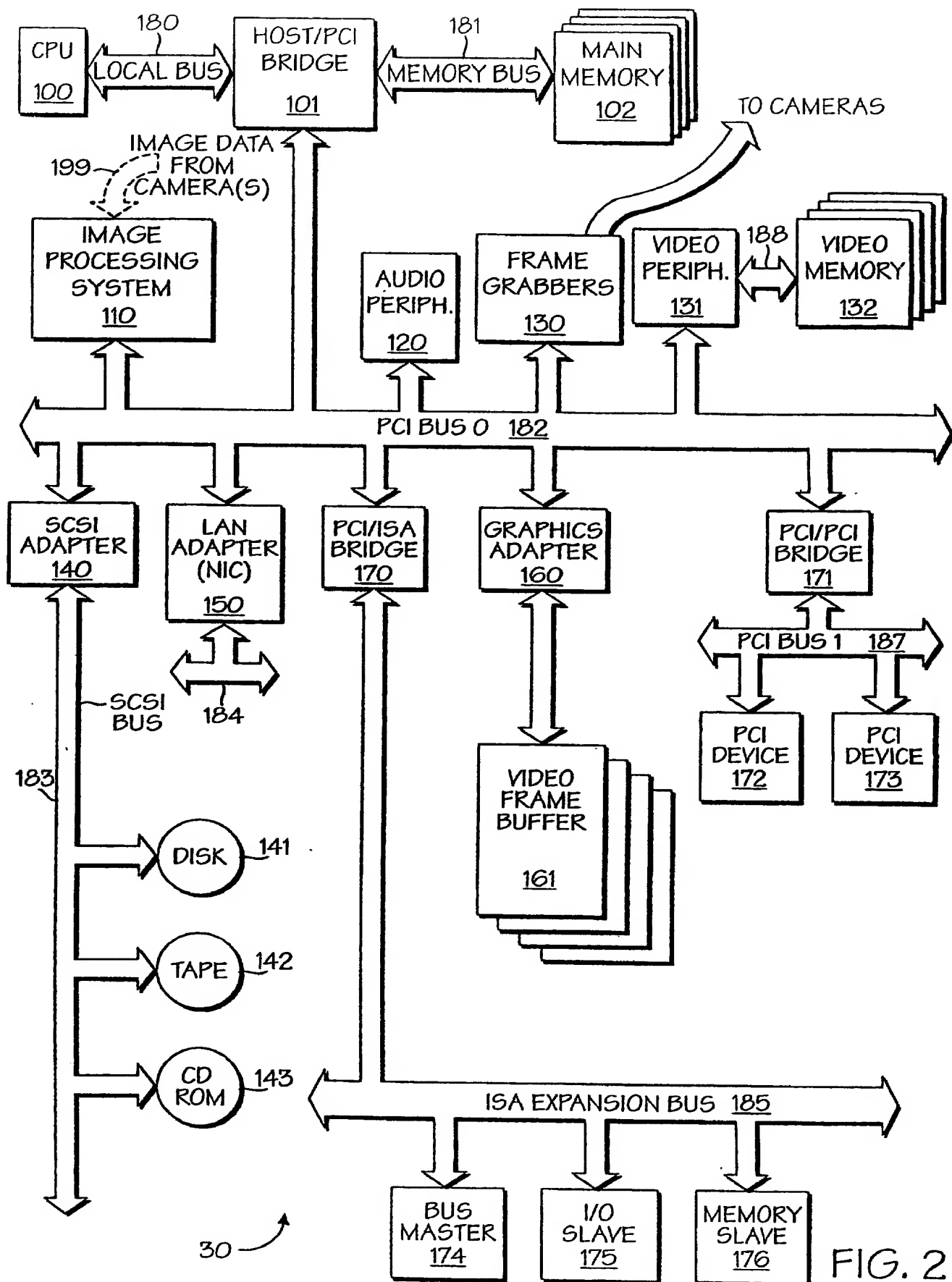


FIG. 2

10020862 121401

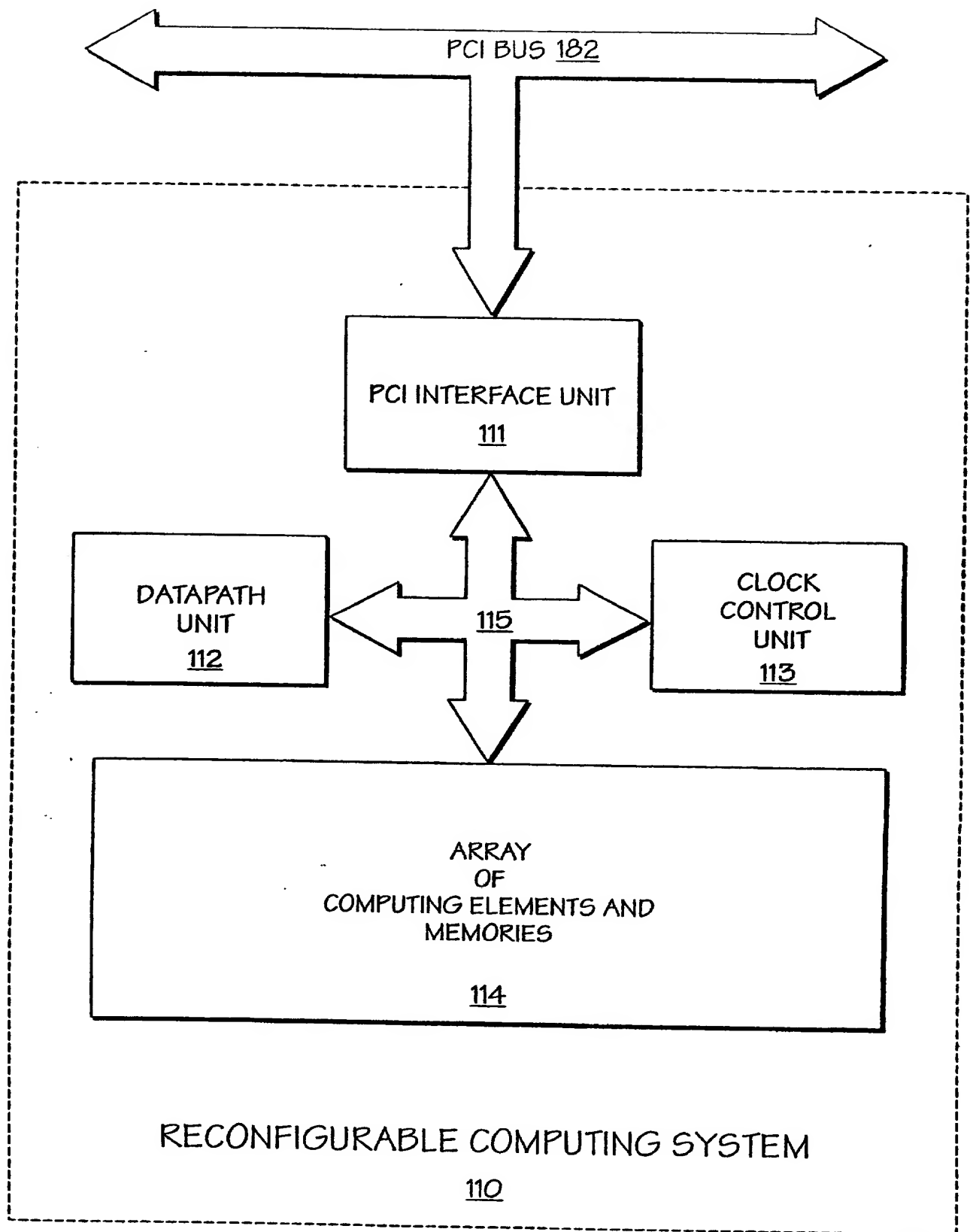


FIG. 3

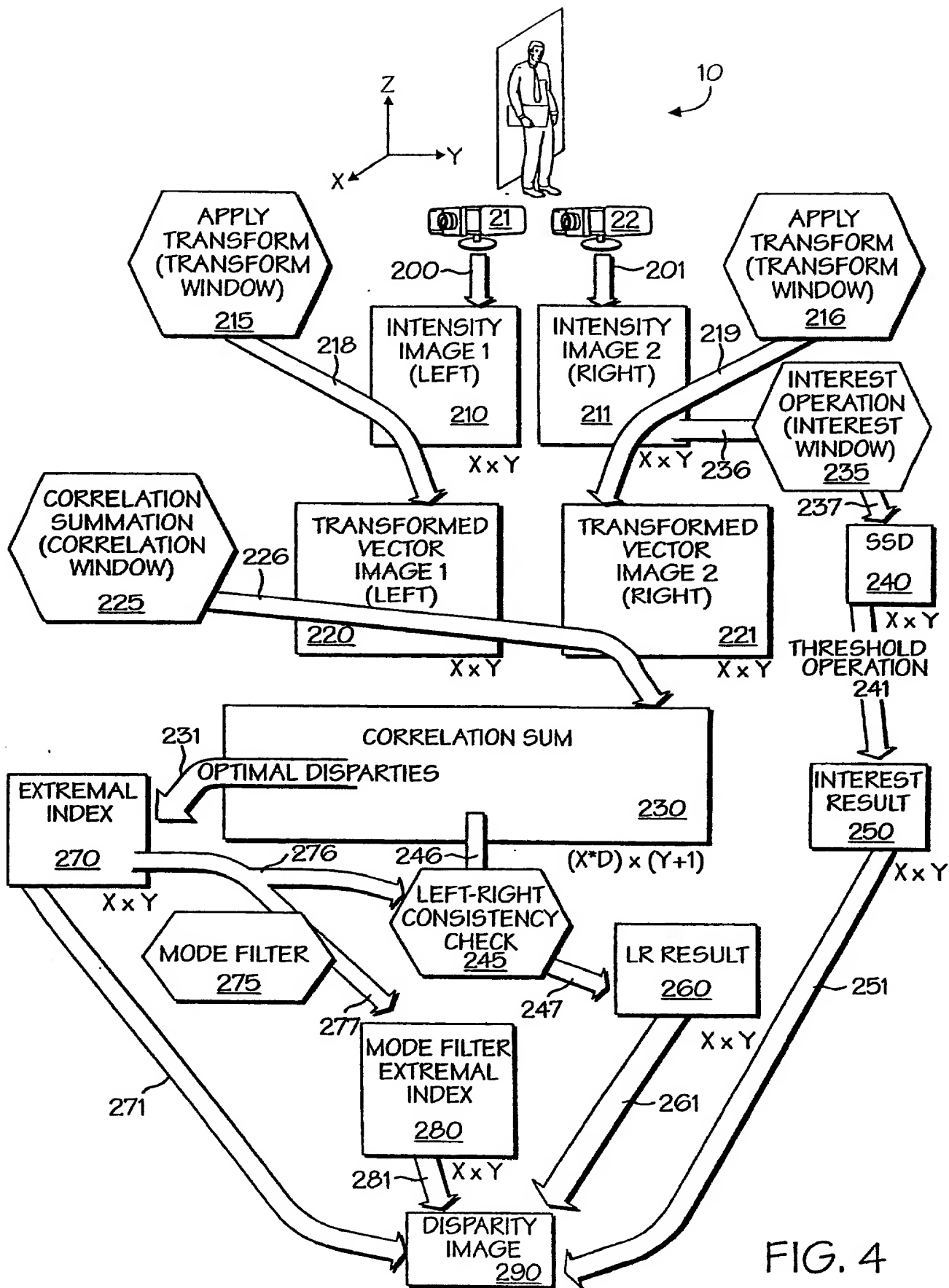


FIG. 4



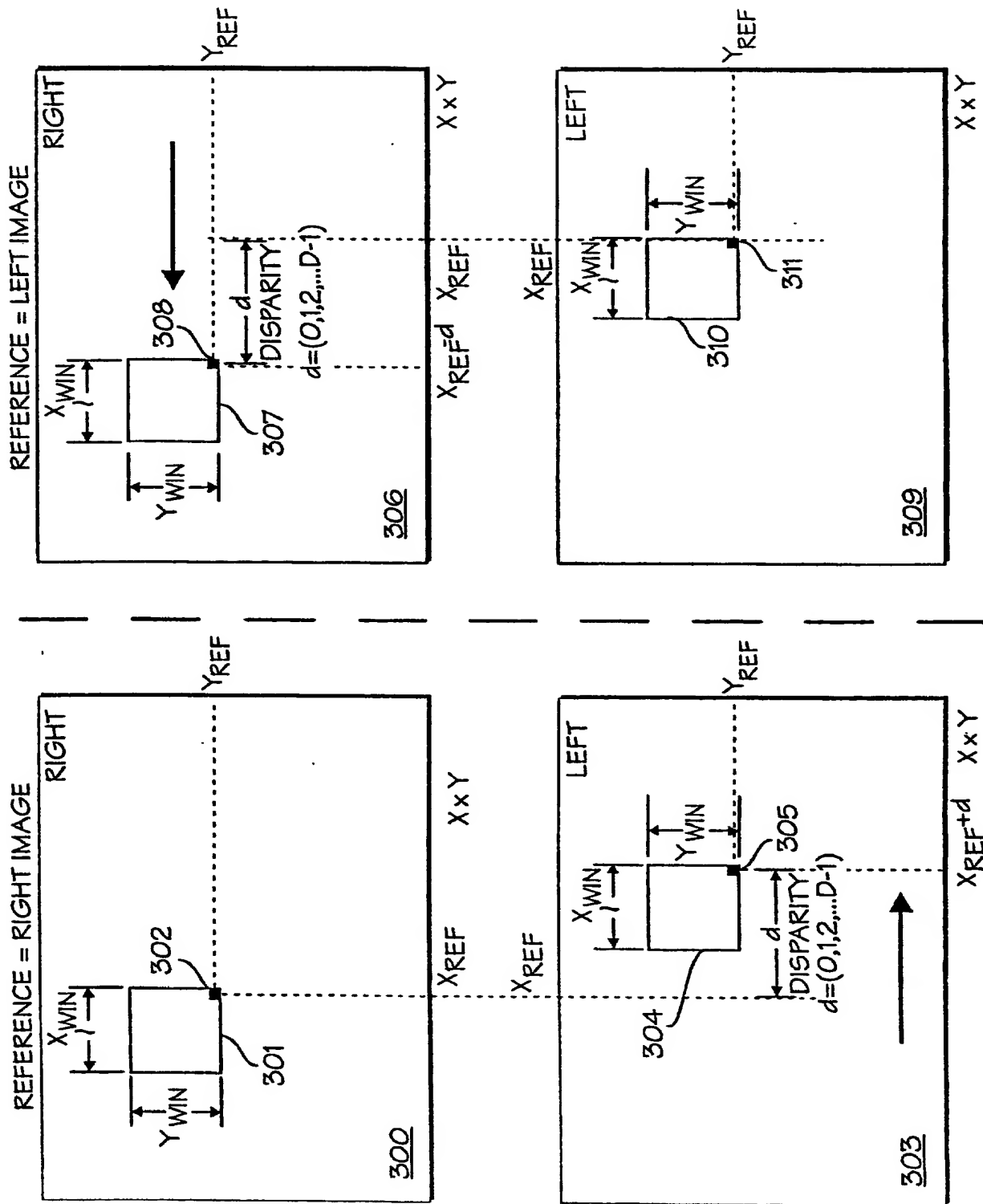
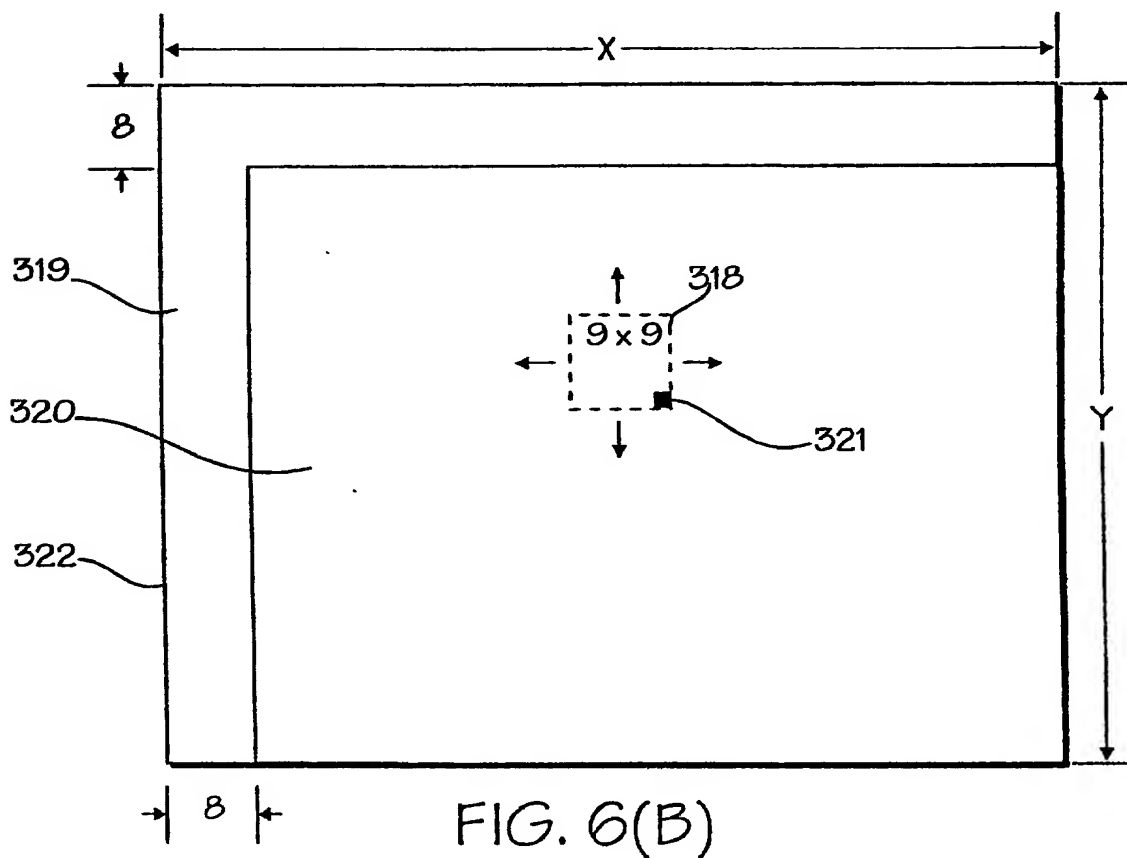
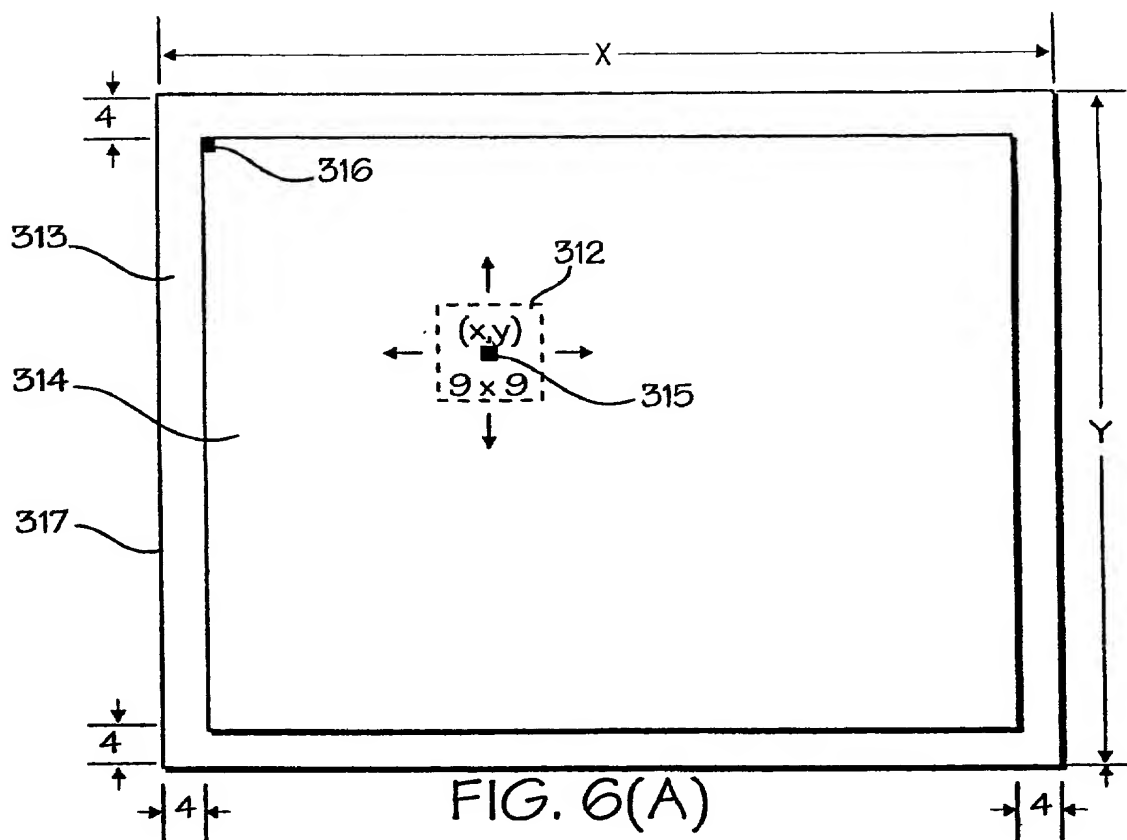


FIG. 5(A)

FIG. 5(B)



DATA FOR CENSUS VECTOR  
CENTERED AT (x,y)

		X								
		-4	-3	-2	-1	0	1	2	3	4
Y	-4						1		2	
	-3	3		4		5		6		
	-2		7		8		9		10	
	-1	11		12		13		14		
	0		15		16	(x,y)		17		18
	1		19		20		21		22	
	2			23		24		25		26
	3		27		28		29		30	
	4			31		32				

324

↑  
323

Fig. 7

Fig. 8(A)

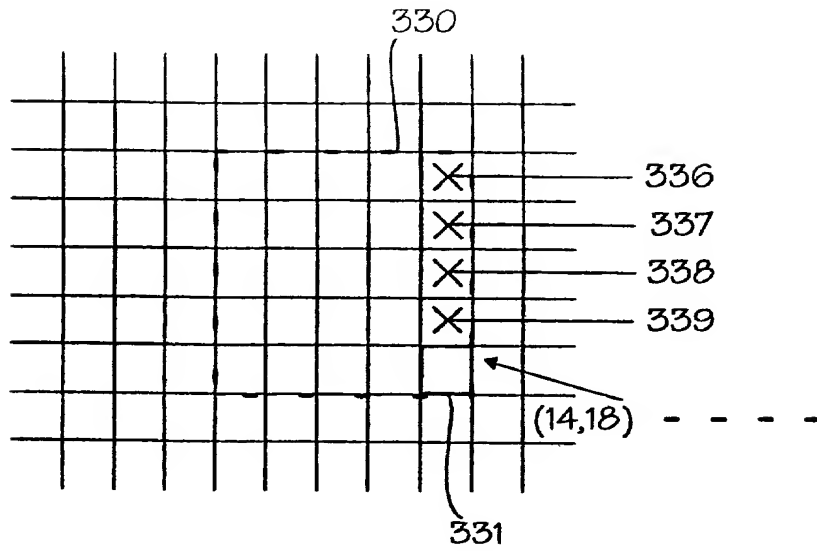


Fig. 8(B)

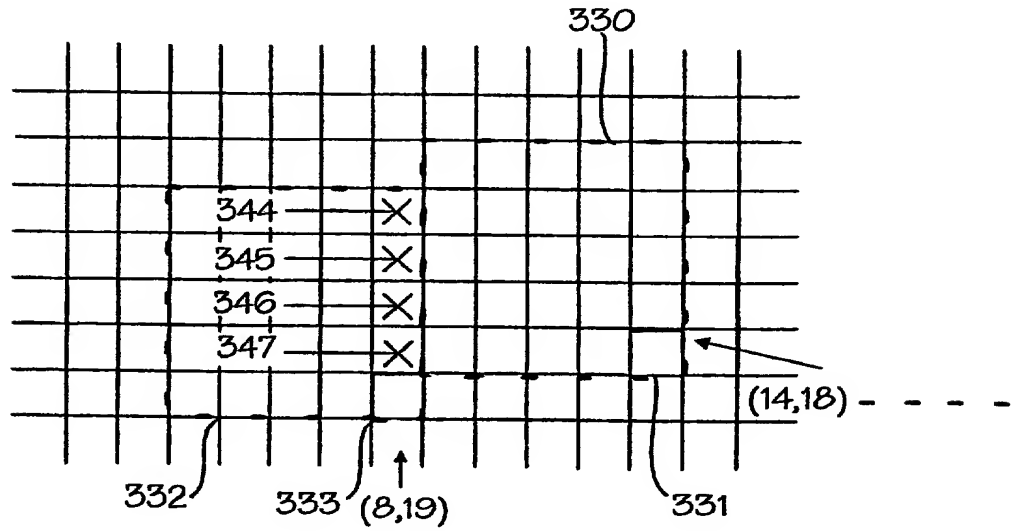


Fig. 8(C)

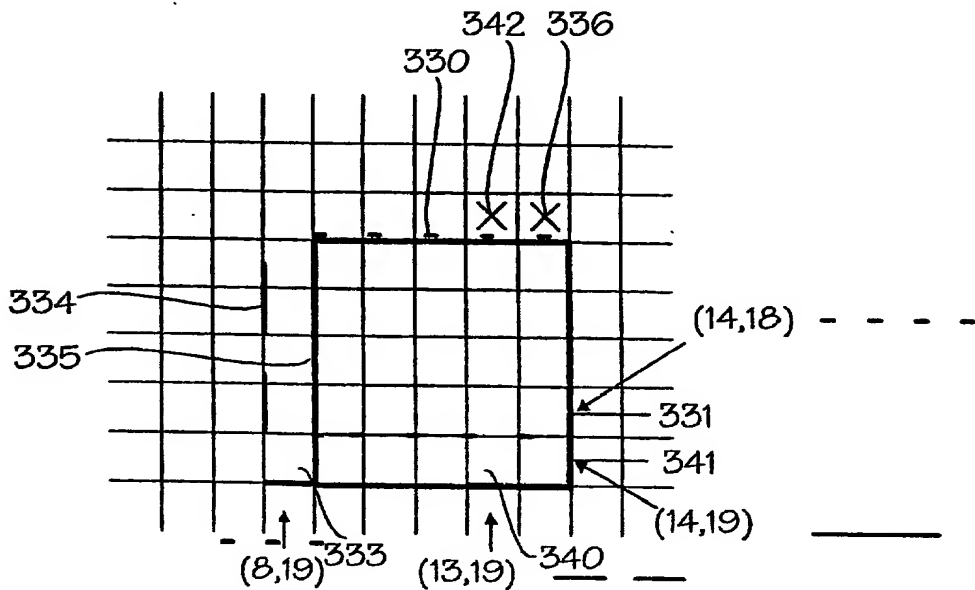


Fig. 9(A)

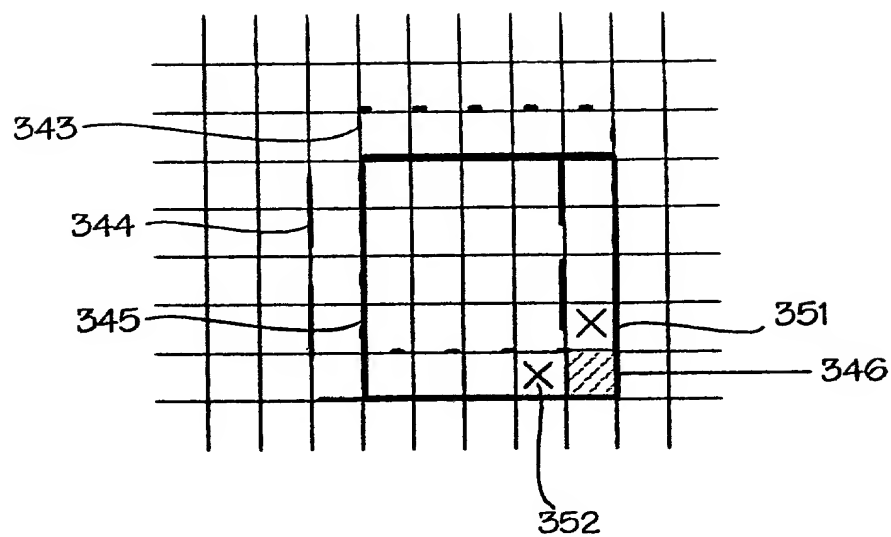


Fig. 9(B)

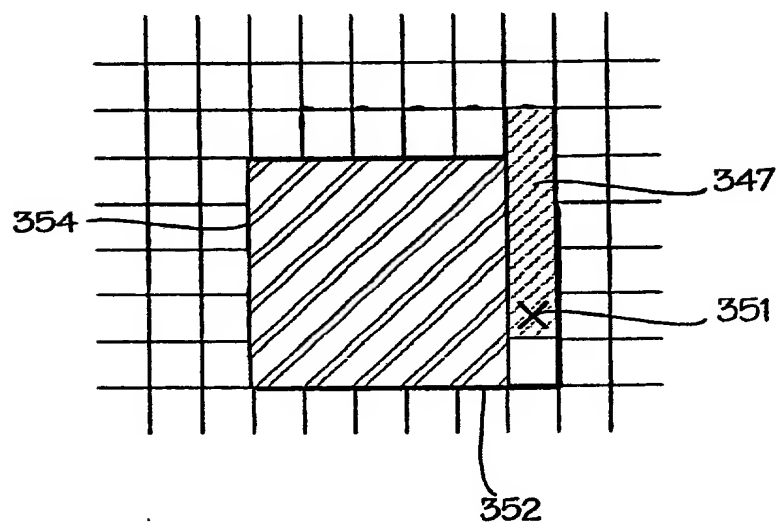
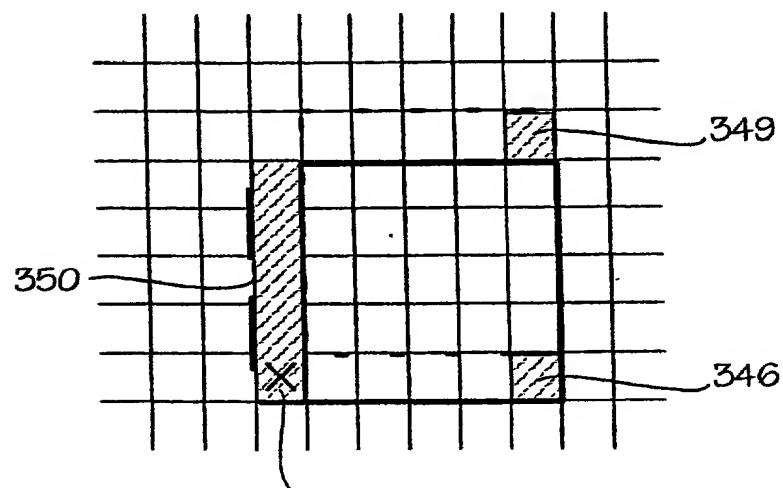


Fig. 9(C)



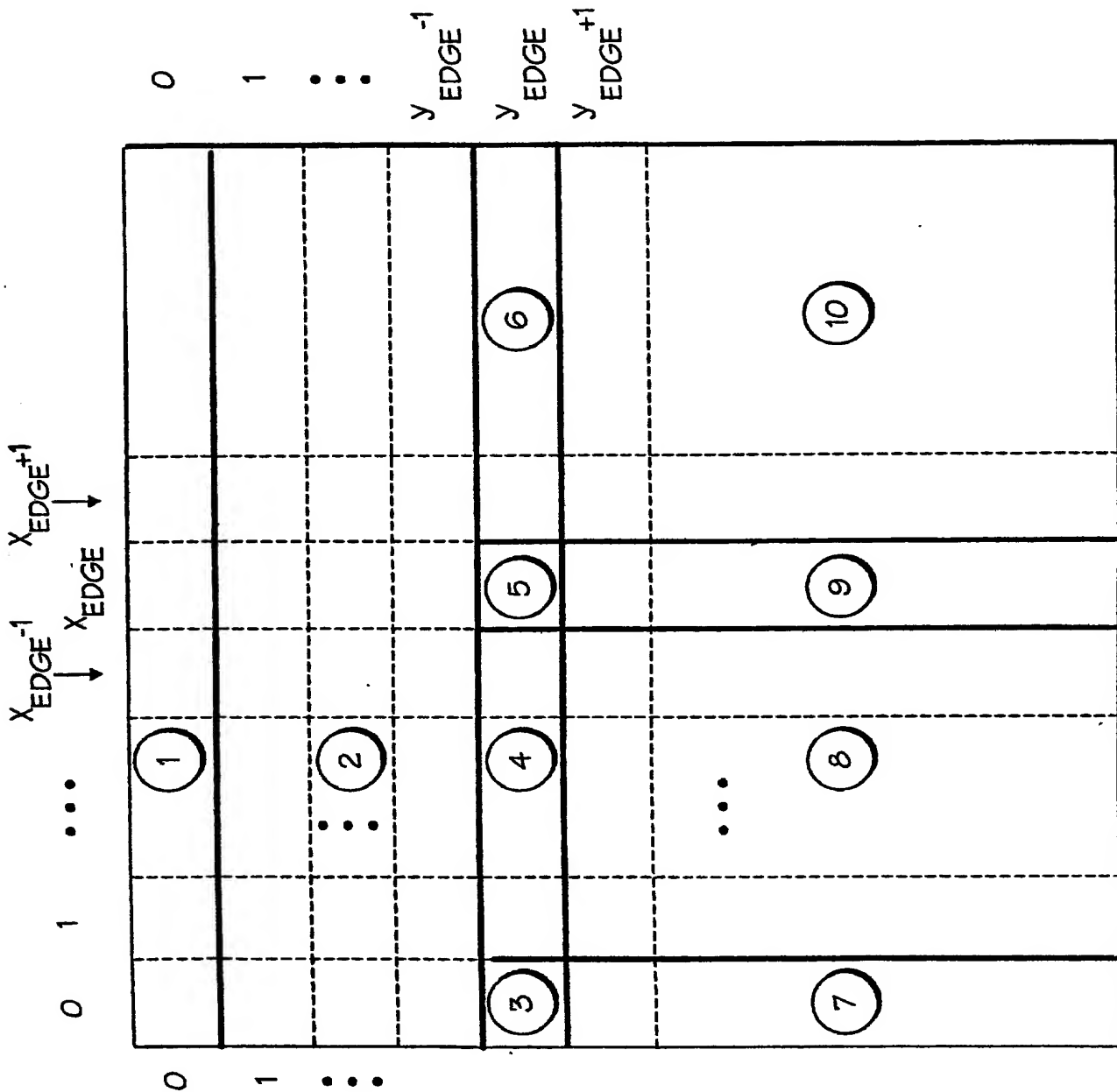


Fig. 10A

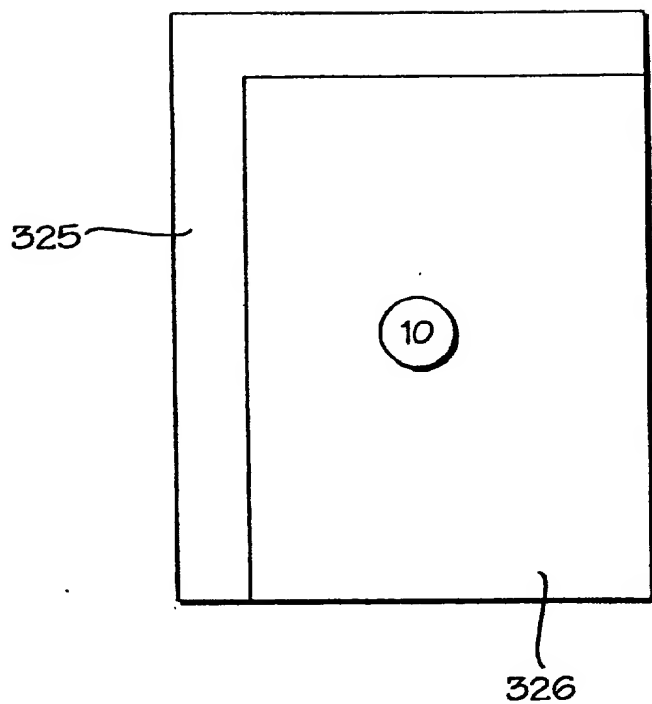


Fig. 10(B)

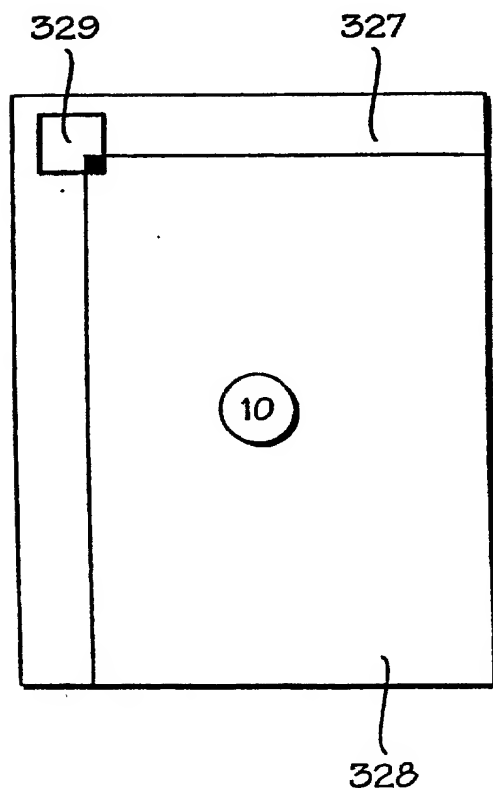
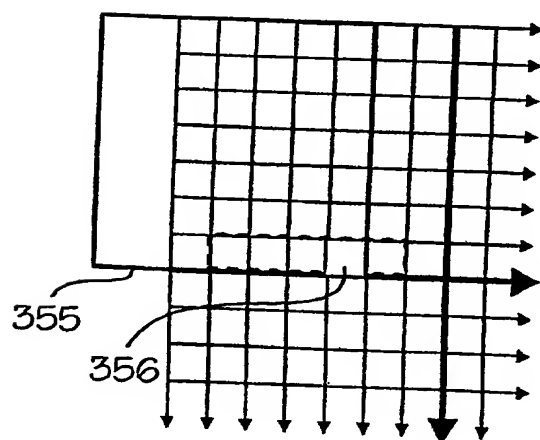


Fig. 10(C)







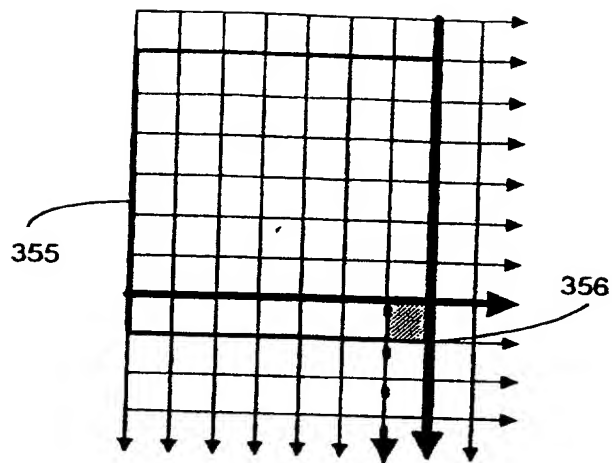


Fig. 11 (I)

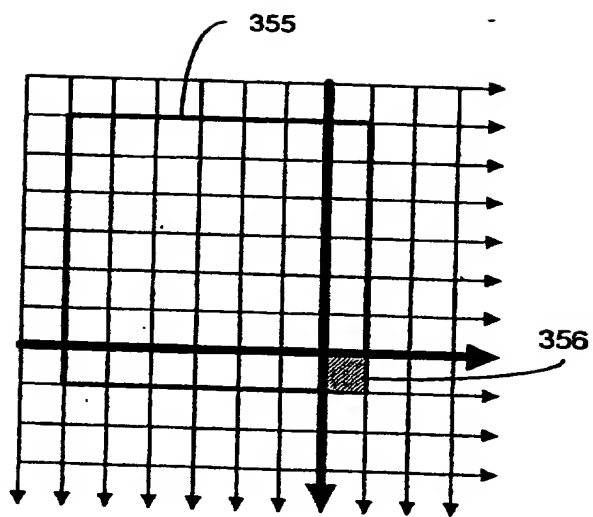


Fig. 11 (J)

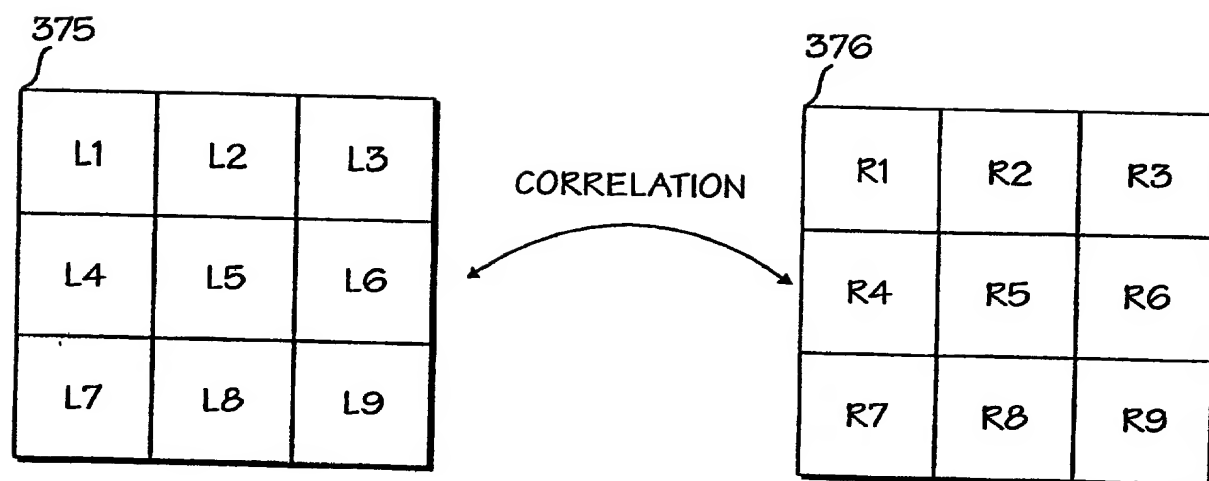


Fig. 12

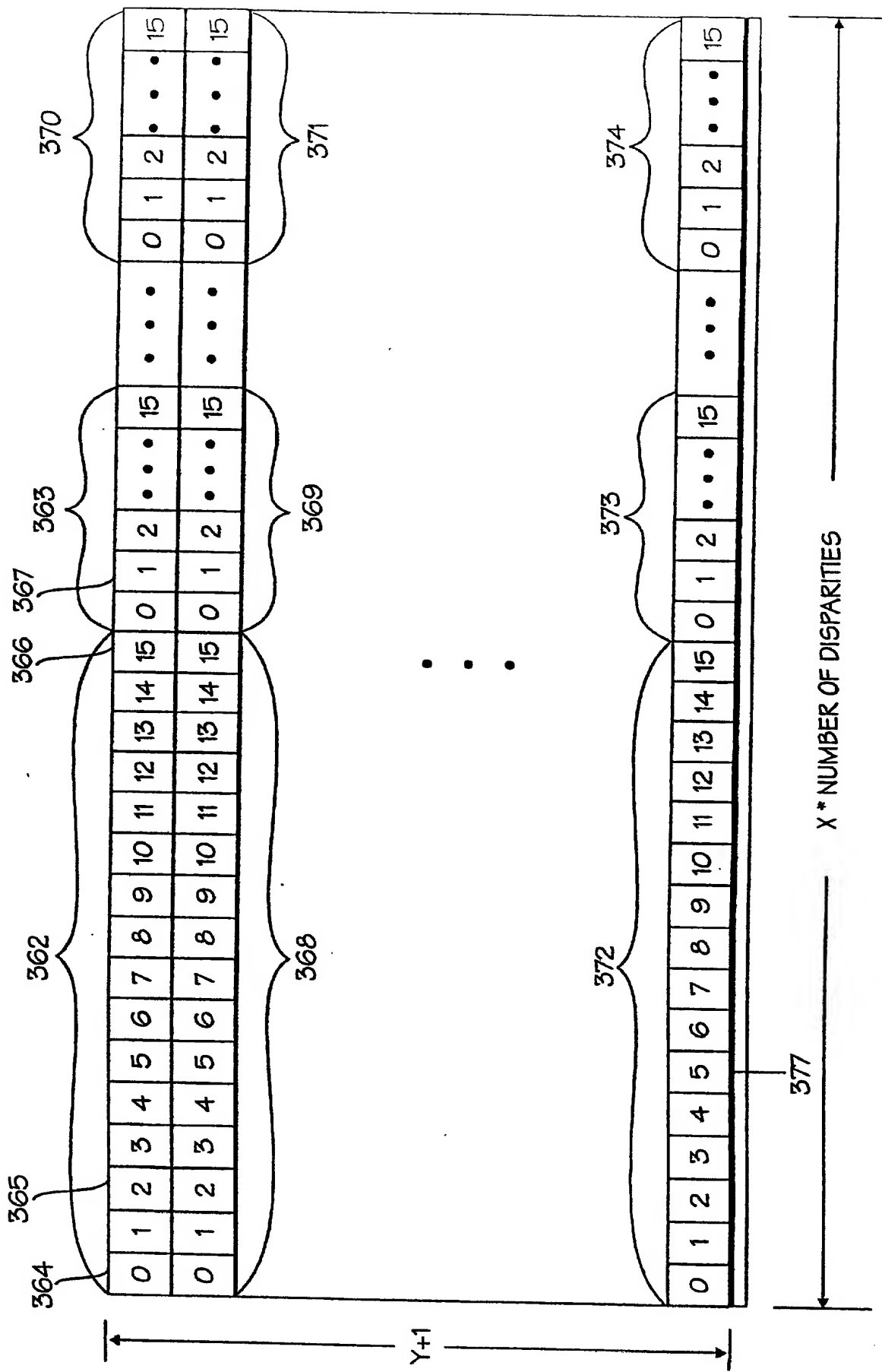
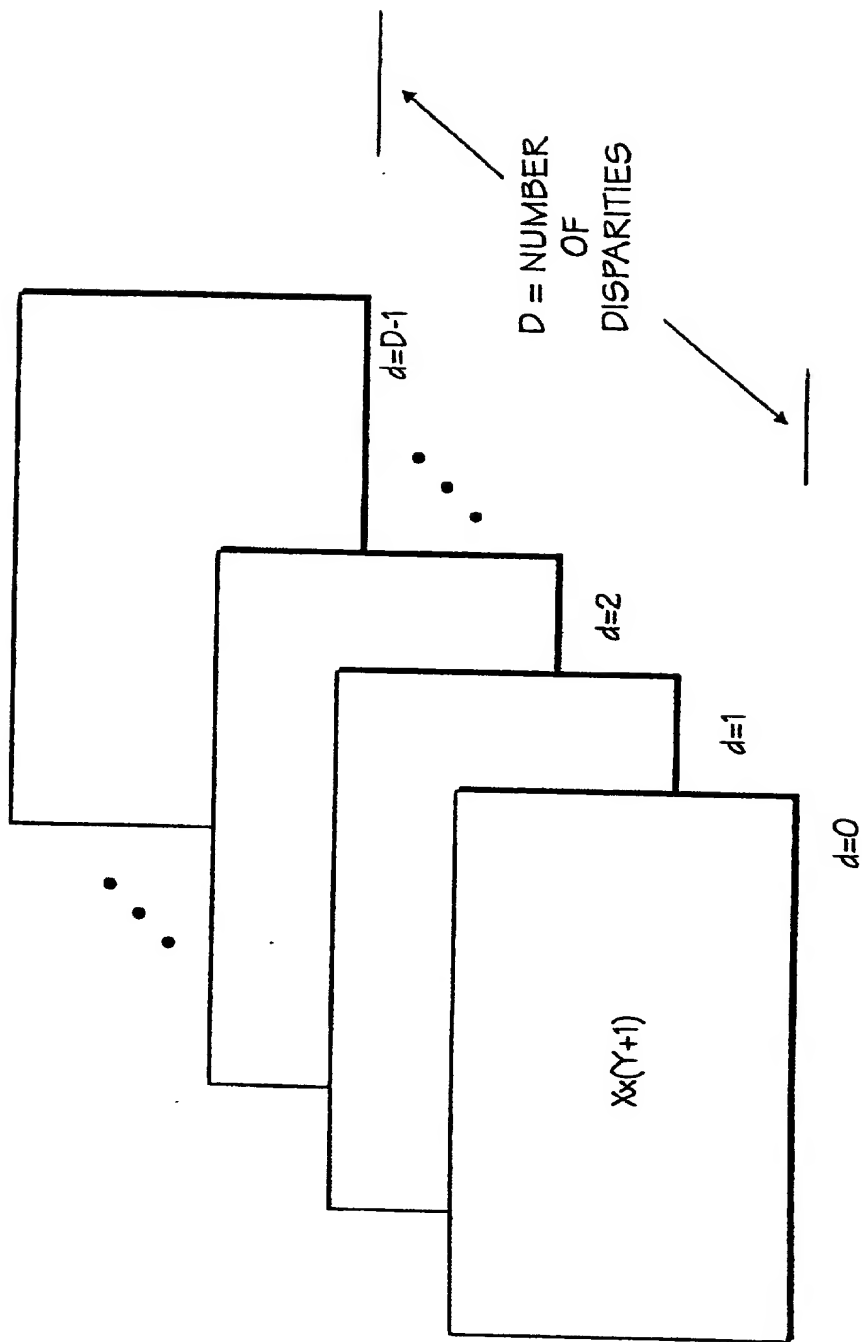


Fig. 13A



(B)

Fig. 13B





RIGHT IMAGE = REFERENCE  
LEFT IMAGE RIGHT IMAGE

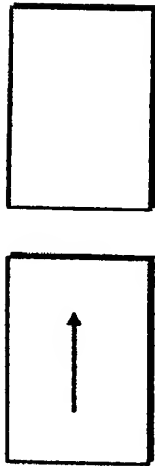
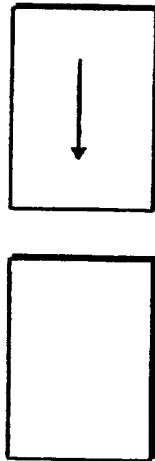


Fig. 16A

Fig. 16D

RIGHT IMAGE = REFERENCE  
LEFT IMAGE RIGHT IMAGE



CENSUS VECTORS  
LEFT IMAGE

A <sub>L</sub>	B <sub>L</sub>	C <sub>L</sub>	D <sub>L</sub>	E <sub>L</sub>	F <sub>L</sub>	G <sub>L</sub>	H <sub>L</sub>	I <sub>L</sub>	J <sub>L</sub>	...
										• • •

Fig. 16E

CENSUS VECTORS  
RIGHT IMAGE

$A_R$	$B_R$	$C_R$	$D_R$	$E_R$	$F_R$	$G_R$	$H_R$	$I_R$	$J_R$
• • •									

Fig. 16F



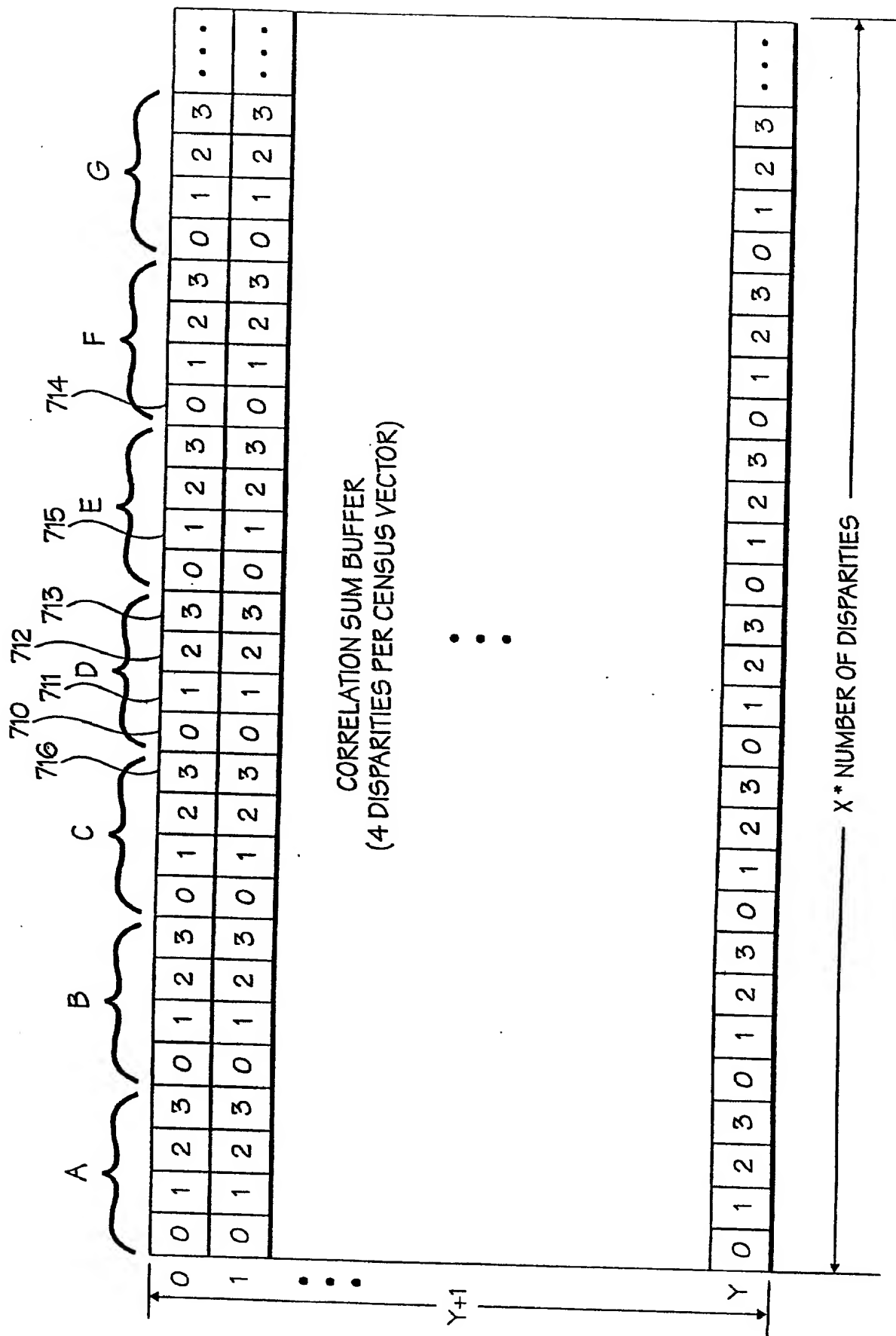


Fig. 16G

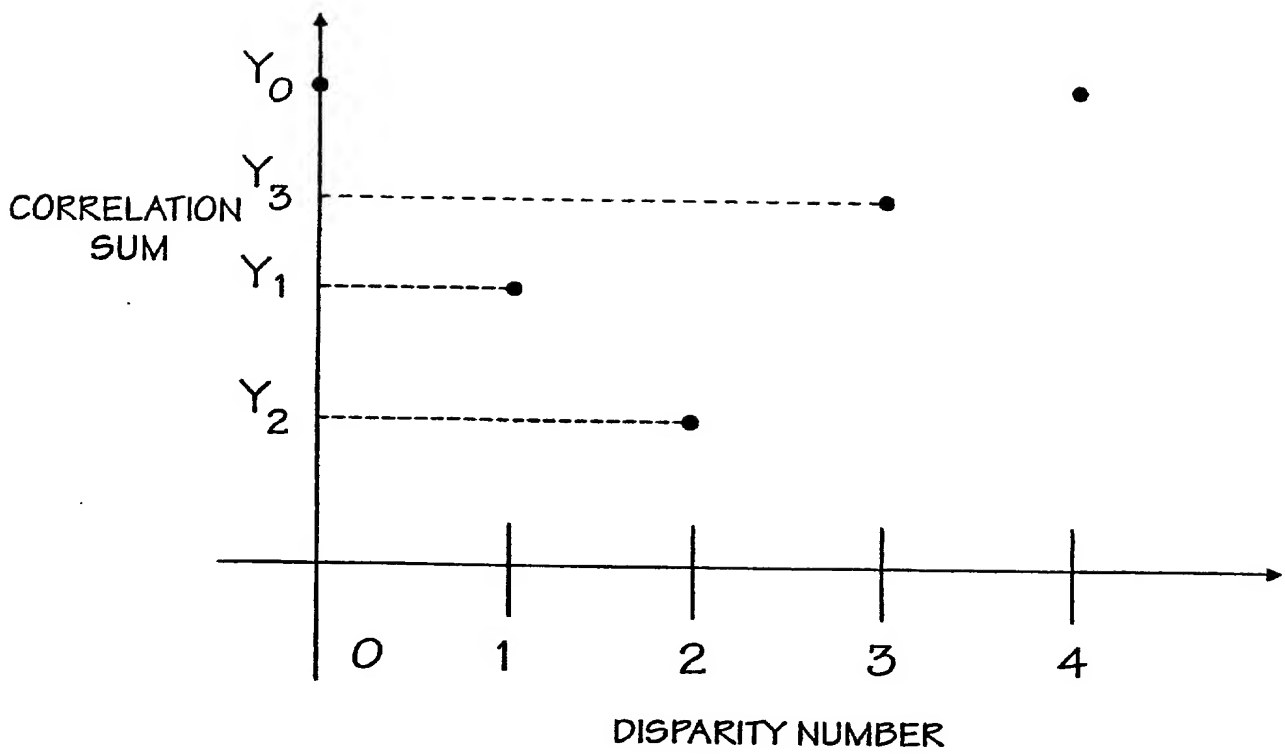


FIG. 17(A)

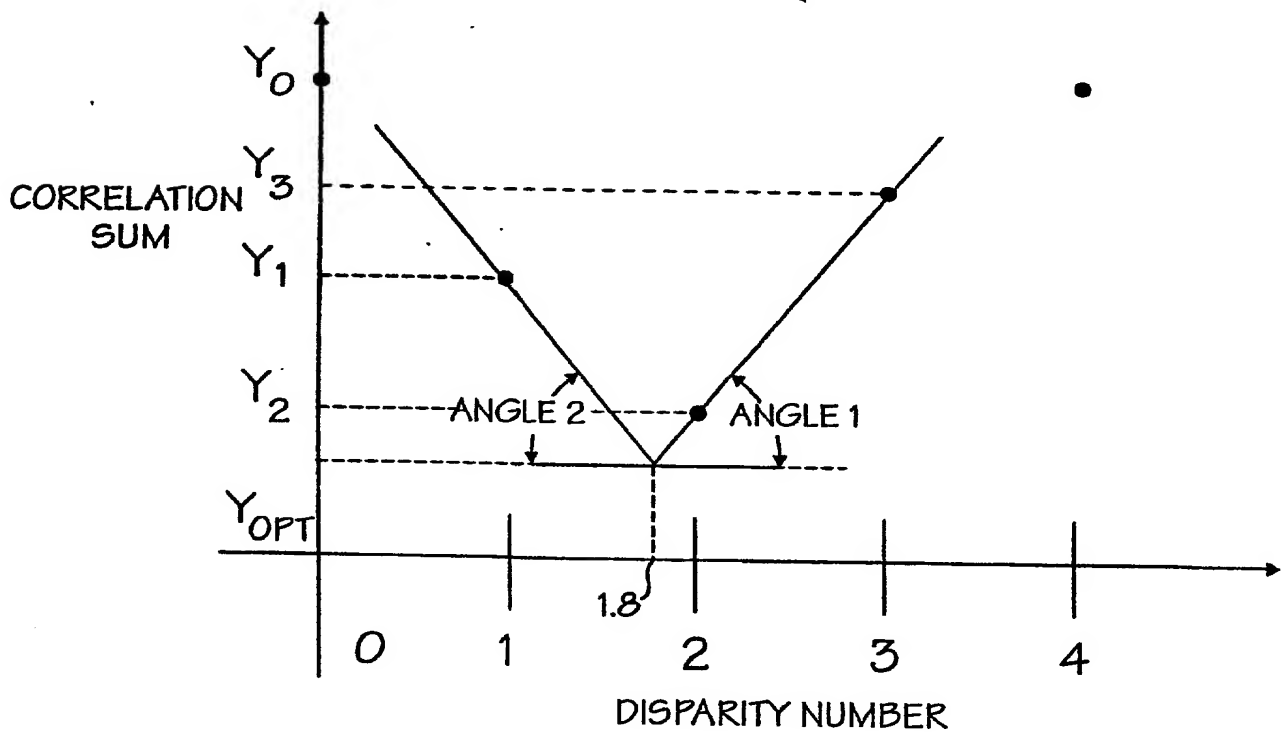


FIG. 17(B)

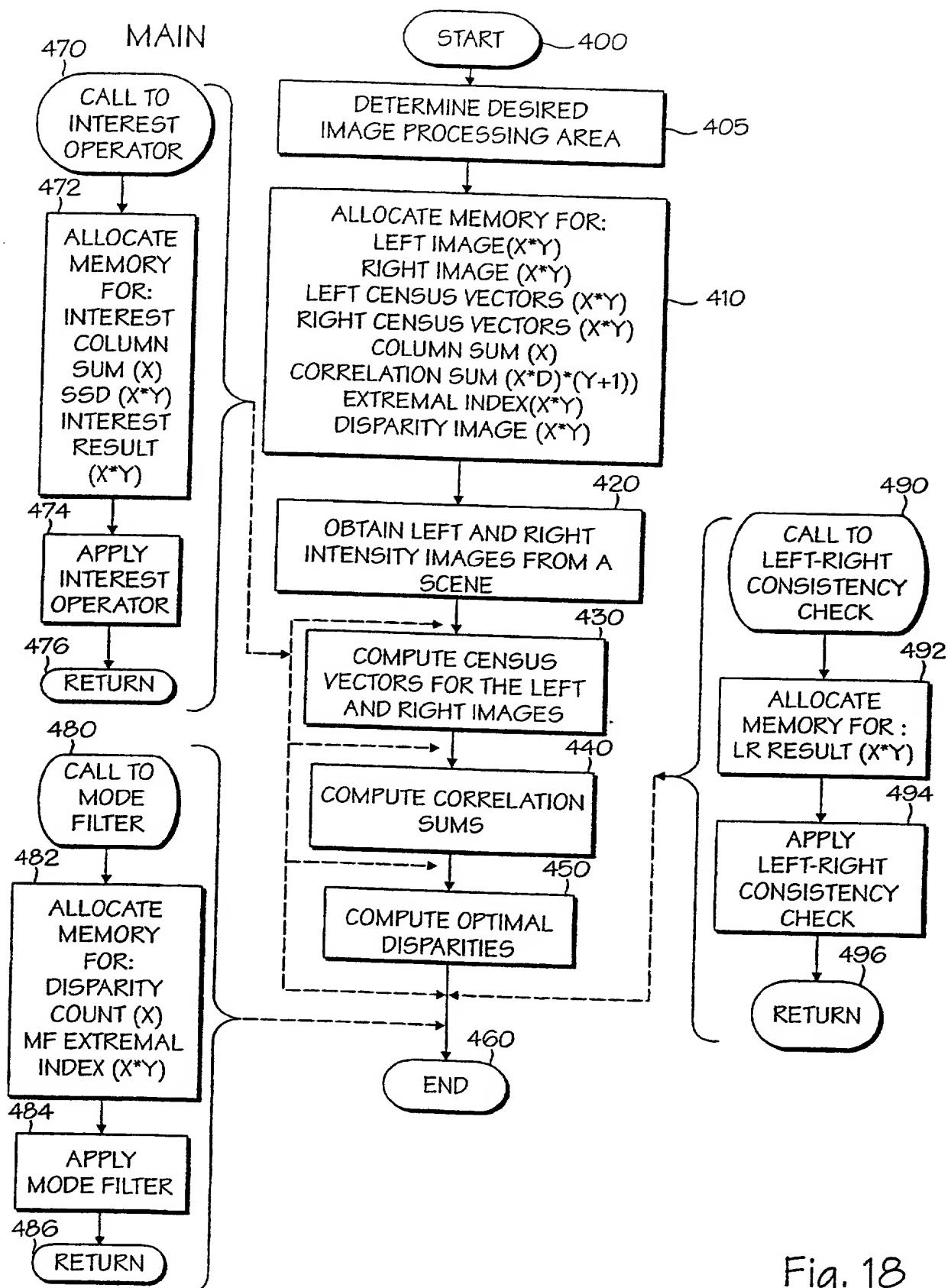


Fig. 18

# CENSUS TRANSFORM & CENSUS VECTORS

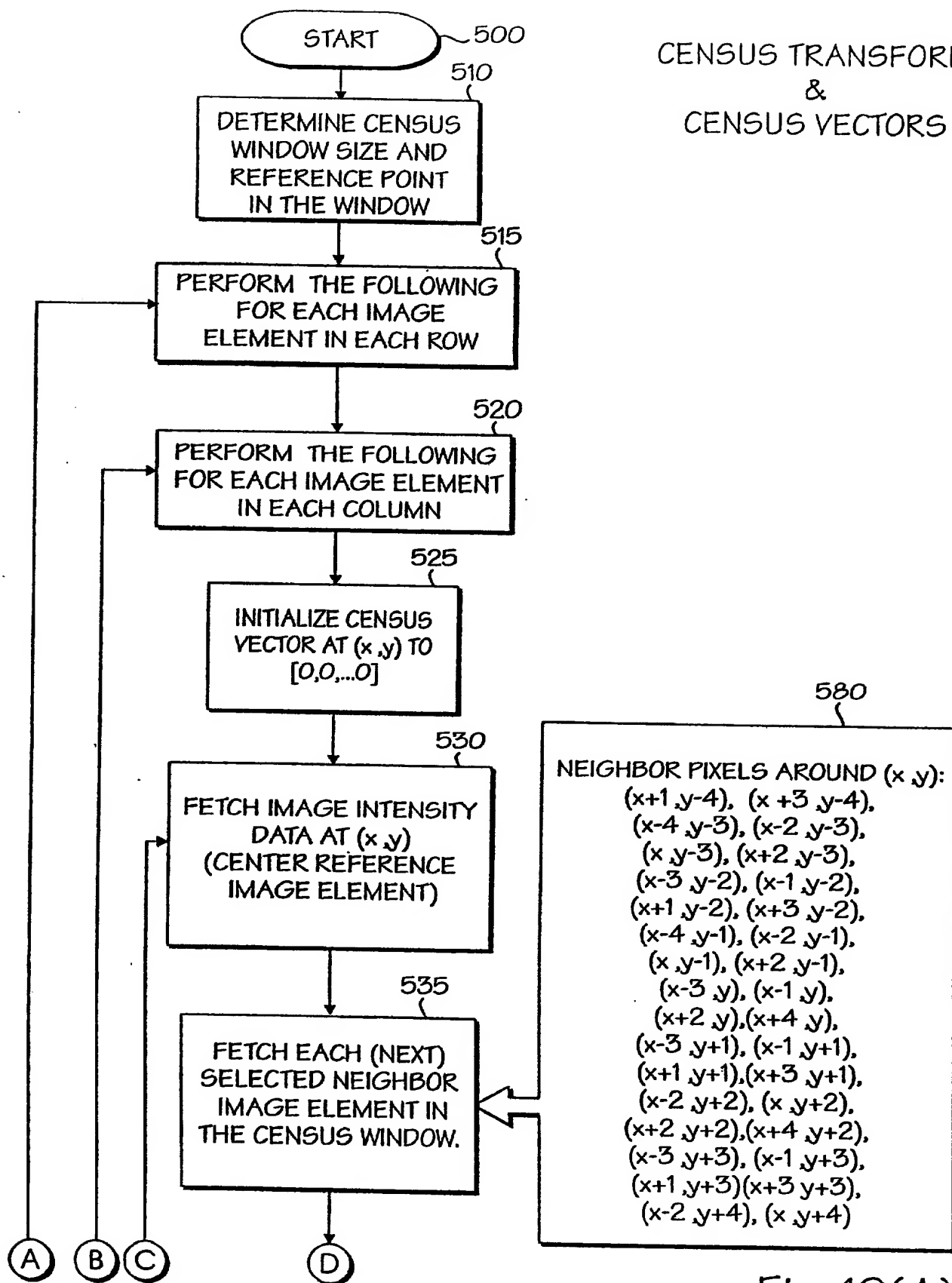


Fig. 19(A)

# CENSUS TRANSFORM & CENSUS VECTORS

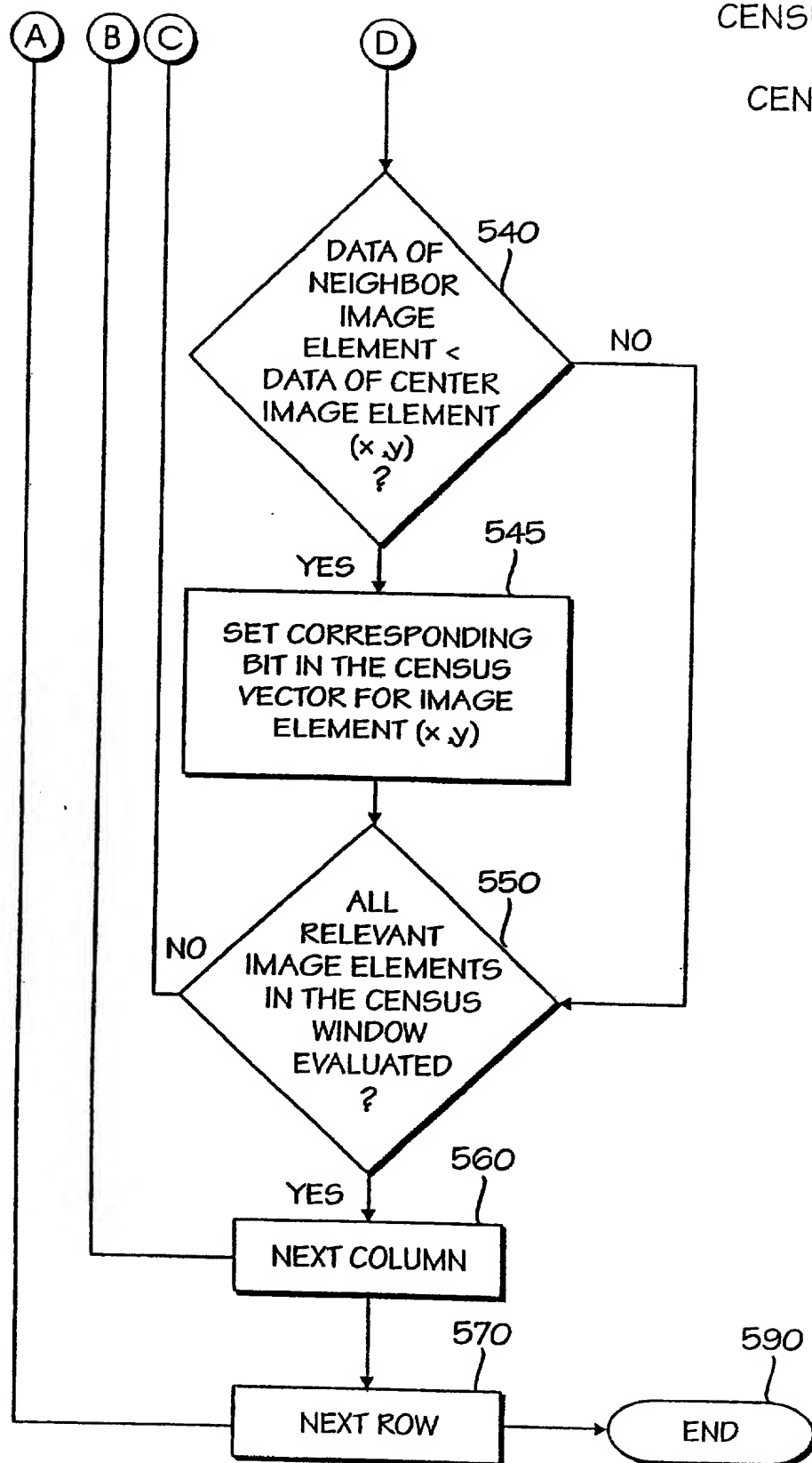


Fig. 19(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION

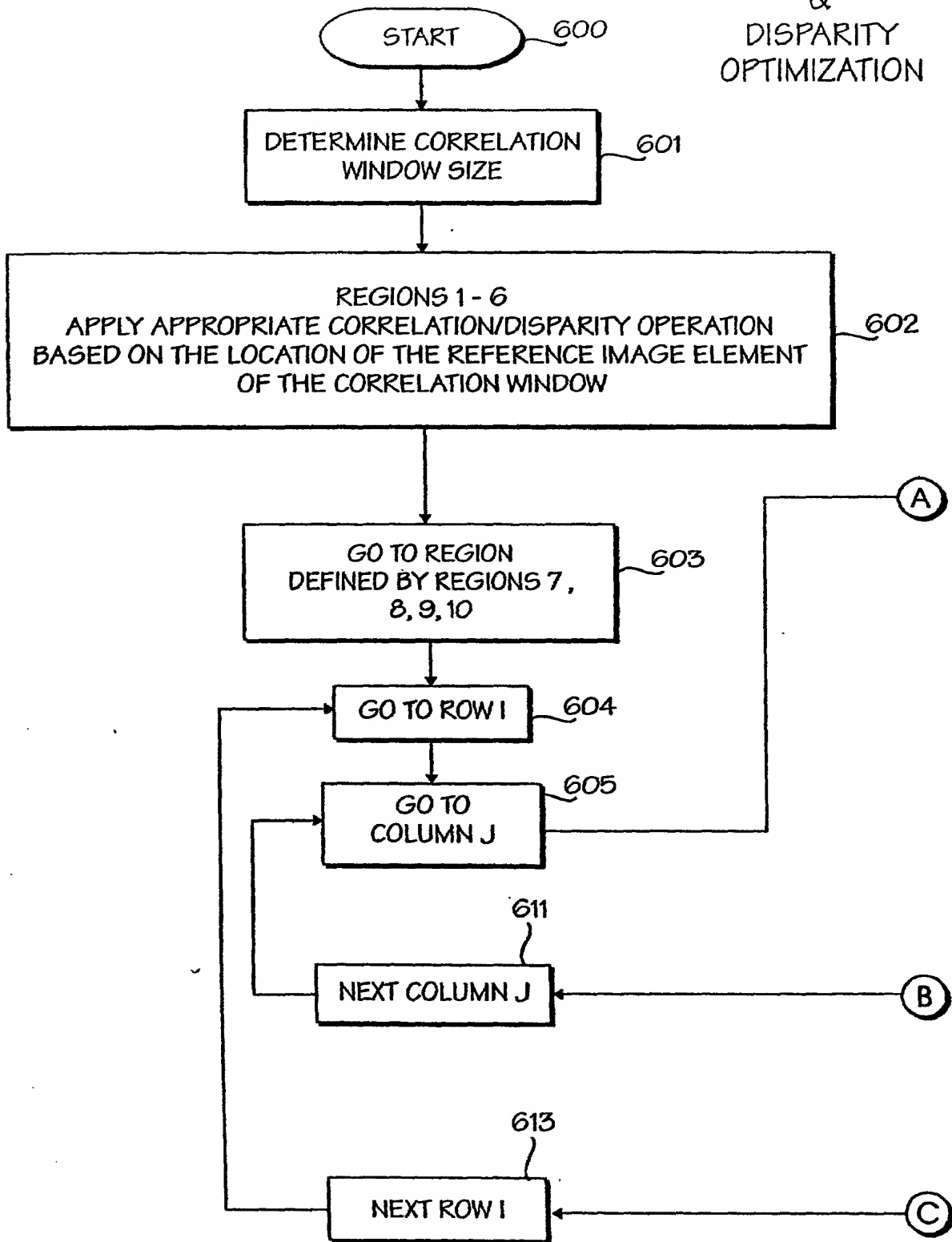


Fig. 20(A)

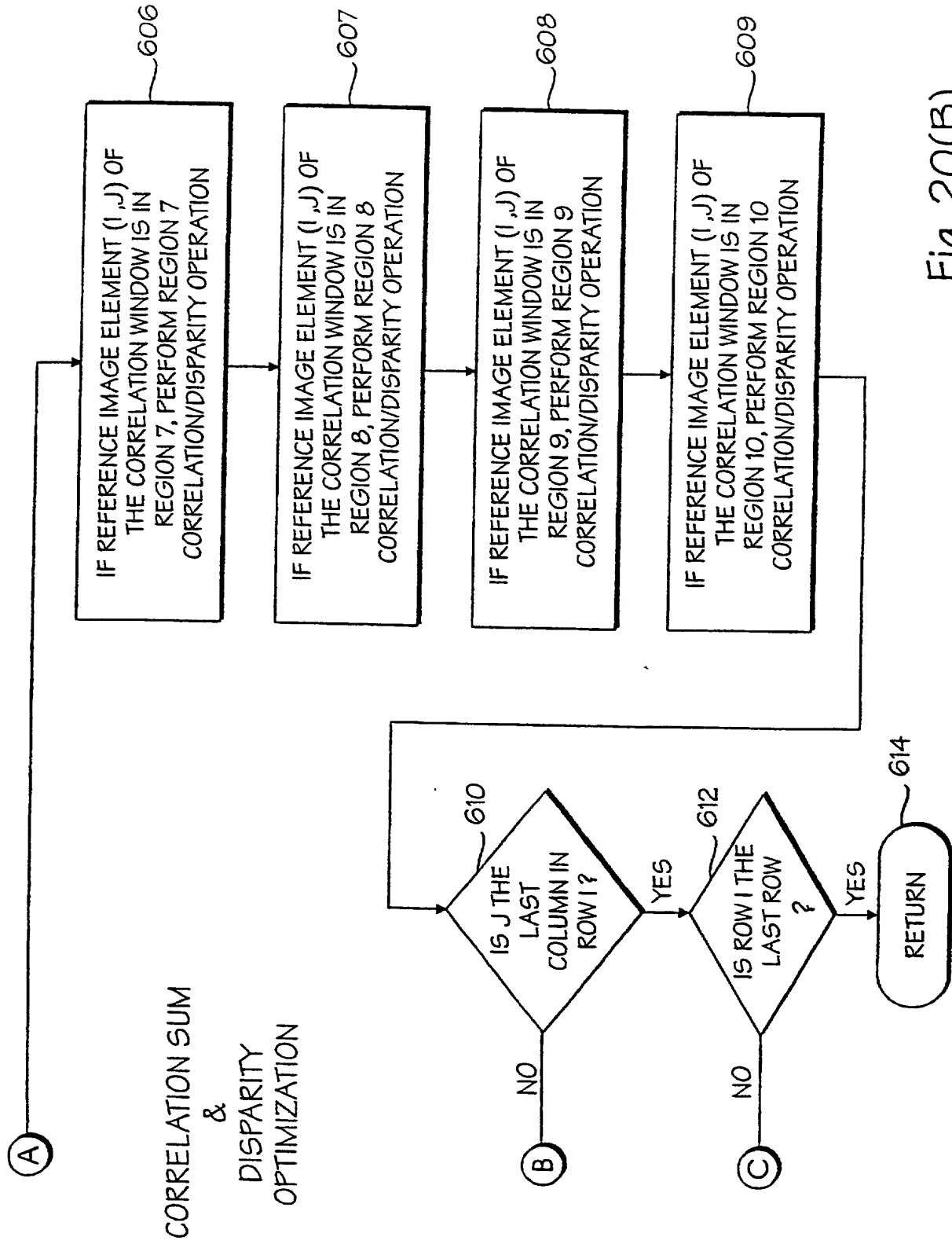


Fig. 20(B)

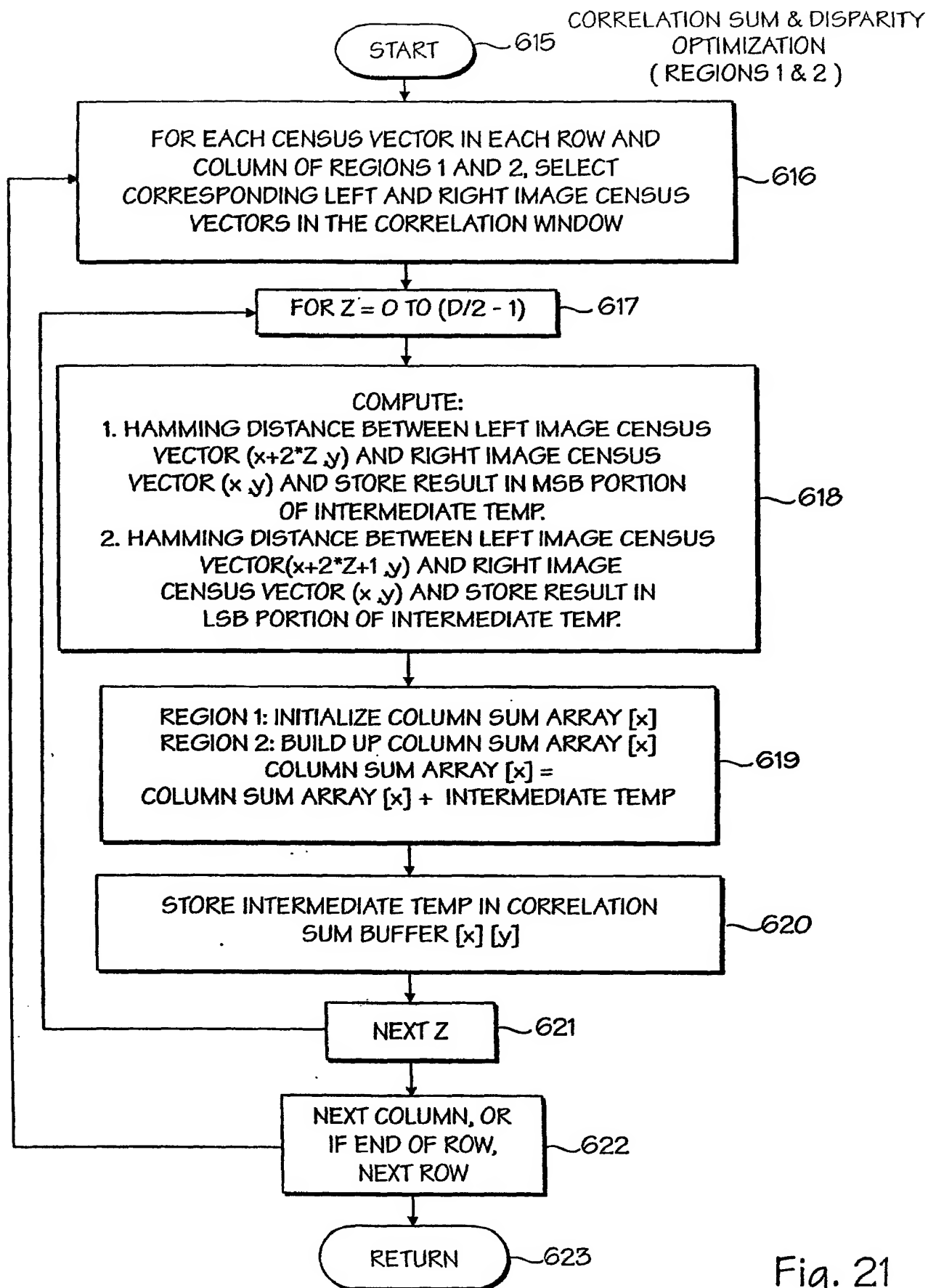


Fig. 21



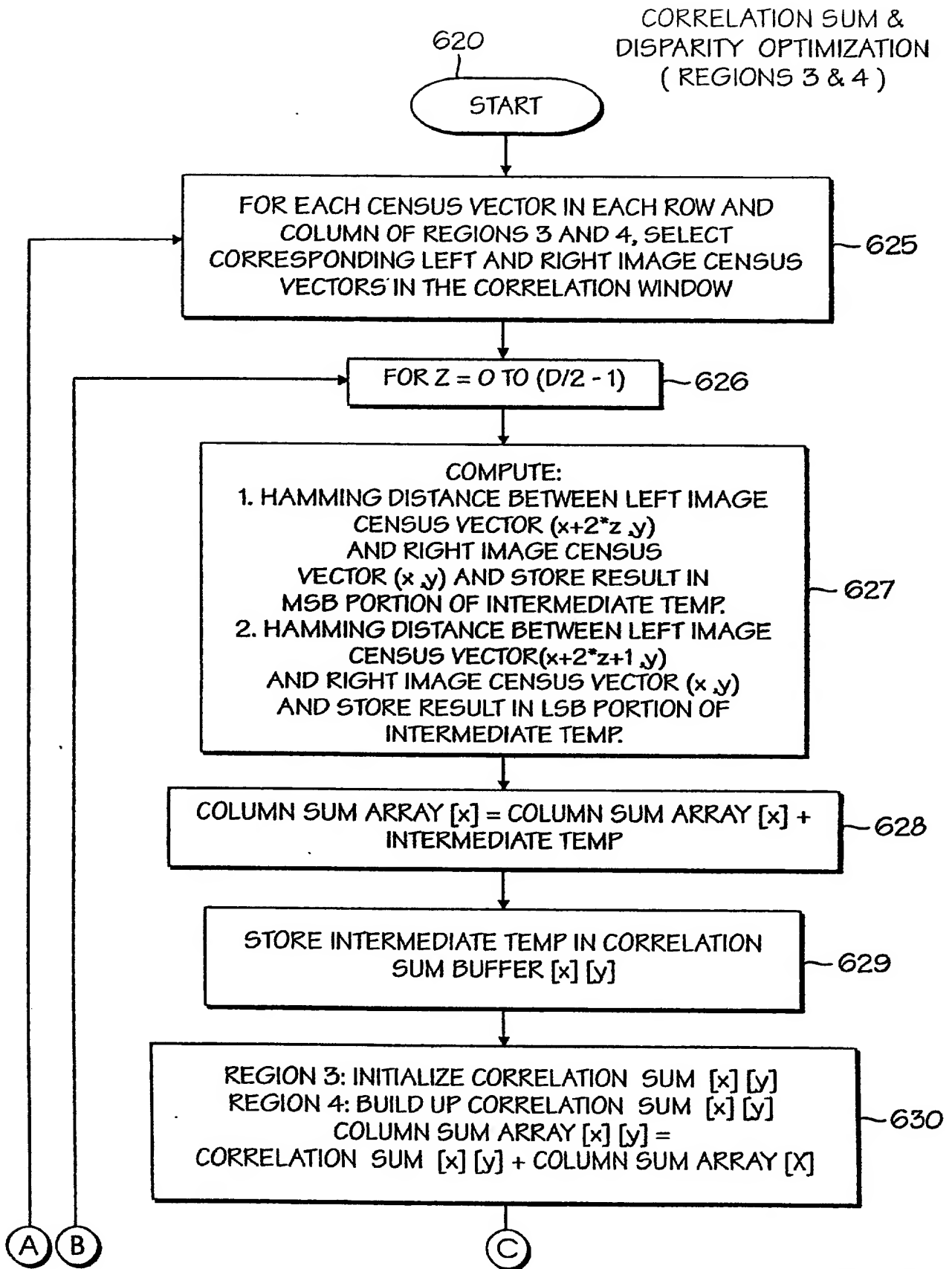


FIG. 22(A)

### CORRELATION SUM & DISPARITY OPTIMIZATION ( REGIONS 3 & 4 )

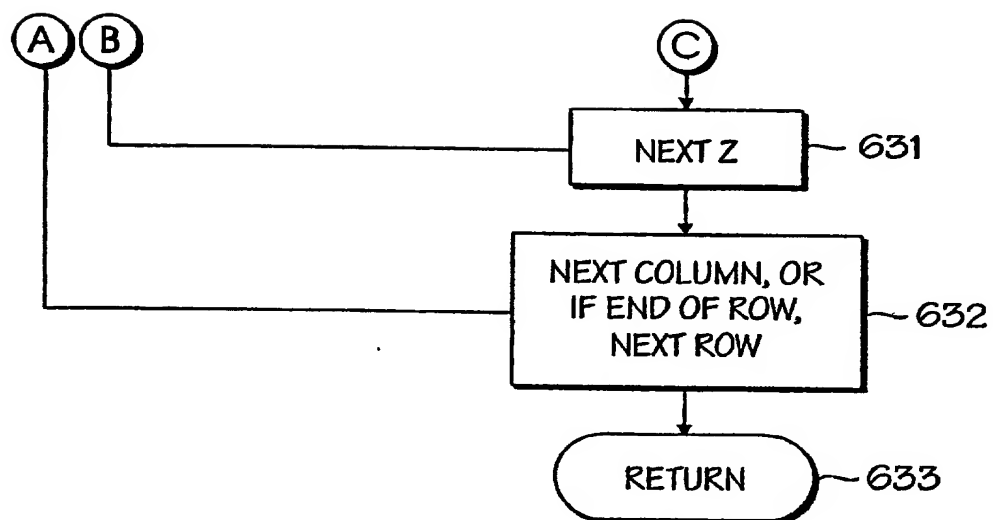


FIG. 22(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
( REGION 5 )

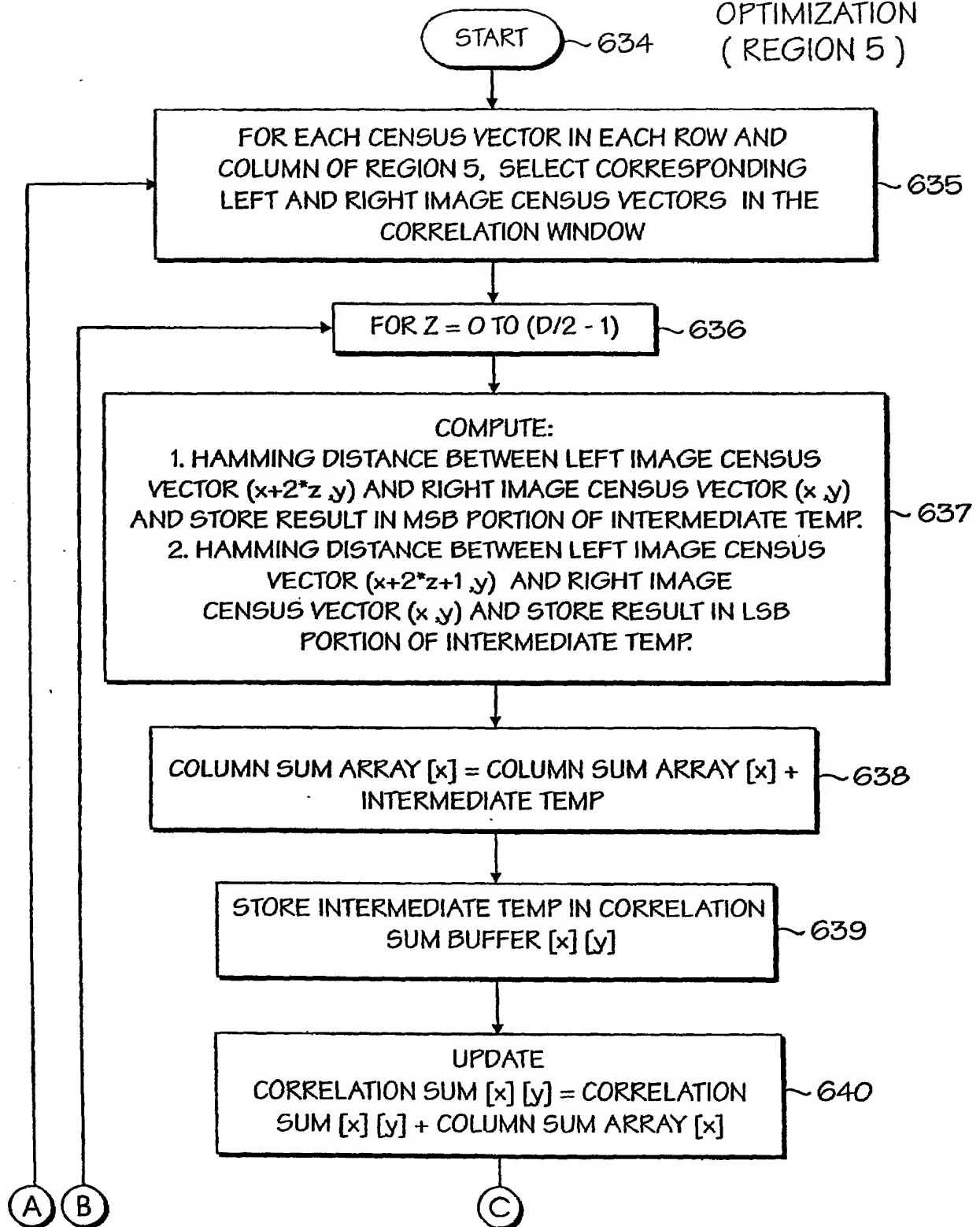


FIG. 23(A)

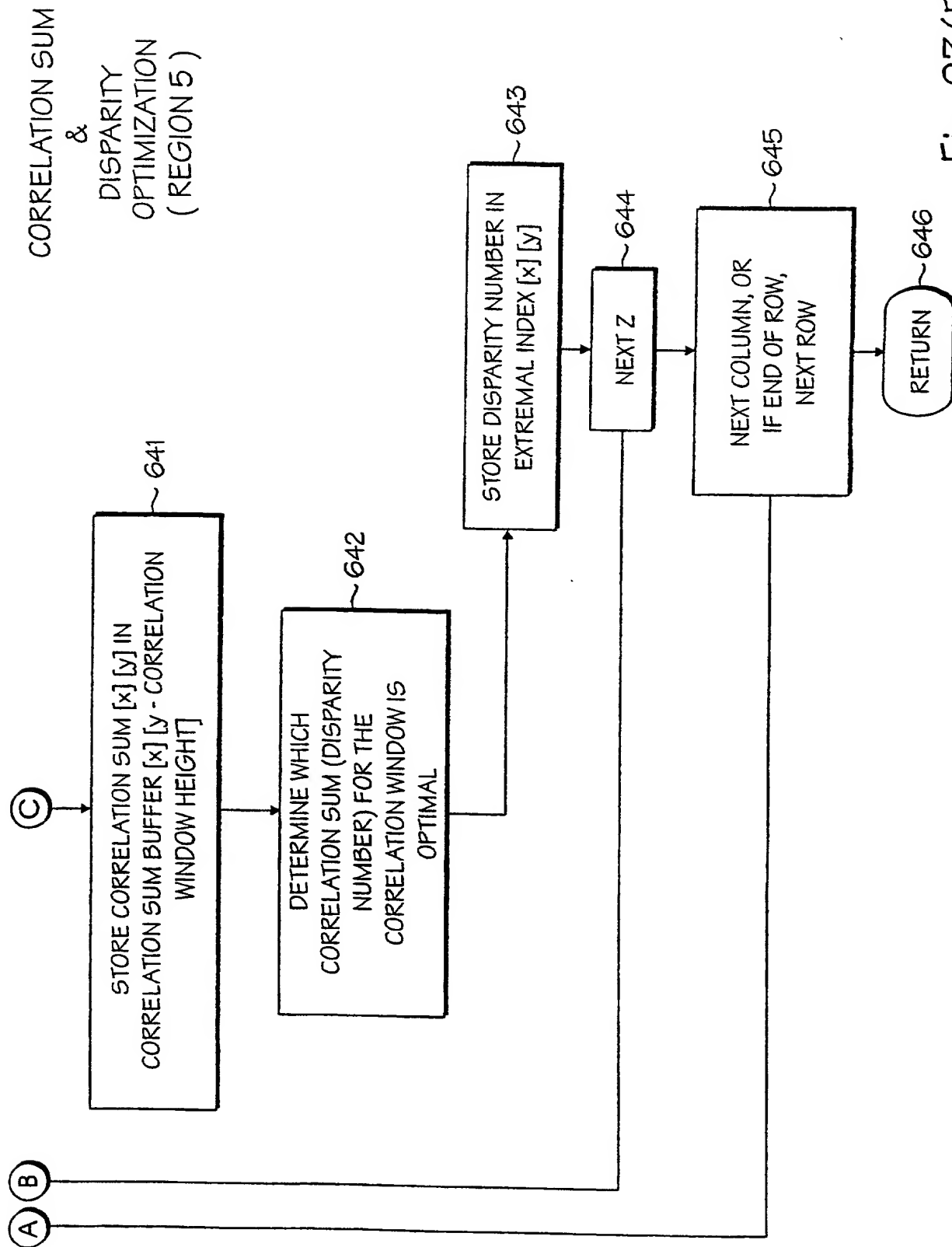


Fig. 23(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
( REGION 6 )

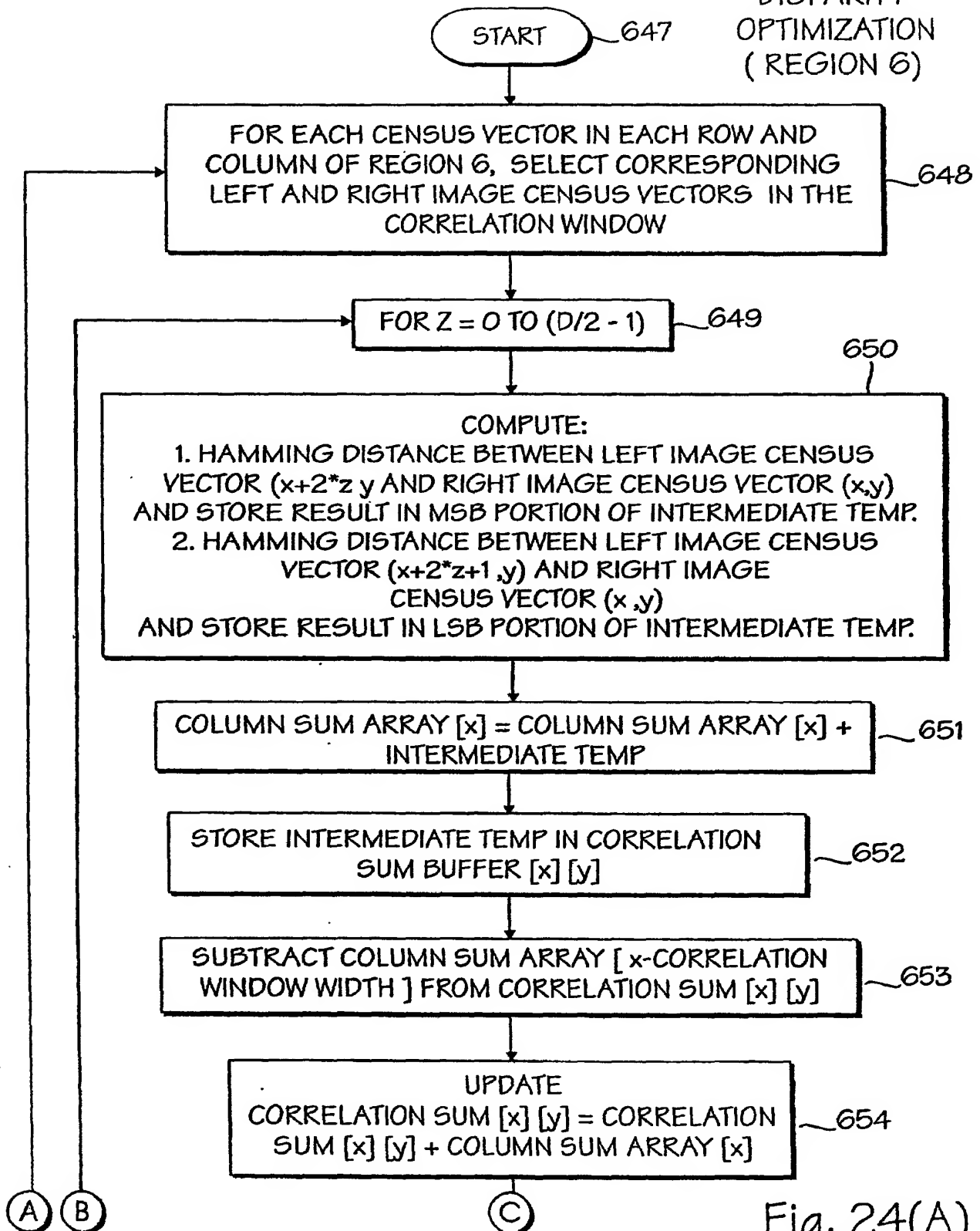


Fig. 24(A)

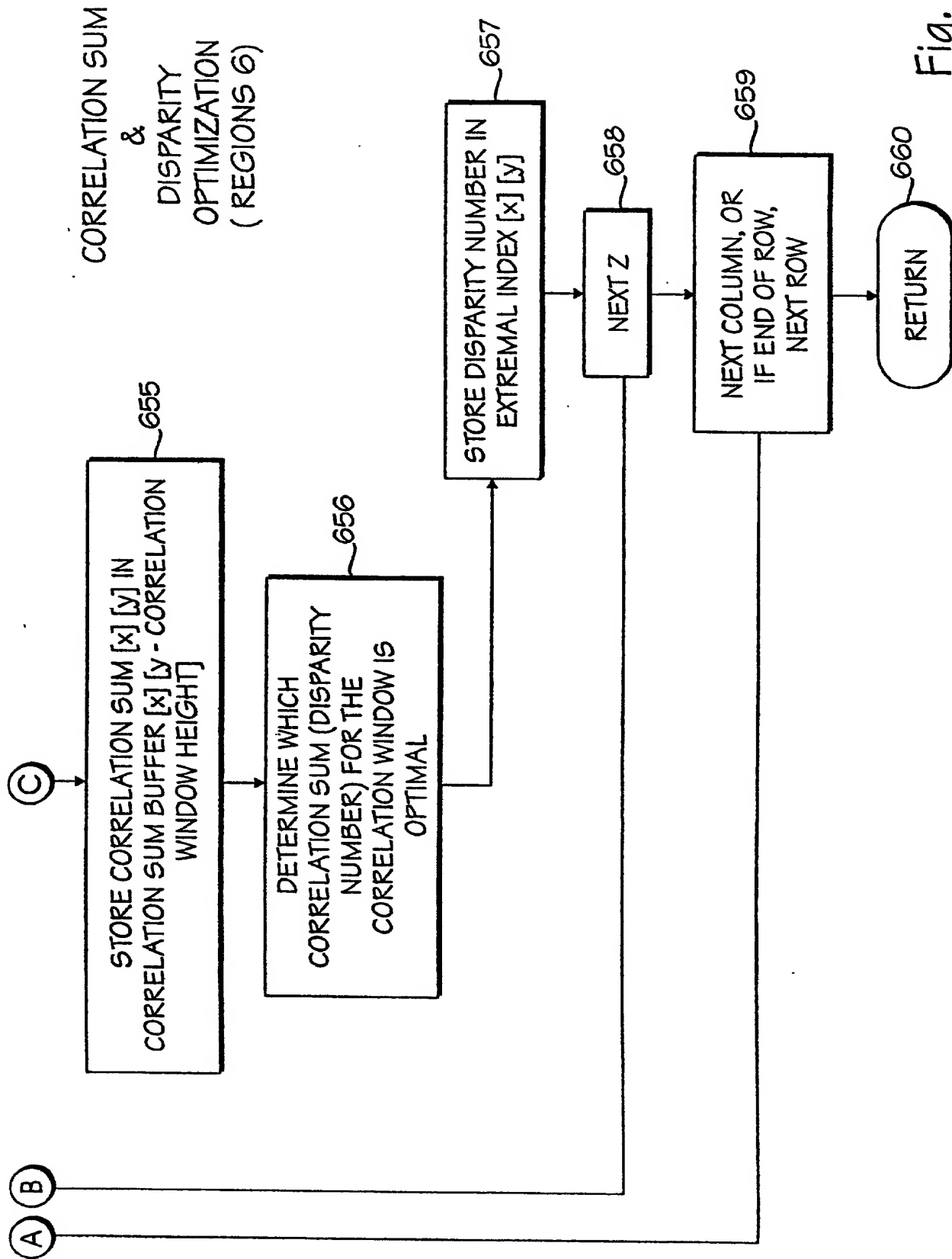


Fig. 24(B)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
(REGIONS 7 & 8)

START

661

FOR EACH CENSUS VECTOR IN EACH ROW AND  
COLUMN OF REGIONS 7 AND 8, SELECT  
CORRESPONDING LEFT AND RIGHT IMAGE CENSUS  
VECTORS IN THE CORRELATION WINDOW

662

FOR  $Z = 0$  TO  $(D/2 - 1)$

663

SUBTRACT TOP RIGHT CORRELATION SUM ELEMENT  
(CORRELATION SUM BUFFER  $[x]$   $[y$ -CORRELATION WINDOW  
HEIGHT]) FROM COLUMN SUM ARRAY  $[x]$

664

COMPUTE:  
1. HAMMING DISTANCE BETWEEN LEFT IMAGE CENSUS  
VECTOR  $(x+2*z, y)$  AND RIGHT IMAGE CENSUS  
VECTOR  $(x, y)$  AND STORE RESULT IN MSB  
PORTION OF INTERMEDIATE TEMP.  
2. HAMMING DISTANCE BETWEEN LEFT IMAGE CENSUS  
VECTOR  $(x+2*z+1, y)$  AND RIGHT IMAGE CENSUS  
VECTOR  $(x, y)$  AND STORE RESULT IN  
LSB PORTION OF INTERMEDIATE TEMP.

665

COLUMN SUM ARRAY  $[x] =$  COLUMN SUM ARRAY  $[x] +$   
INTERMEDIATE TEMP

666

A B

C

Fig. 25(A)

CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
(REGIONS 7 & 8)

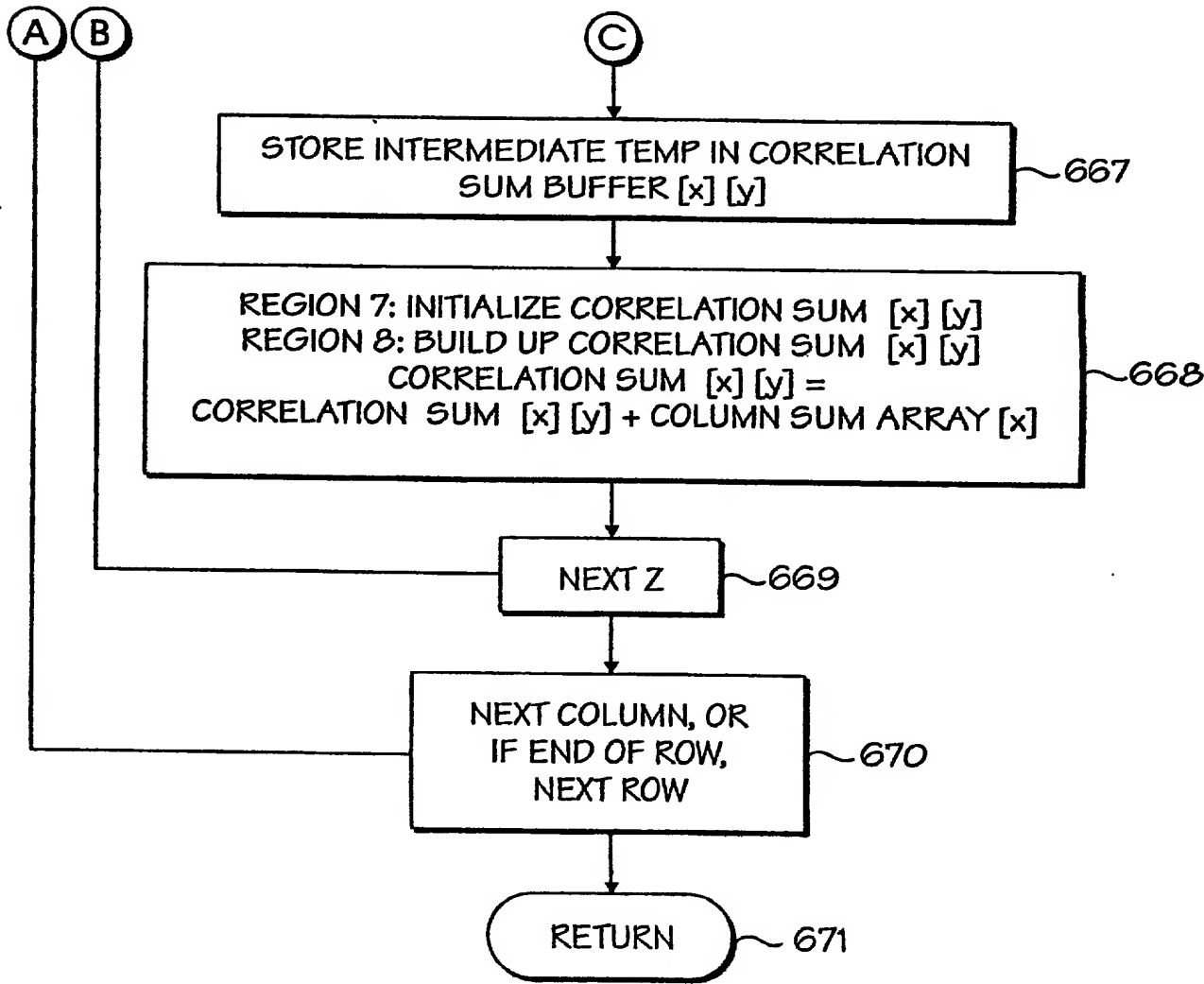


Fig. 25(B)



CORRELATION SUM  
&  
DISPARITY  
OPTIMIZATION  
(REGION 9)

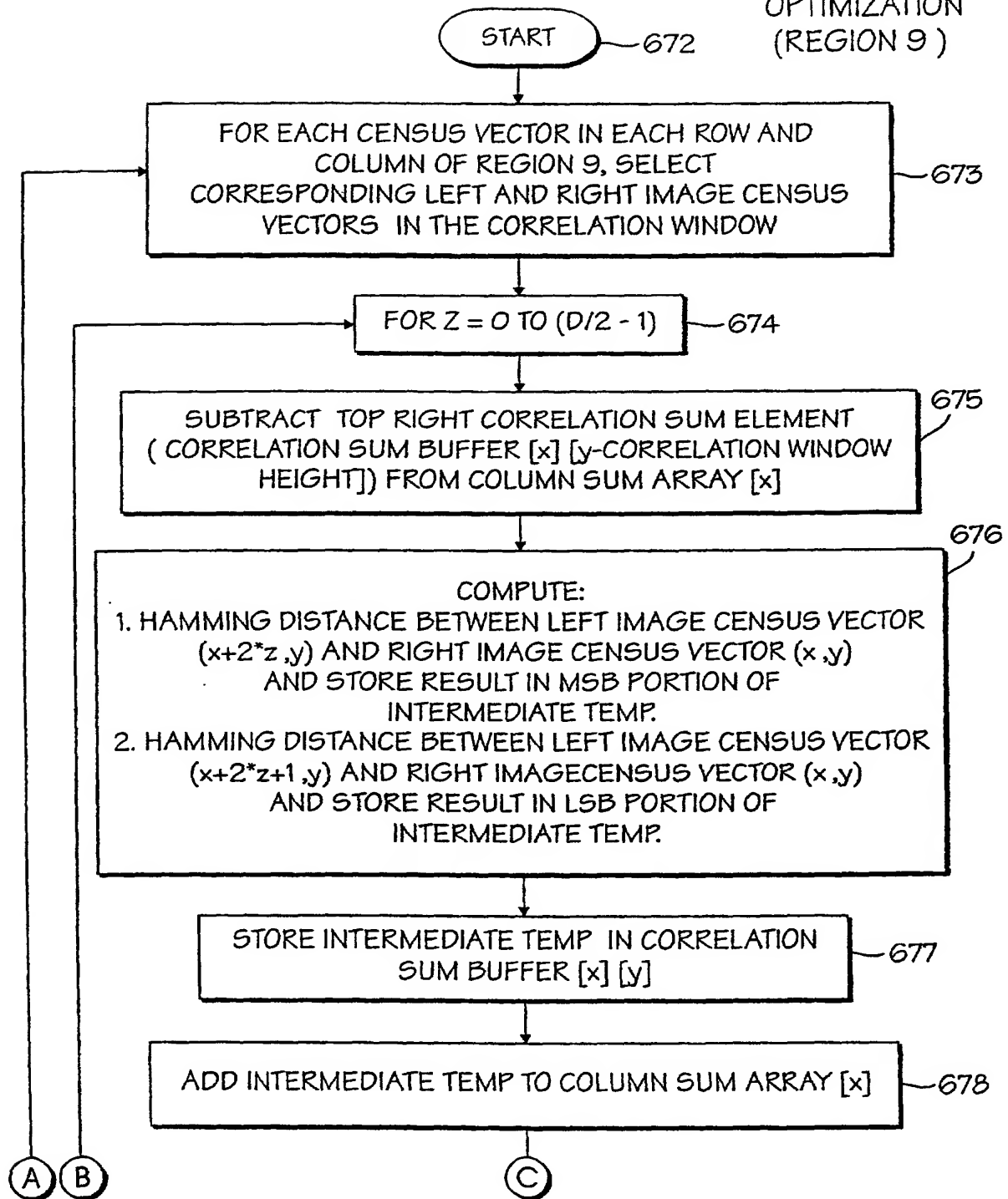


FIG. 26(A)

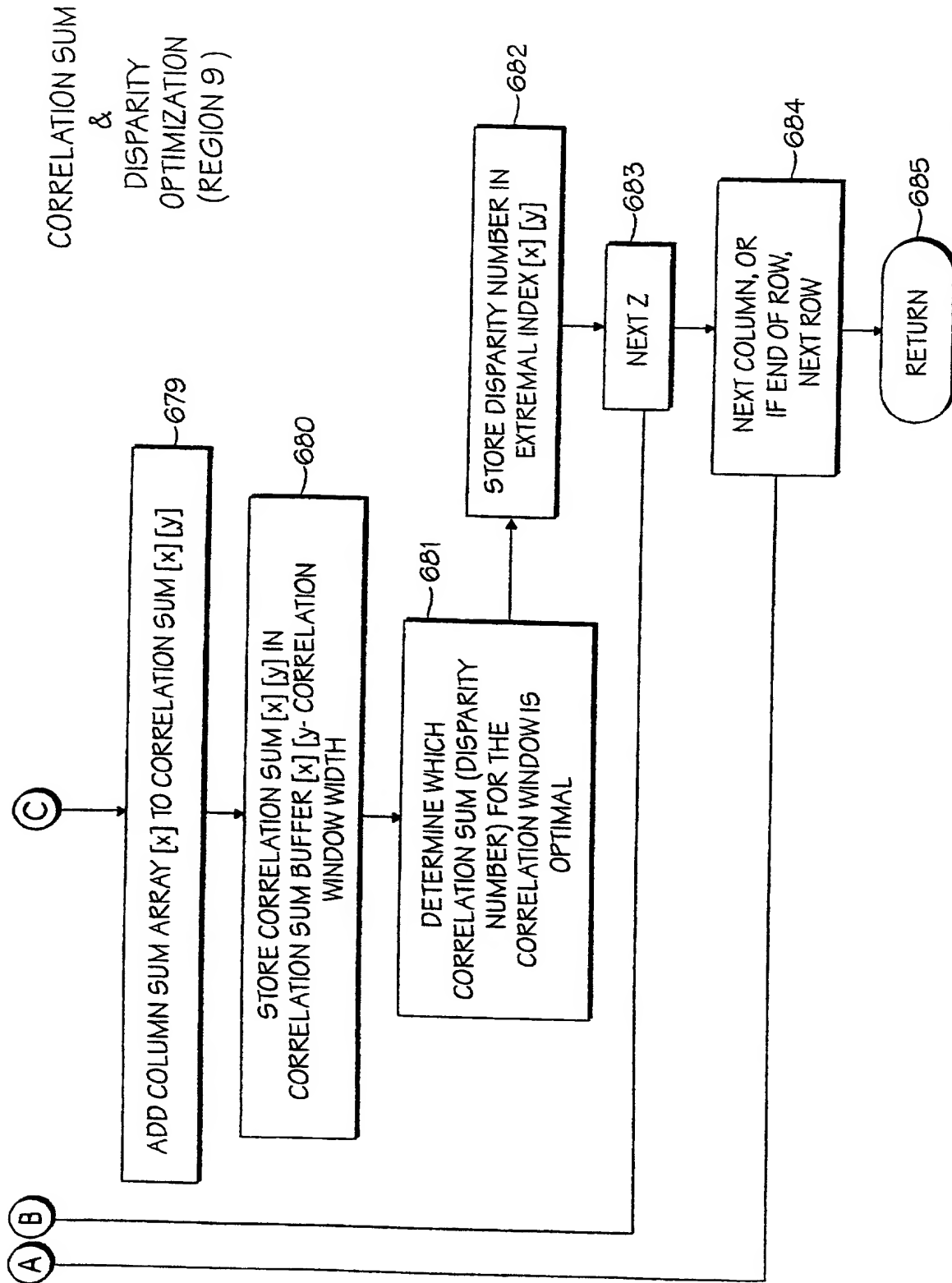
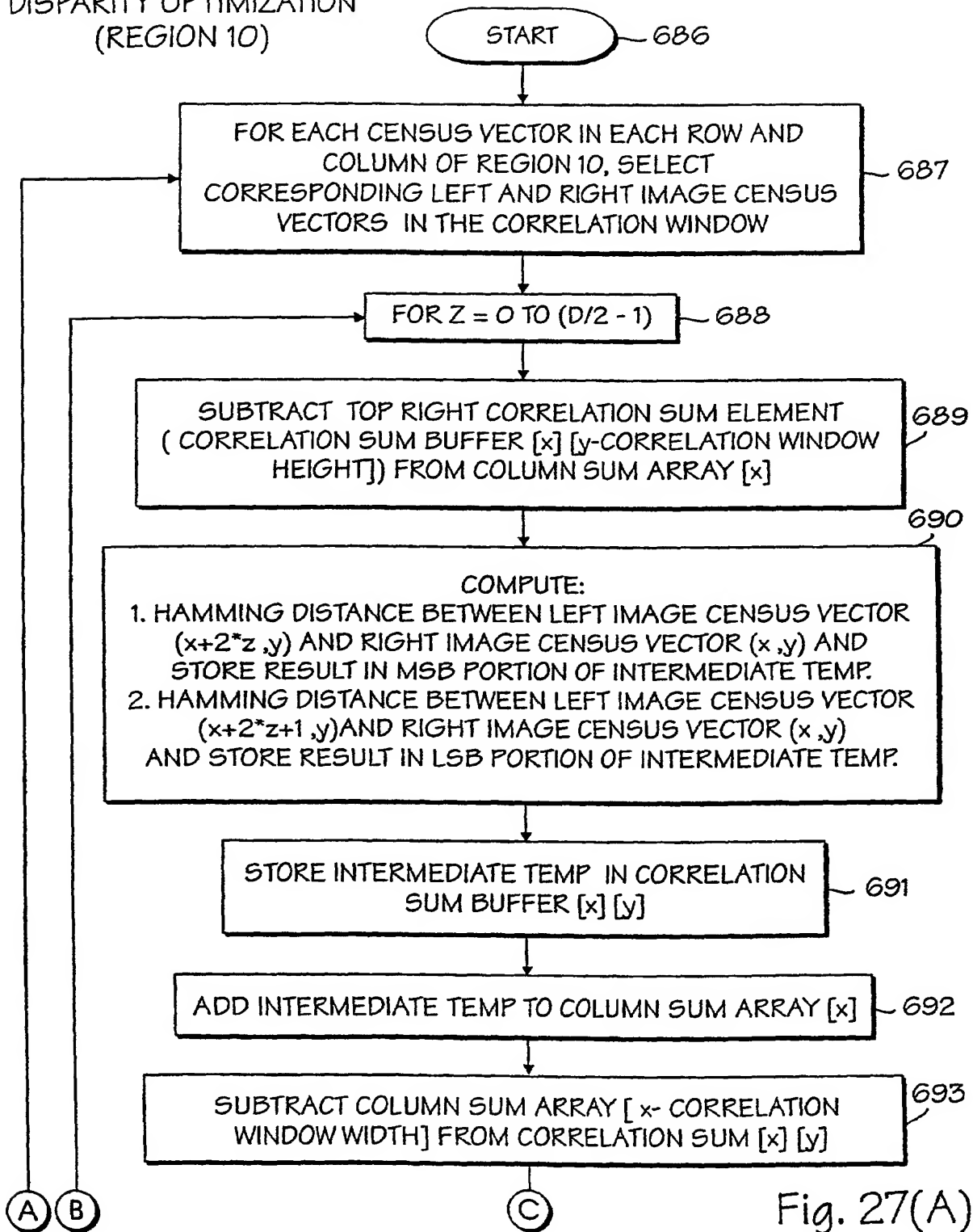


FIG. 26(B)

CORRELATION SUM  
&  
DISPARITY OPTIMIZATION  
(REGION 10)



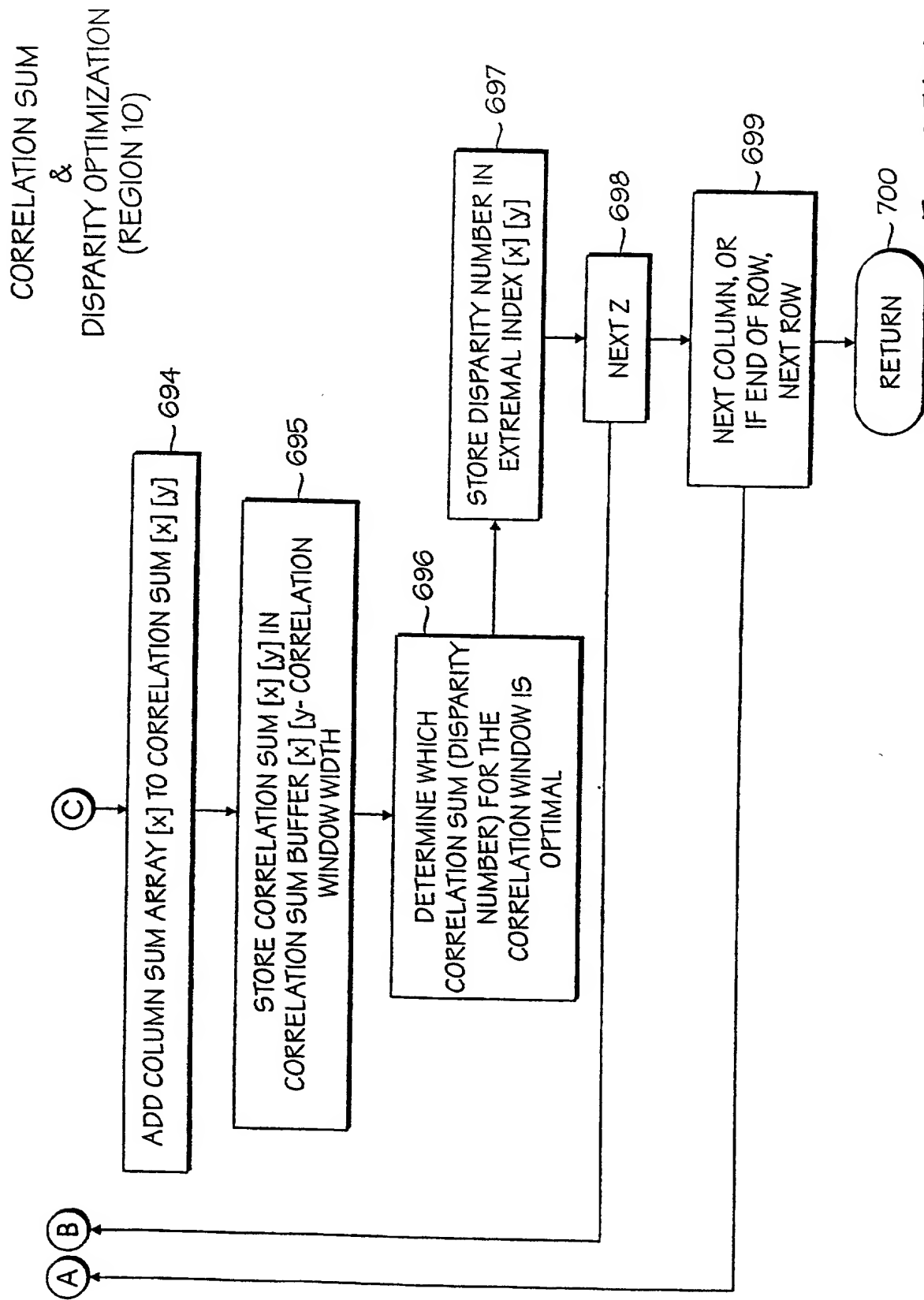


Fig. 27(B)

1002032.124404

INTEREST  
OPERATION

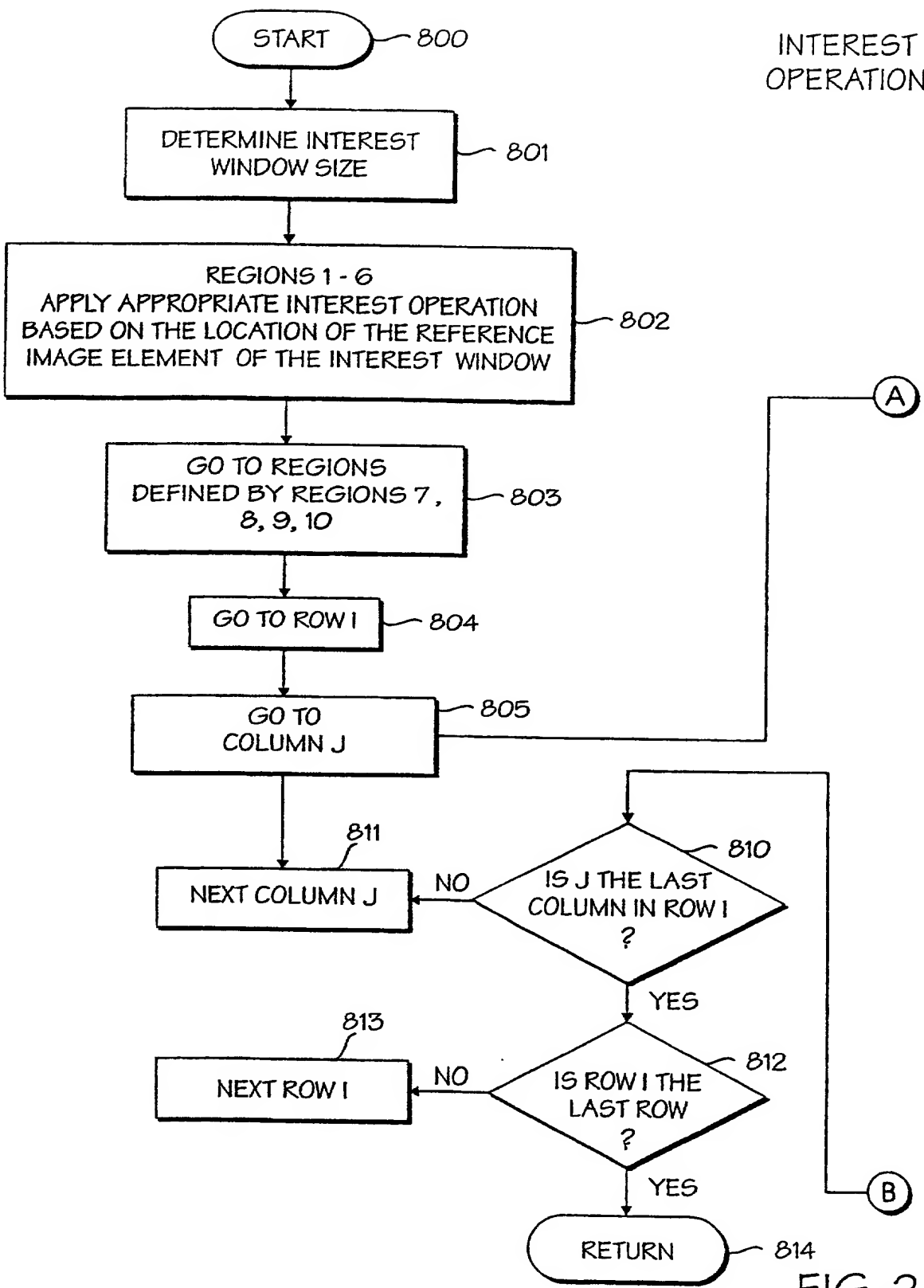


FIG. 28(A)

10020852-124401

# INTEREST OPERATION

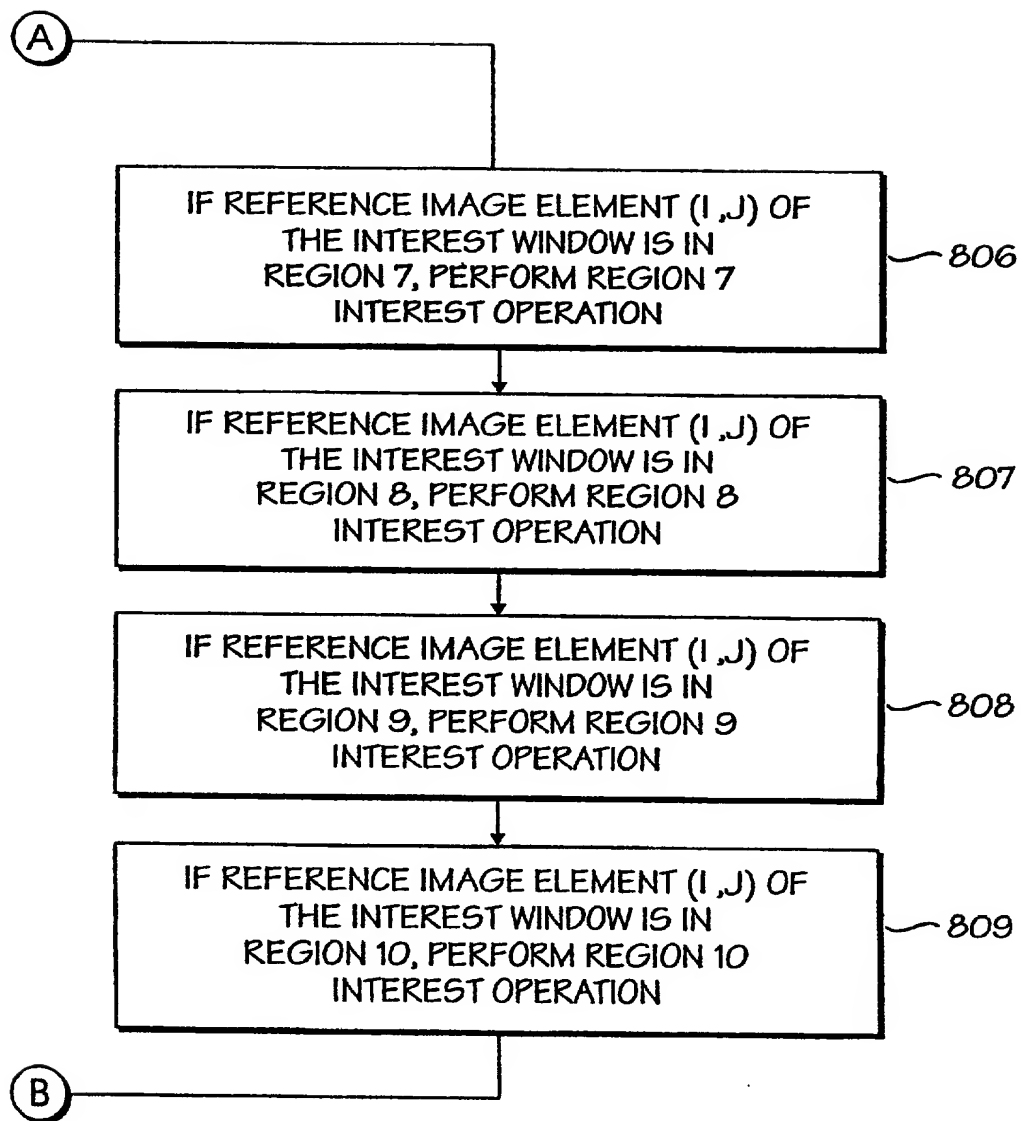


FIG. 28(B)

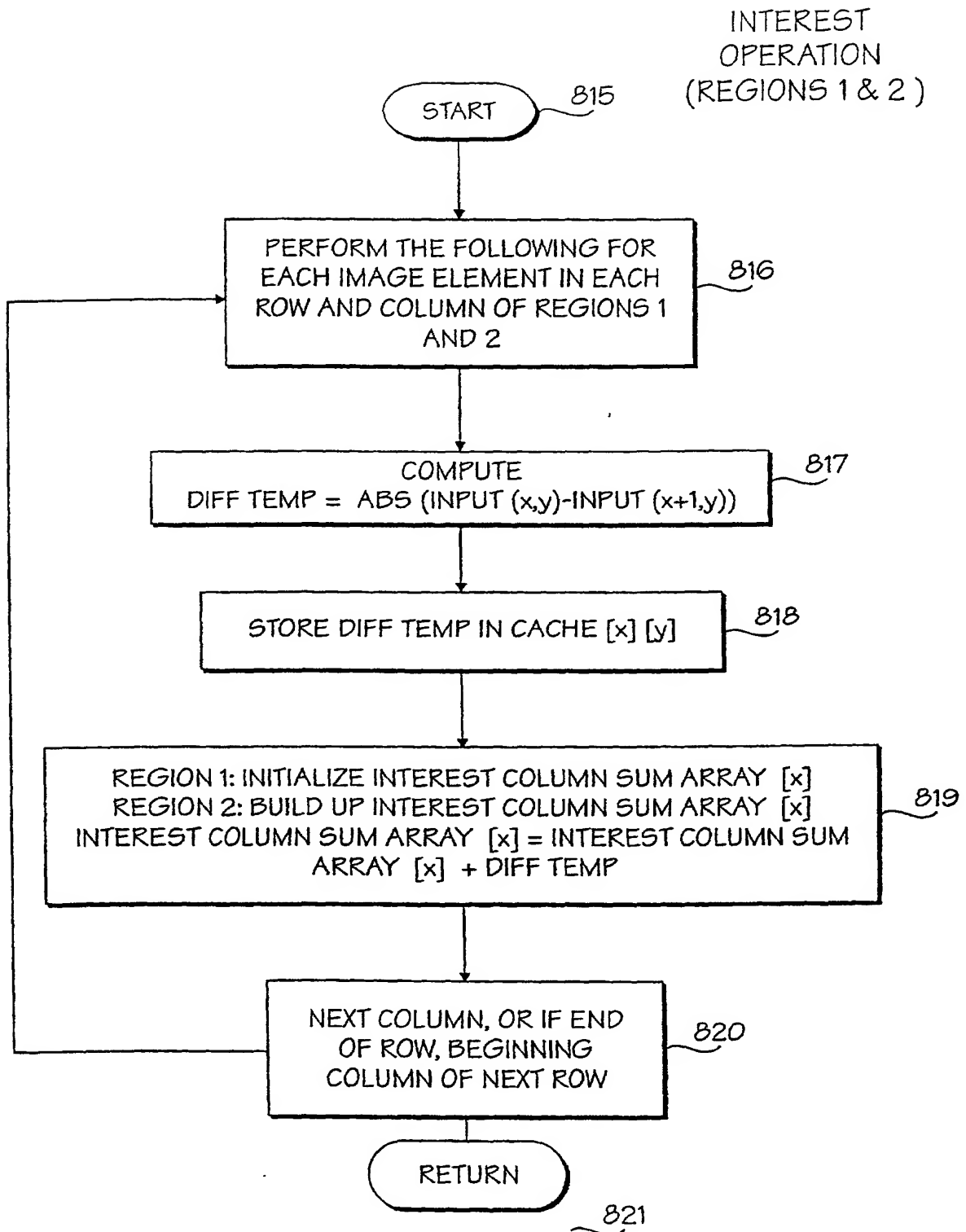


FIG. 29

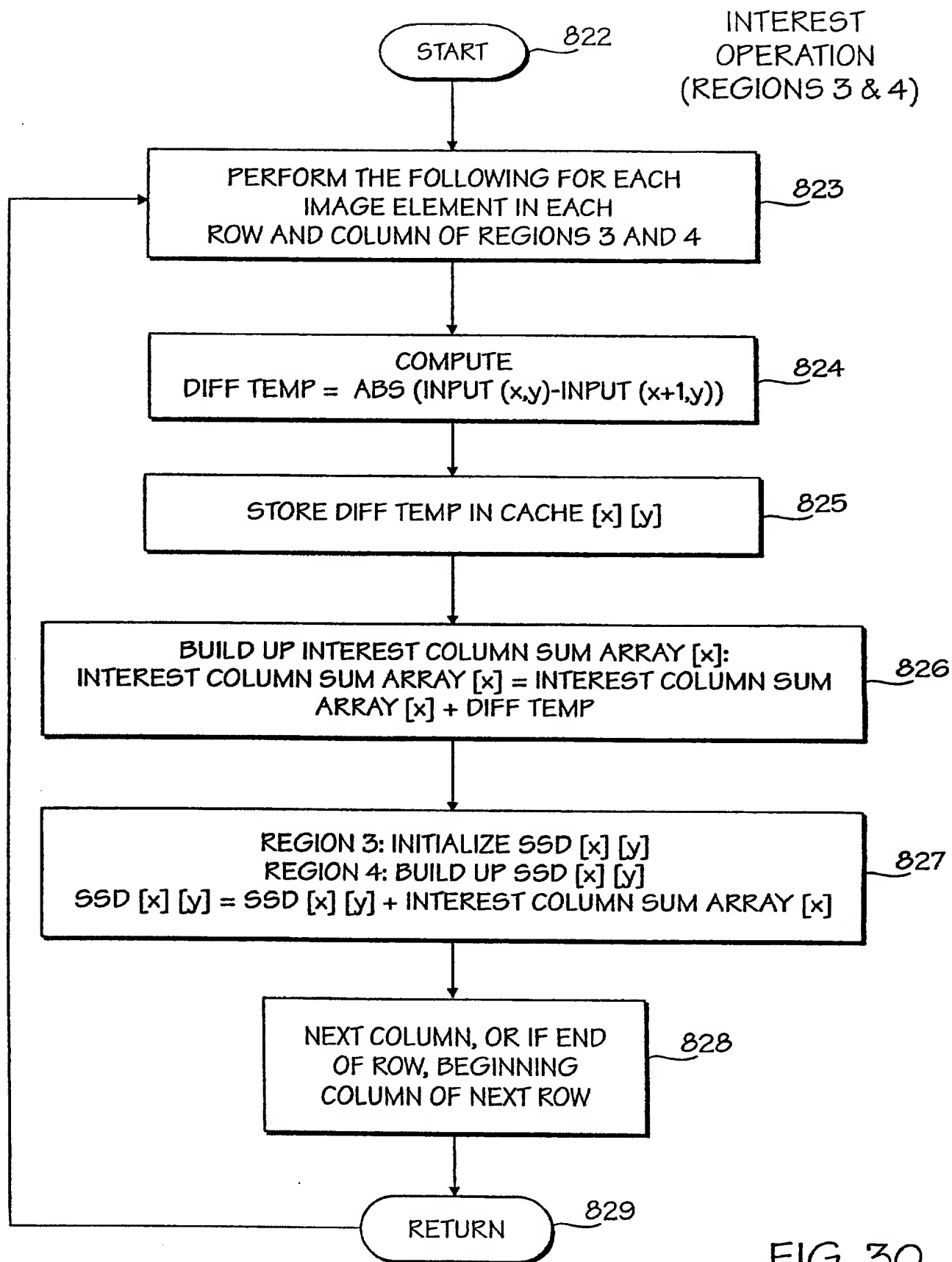


FIG. 30



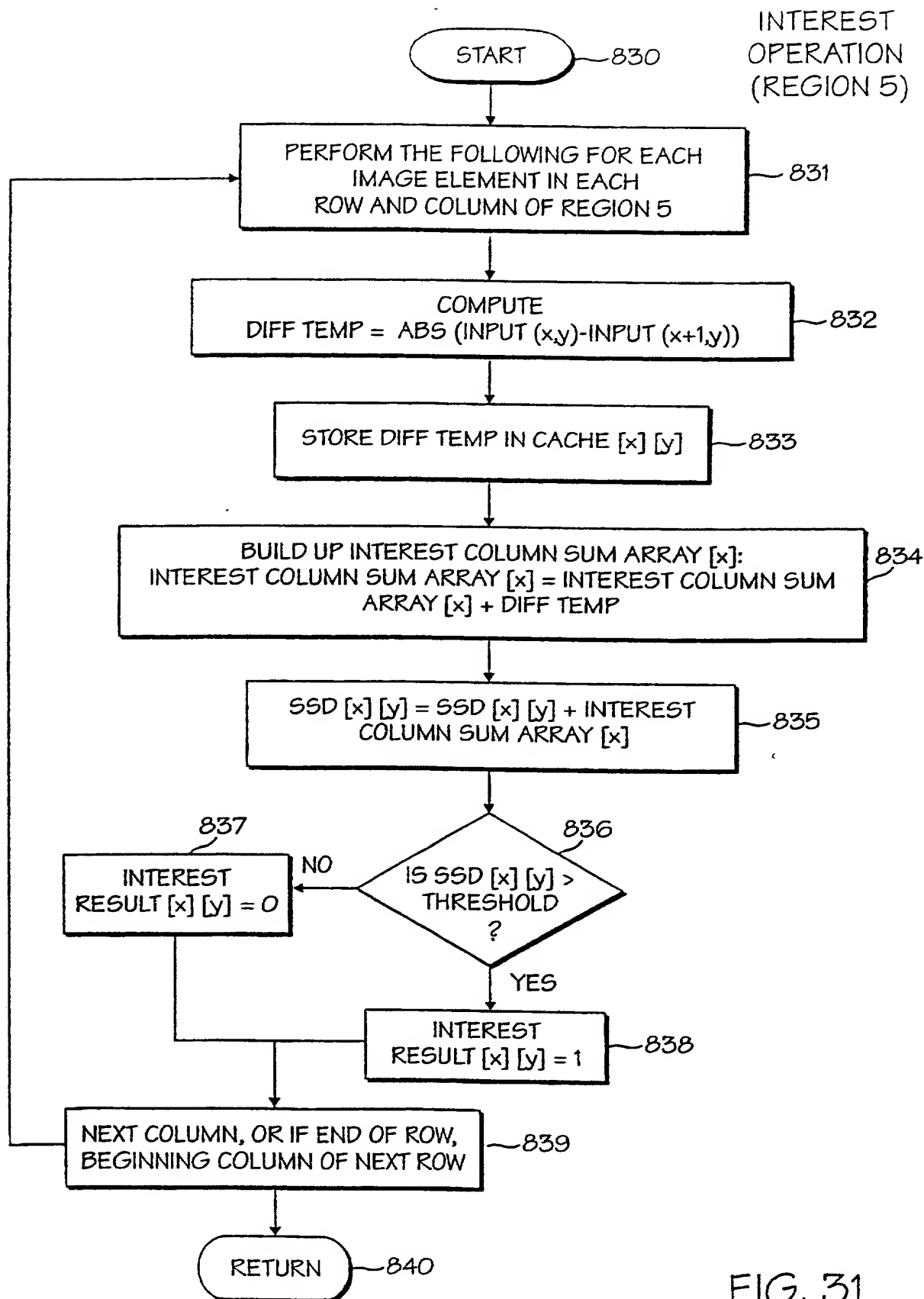


FIG. 31

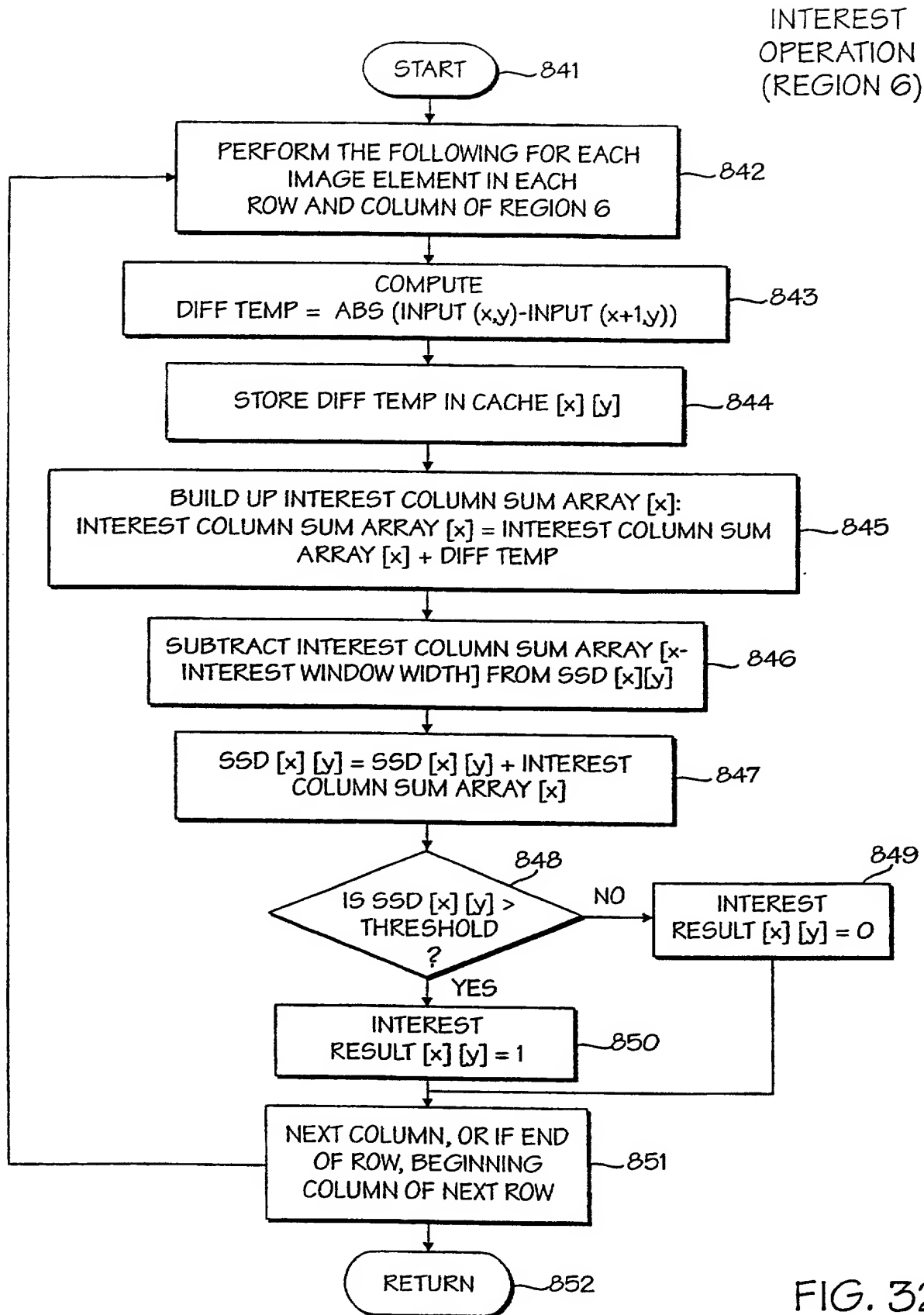


FIG. 32

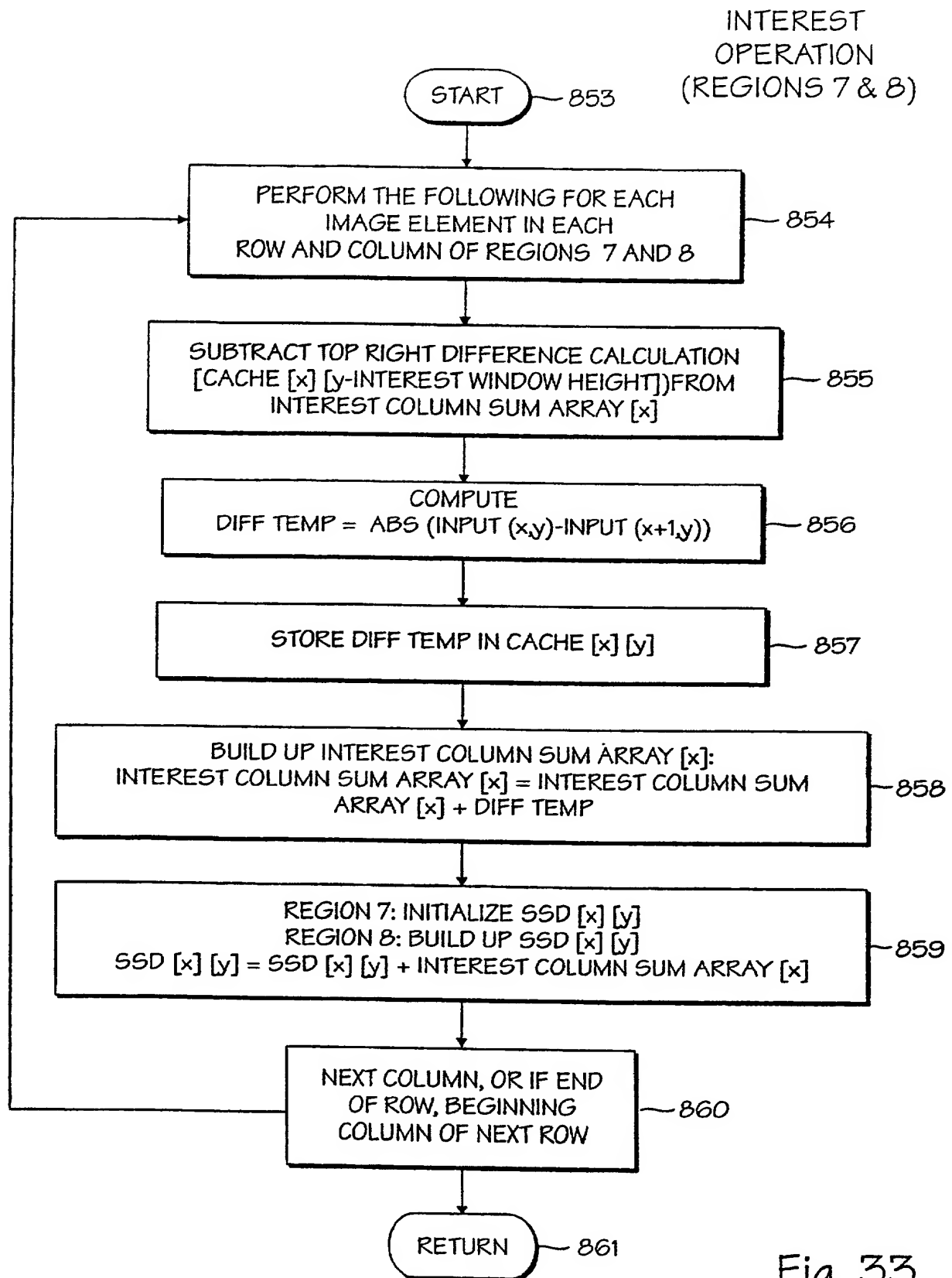


Fig. 33

FIG. 34

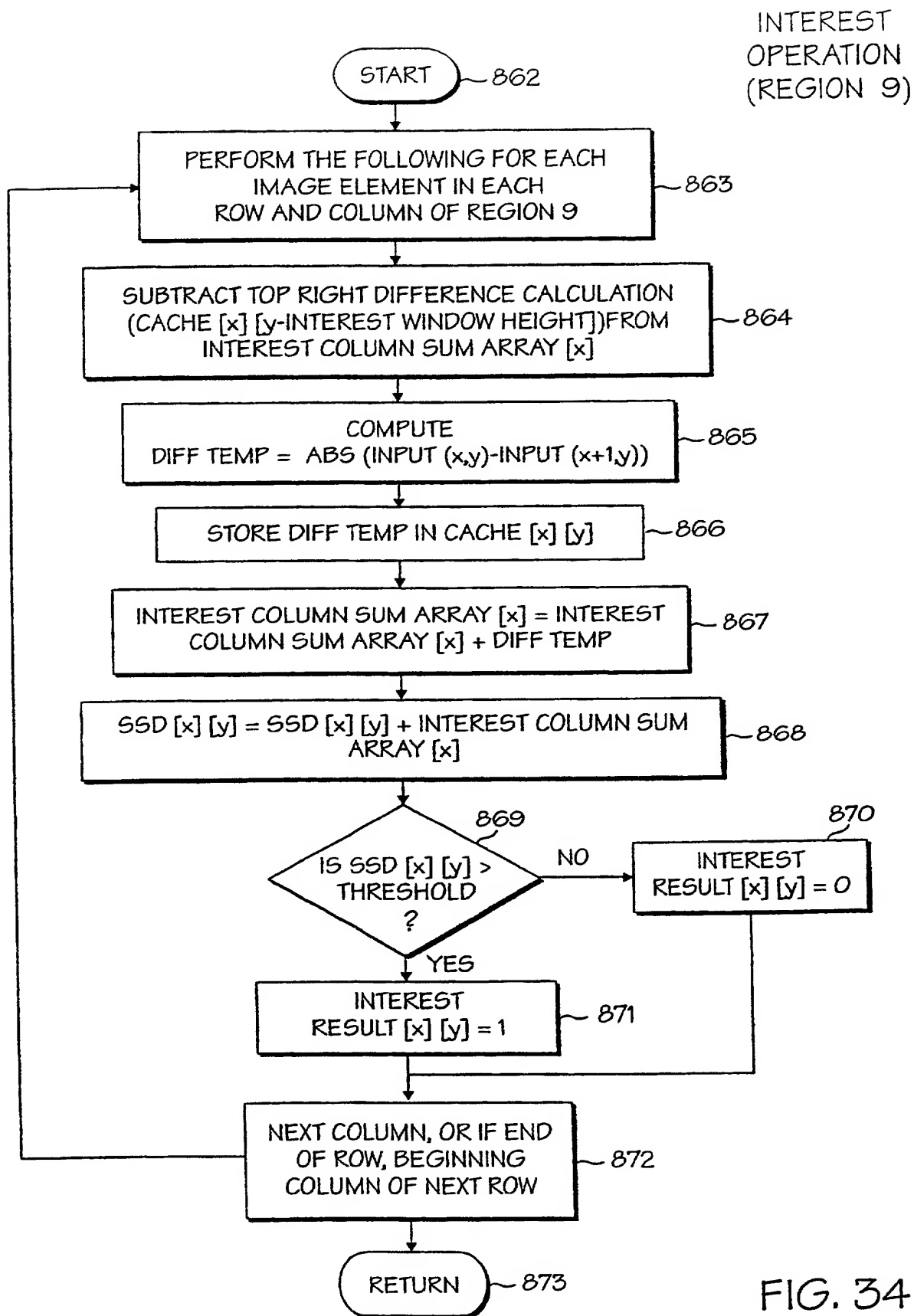


FIG. 34

INTEREST  
OPERATION  
(REGION 10)

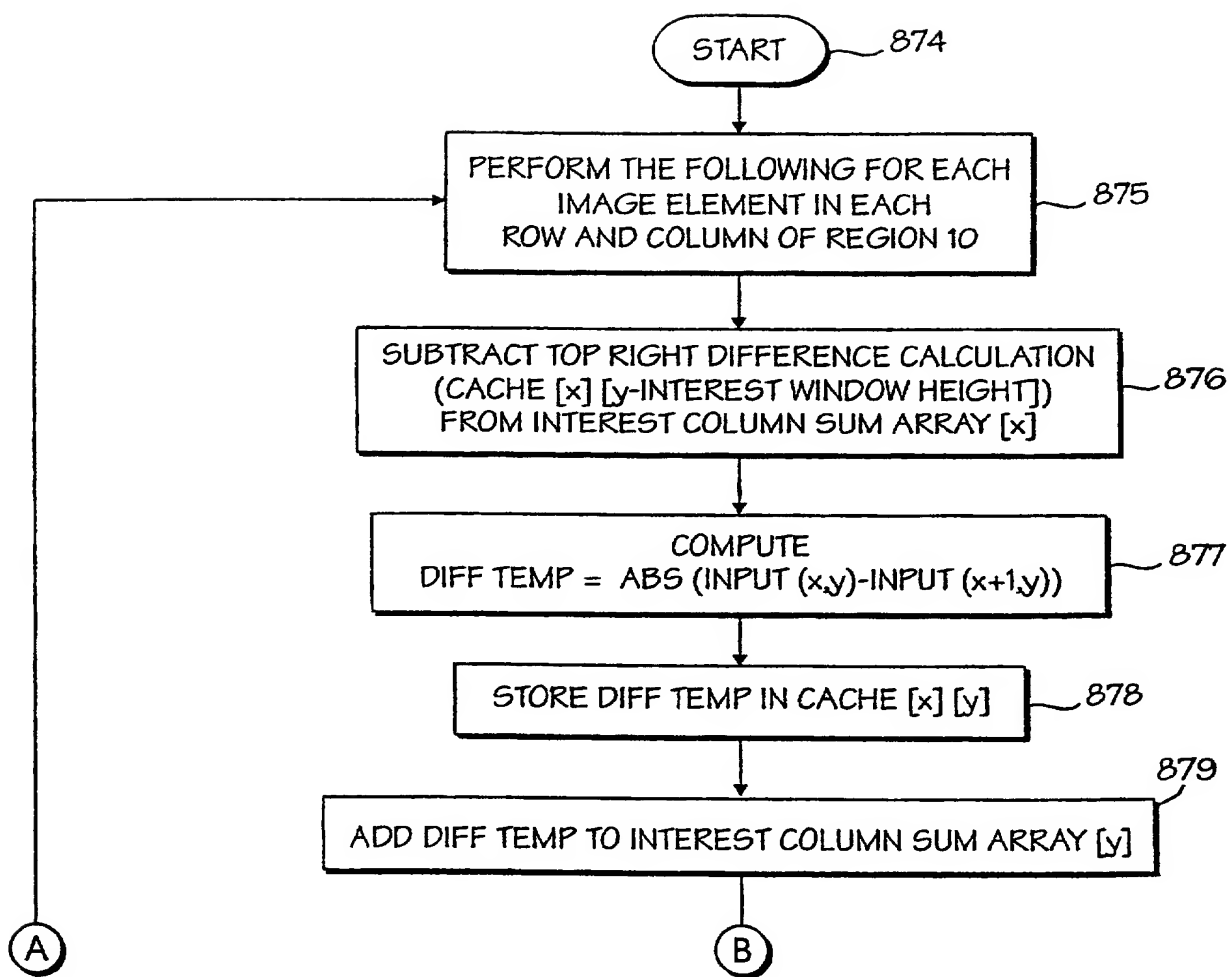


Fig. 35(A)

INTEREST  
OPERATION  
(REGION 10)

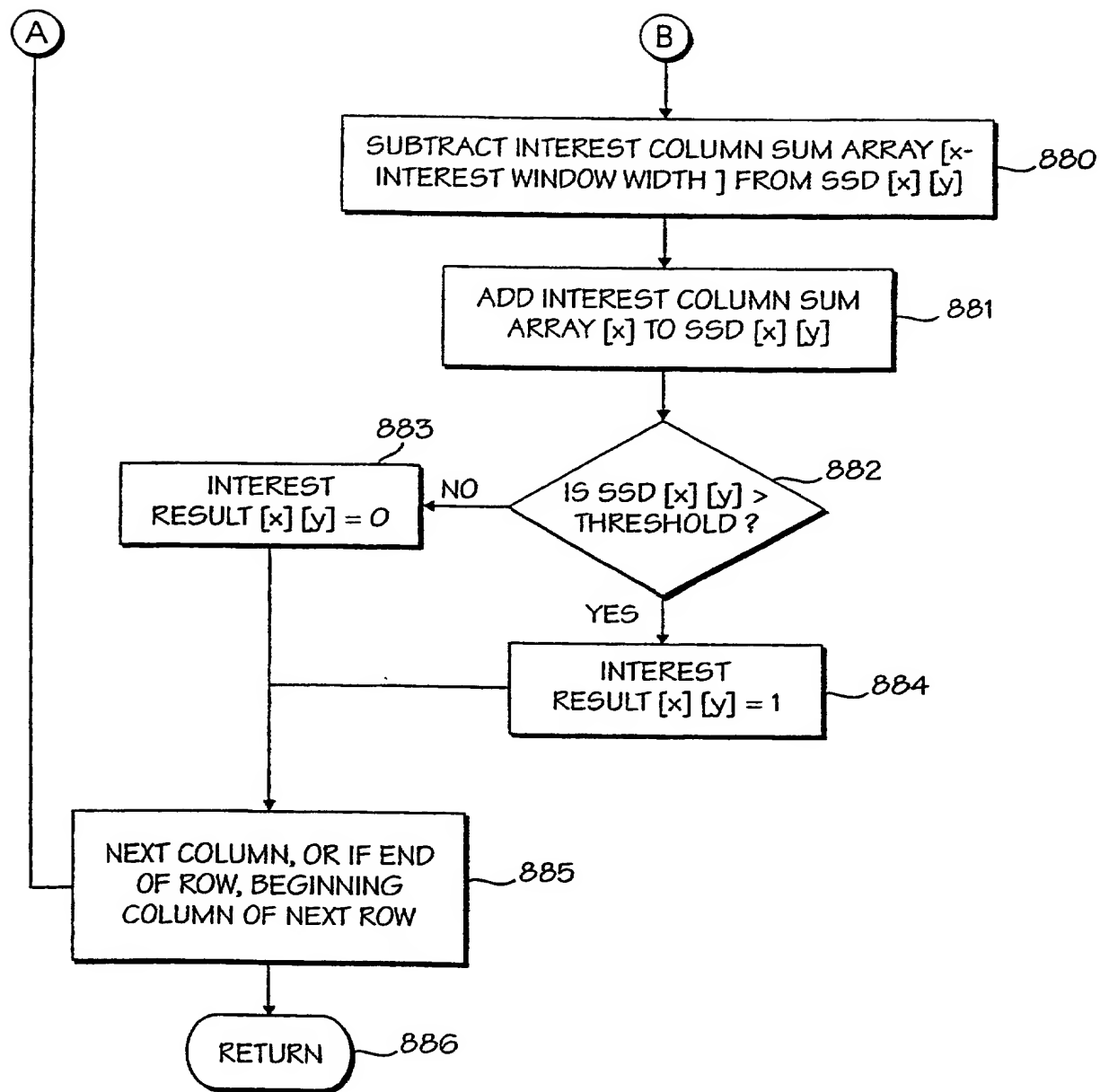
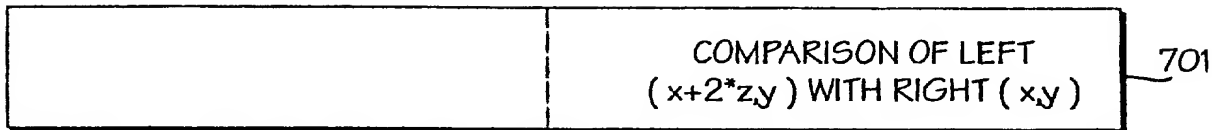


Fig. 35(B)

FIG. 36

DATA PACKING

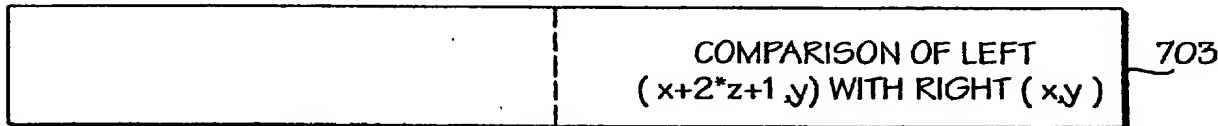
INTERMEDIATE TEMP 1



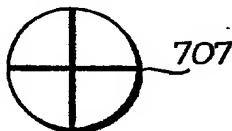
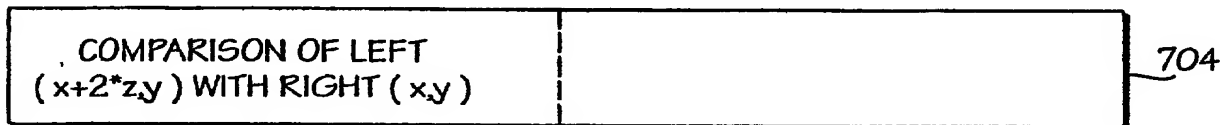
INTERMEDIATE TEMP 2



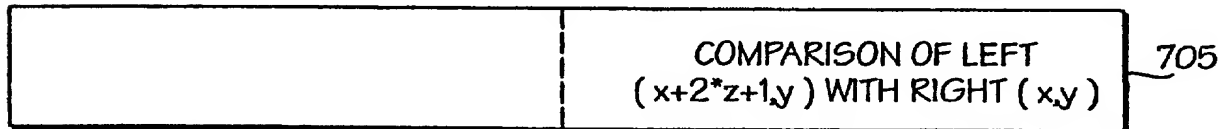
INTERMEDIATE TEMP 1



INTERMEDIATE TEMP 2



INTERMEDIATE TEMP 1



INTERMEDIATE TEMP 1

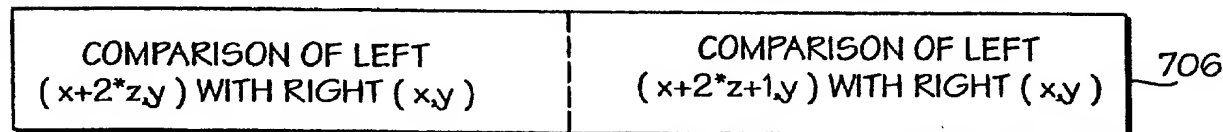


FIG. 36

# LEFT - RIGHT CONSISTENCY CHECK

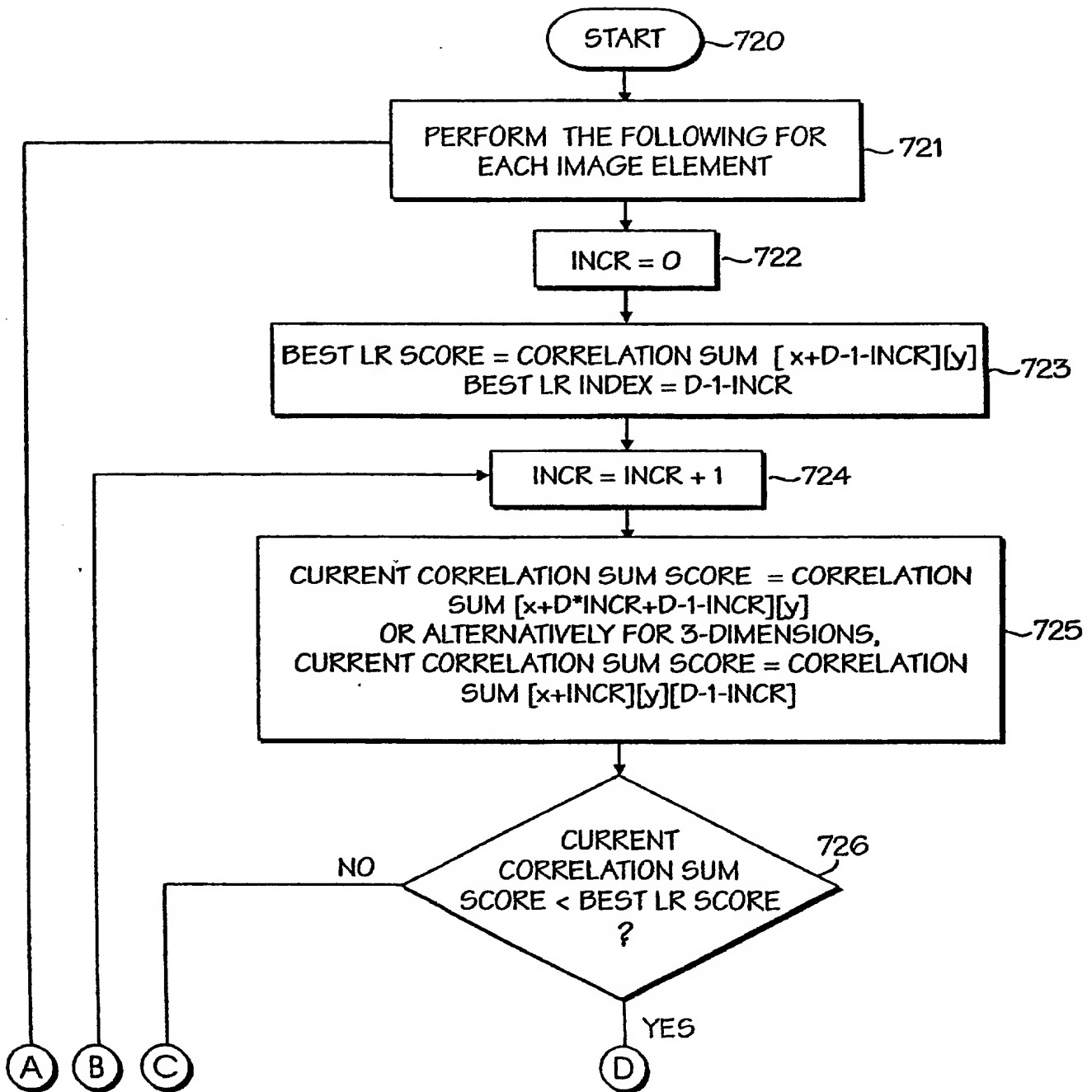


FIG. 37(A)



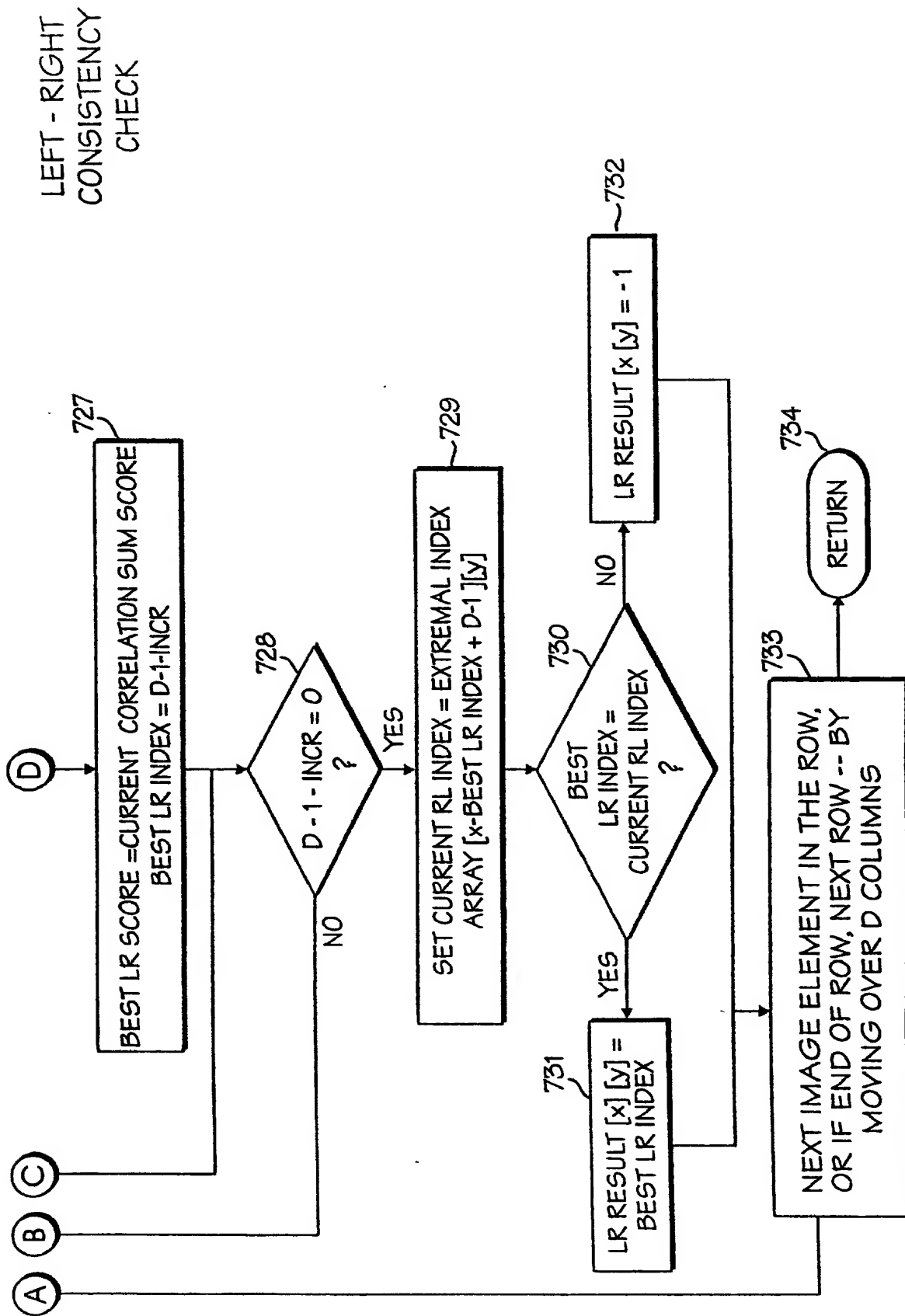
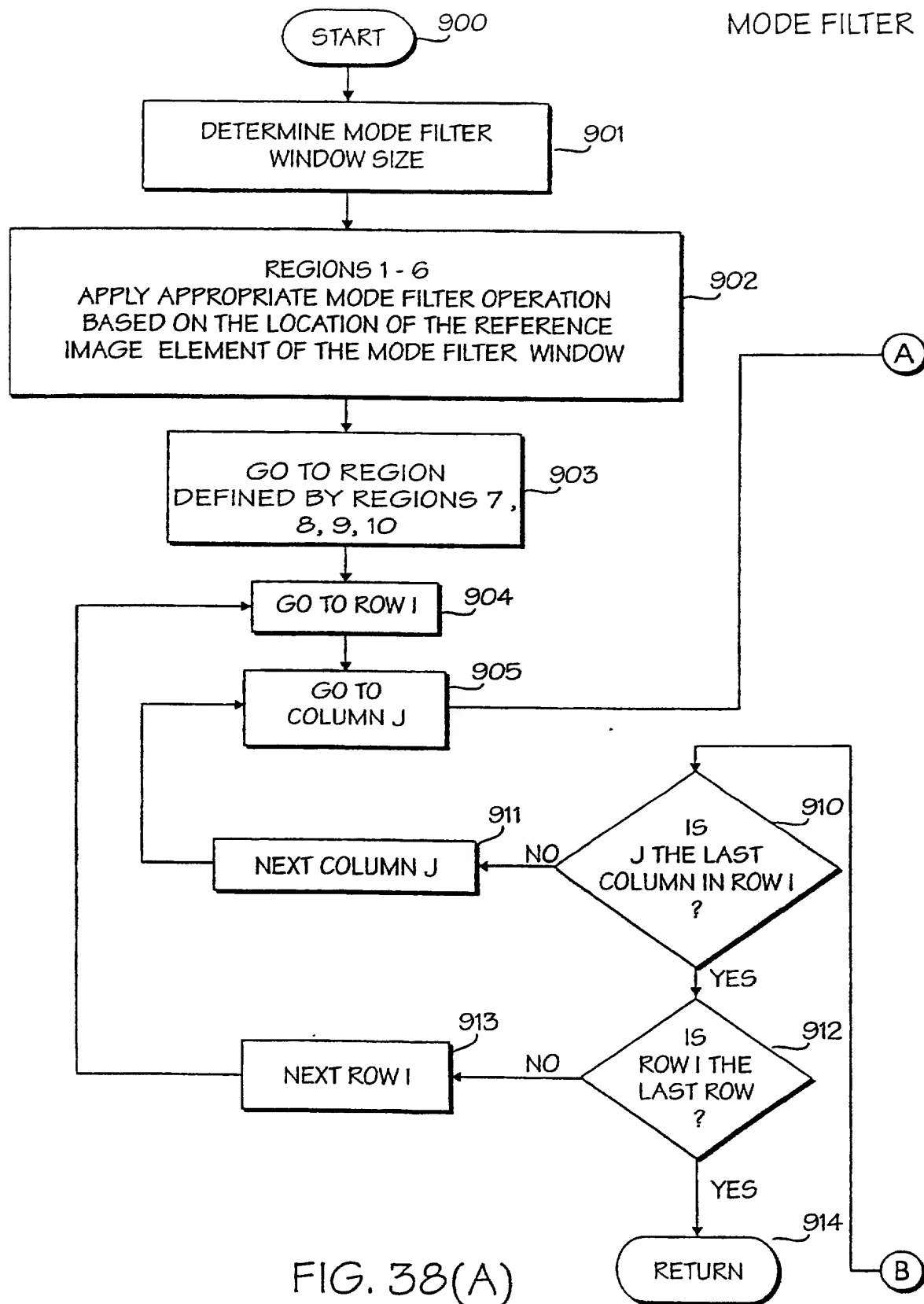


FIG. 37(B)



1002036 121401

MODE FILTER

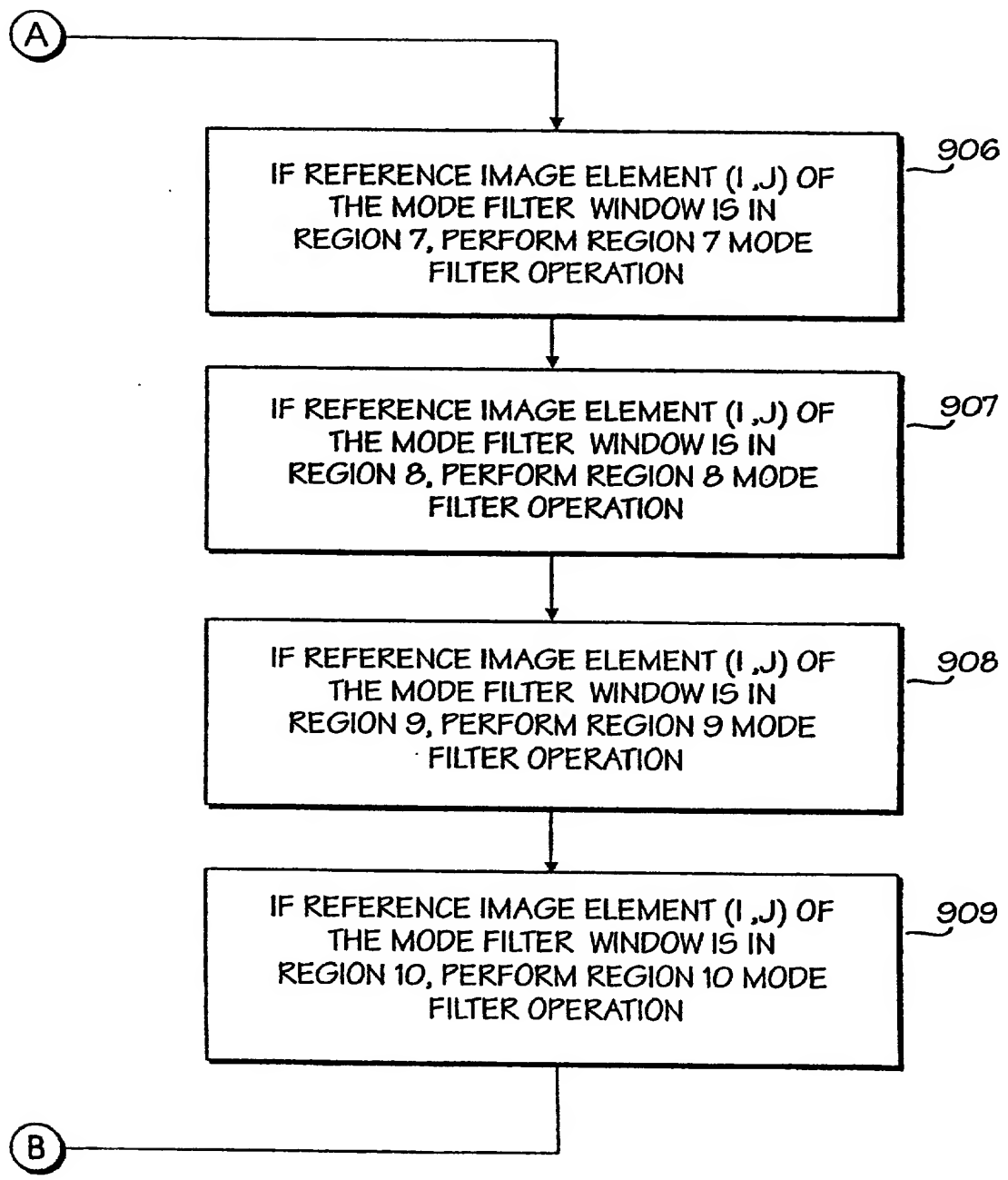


FIG. 38(B)

# MODE FILTER ( REGIONS 1 & 2 )

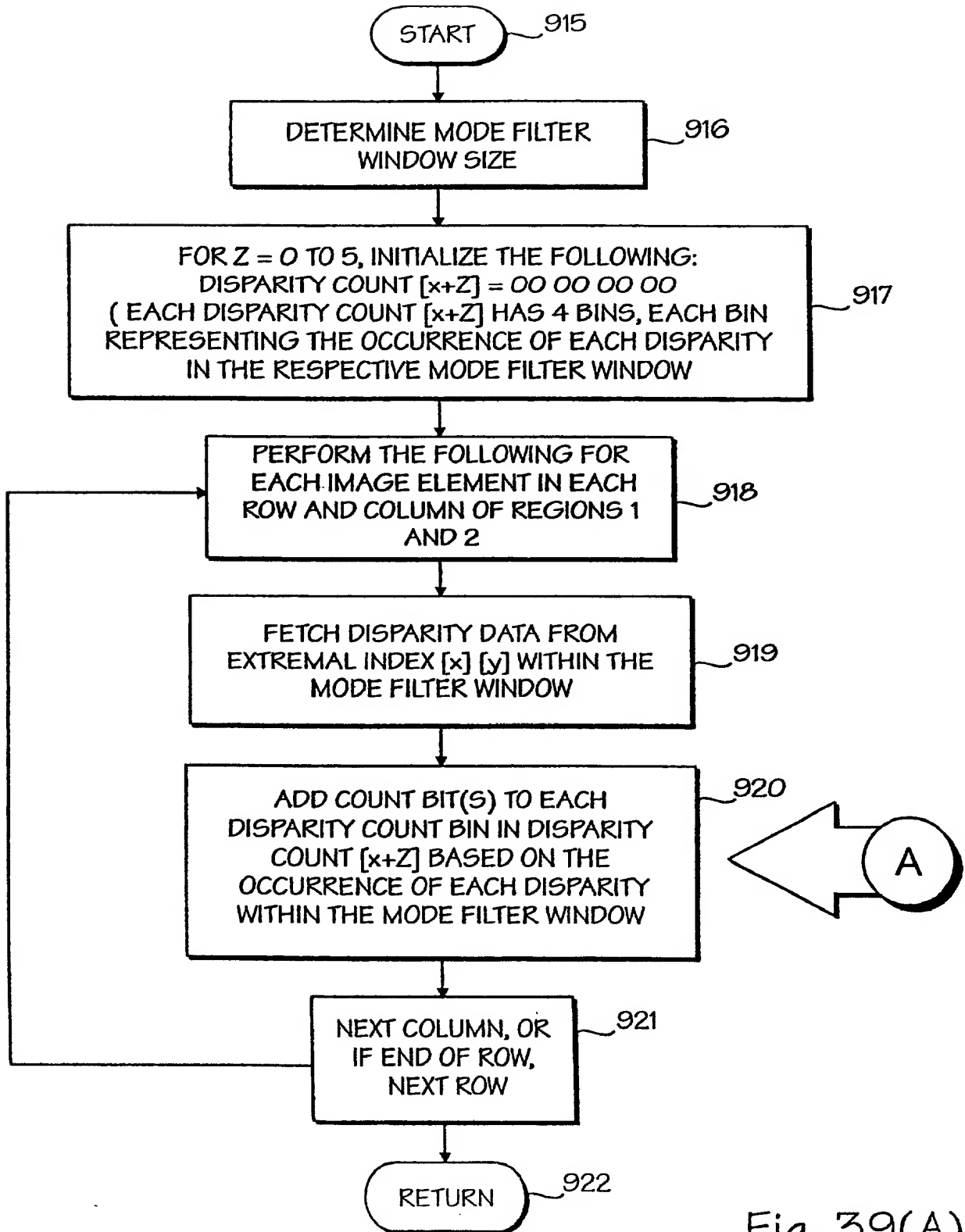


Fig. 39(A)

10020862 121401

MODE FILTER  
( REGIONS 1 & 2 )

A

<u>DISP</u>		<u>DISPARITY COUNT [x+Z]</u>
0	[x]	<span style="border: 1px solid black;">00</span> 00 00 00
1	[x]	00 <span style="border: 1px solid black;">00</span> 00 00
2	[x]	00 00 <span style="border: 1px solid black;">00</span> 00
3	[x]	00 00 00 <span style="border: 1px solid black;">00</span>
4	[x+1]	<span style="border: 1px solid black;">00</span> 00 00 00
5	[x+1]	00 <span style="border: 1px solid black;">00</span> 00 00
6	[x+1]	00 00 <span style="border: 1px solid black;">00</span> 00
7	[x+1]	00 00 00 <span style="border: 1px solid black;">00</span>
8	[x+2]	<span style="border: 1px solid black;">00</span> 00 00 00
9	[x+2]	00 <span style="border: 1px solid black;">00</span> 00 00
10	[x+2]	00 00 <span style="border: 1px solid black;">00</span> 00
11	[x+2]	00 00 00 <span style="border: 1px solid black;">00</span>
12	[x+3]	<span style="border: 1px solid black;">00</span> 00 00 00
13	[x+3]	00 <span style="border: 1px solid black;">00</span> 00 00
14	[x+3]	00 00 <span style="border: 1px solid black;">00</span> 00
15	[x+3]	00 00 00 <span style="border: 1px solid black;">00</span>
16	[x+4]	<span style="border: 1px solid black;">00</span> 00 00 00
17	[x+4]	00 <span style="border: 1px solid black;">00</span> 00 00
18	[x+4]	00 00 <span style="border: 1px solid black;">00</span> 00
19	[x+4]	00 00 00 <span style="border: 1px solid black;">00</span>
20	[x+5]	<span style="border: 1px solid black;">00</span> 00 00 00
21	[x+5]	00 <span style="border: 1px solid black;">00</span> 00 00
22	[x+5]	00 00 <span style="border: 1px solid black;">00</span> 00
23	[x+5]	00 00 00 <span style="border: 1px solid black;">00</span>

923

Fig. 39(B)

MODE FILTER  
( REGIONS 3 & 4)

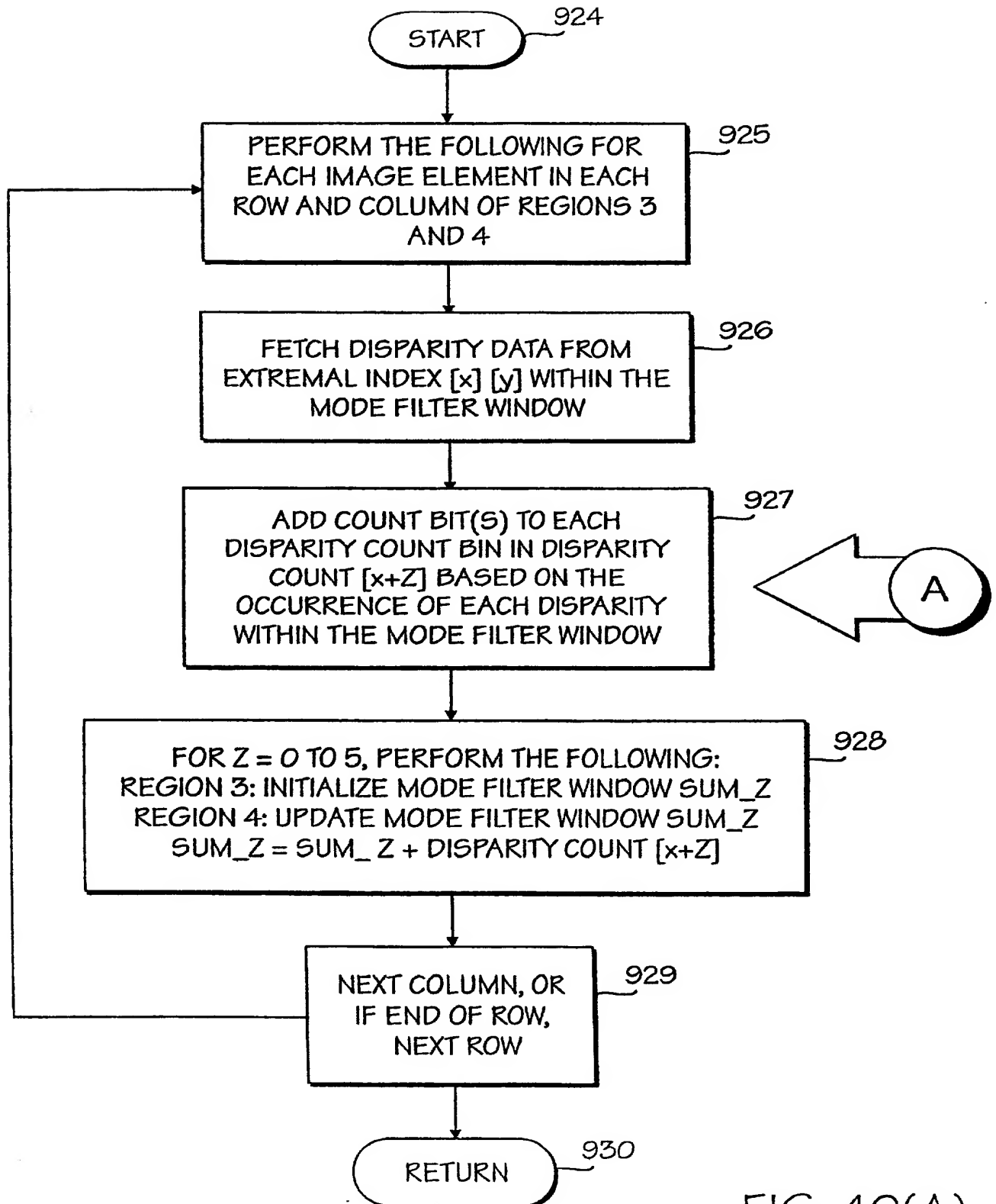


FIG. 40(A)

MODE FILTER  
( REGIONS 3 & 4 )

A

<u>DISP</u>	<u>DISPARITY COUNT [x+Z]</u>			
0	[x]	<u>00</u>	00 00 00	
1	[x]	00	<u>00</u>	00 00
2	[x]	00 00	<u>00</u>	00
3	[x]	00 00 00	<u>00</u>	
4	[x+1]	<u>00</u>	00 00 00	
5	[x+1]	00	<u>00</u>	00 00
6	[x+1]	00 00	<u>00</u>	00
7	[x+1]	00 00 00	<u>00</u>	
8	[x+2]	<u>00</u>	00 00 00	
9	[x+2]	00	<u>00</u>	00 00
10	[x+2]	00 00	<u>00</u>	00
11	[x+2]	00 00 00	<u>00</u>	
12	[x+3]	<u>00</u>	00 00 00	
13	[x+3]	00	<u>00</u>	00 00
14	[x+3]	00 00	<u>00</u>	00
15	[x+3]	00 00 00	<u>00</u>	
16	[x+4]	<u>00</u>	00 00 00	
17	[x+4]	00	<u>00</u>	00 00
18	[x+4]	00 00	<u>00</u>	00
19	[x+4]	00 00 00	<u>00</u>	
20	[x+5]	<u>00</u>	00 00 00	
21	[x+5]	00	<u>00</u>	00 00
22	[x+5]	00 00	<u>00</u>	00
23	[x+5]	00 00 00	<u>00</u>	

931

FIG. 40(B)

# MODE FILTER ( REGION 5)

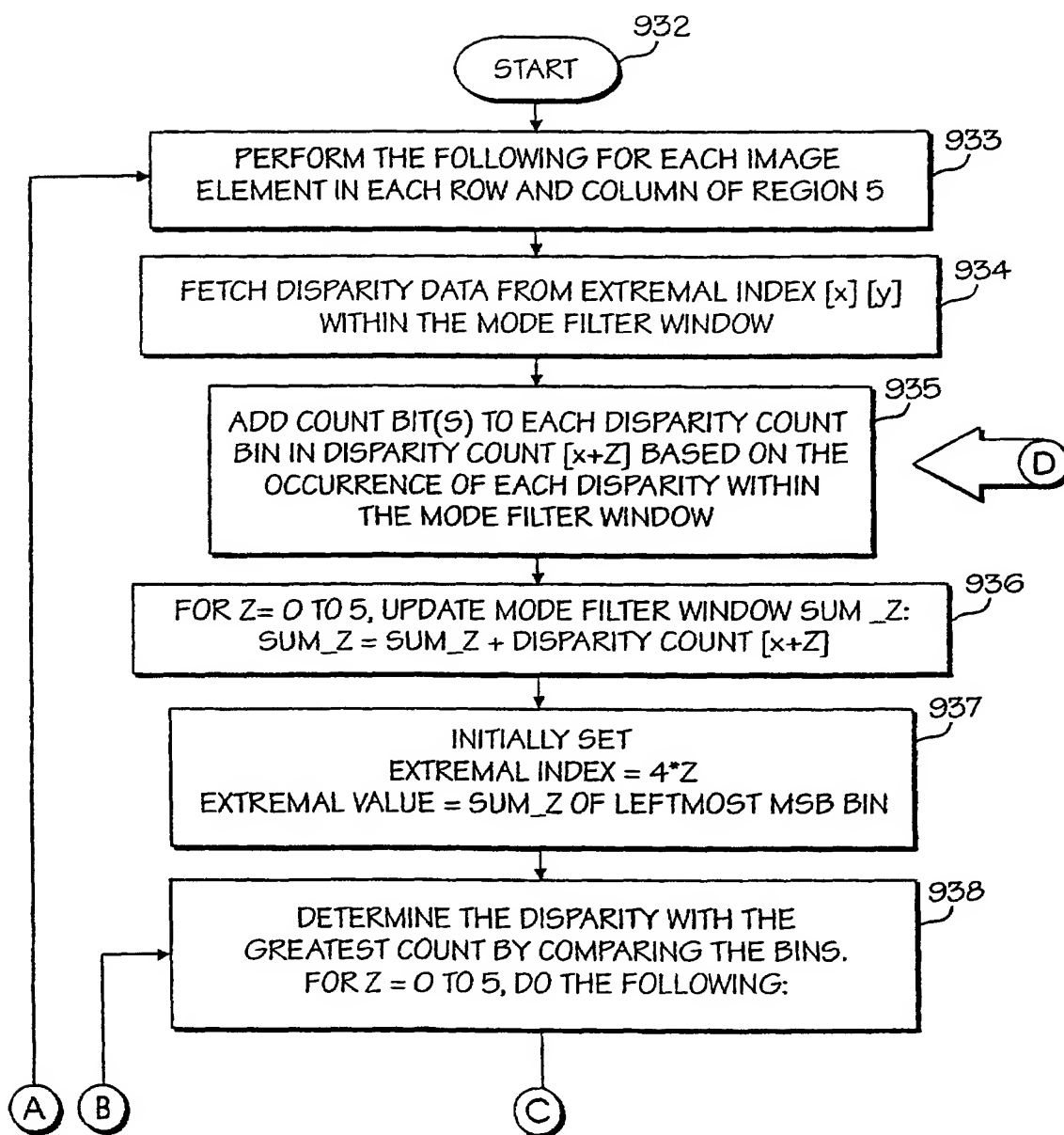


FIG. 41(A)



MODE FILTER  
( REGION 5)

D

DISP	DISPARITY COUNT [x+Z]			
0	[x]	00	00	00
1	[x]	00	00	00
2	[x]	00	00	00
3	[x]	00	00	00
4	[x+1]	00	00	00
5	[x+1]	00	00	00
6	[x+1]	00	00	00
7	[x+1]	00	00	00
8	[x+2]	00	00	00
9	[x+2]	00	00	00
10	[x+2]	00	00	00
11	[x+2]	00	00	00
12	[x+3]	00	00	00
13	[x+3]	00	00	00
14	[x+3]	00	00	00
15	[x+3]	00	00	00
16	[x+4]	00	00	00
17	[x+4]	00	00	00
18	[x+4]	00	00	00
19	[x+4]	00	00	00
20	[x+5]	00	00	00
21	[x+5]	00	00	00
22	[x+5]	00	00	00
23	[x+5]	00	00	00

951

FIG. 41(B)

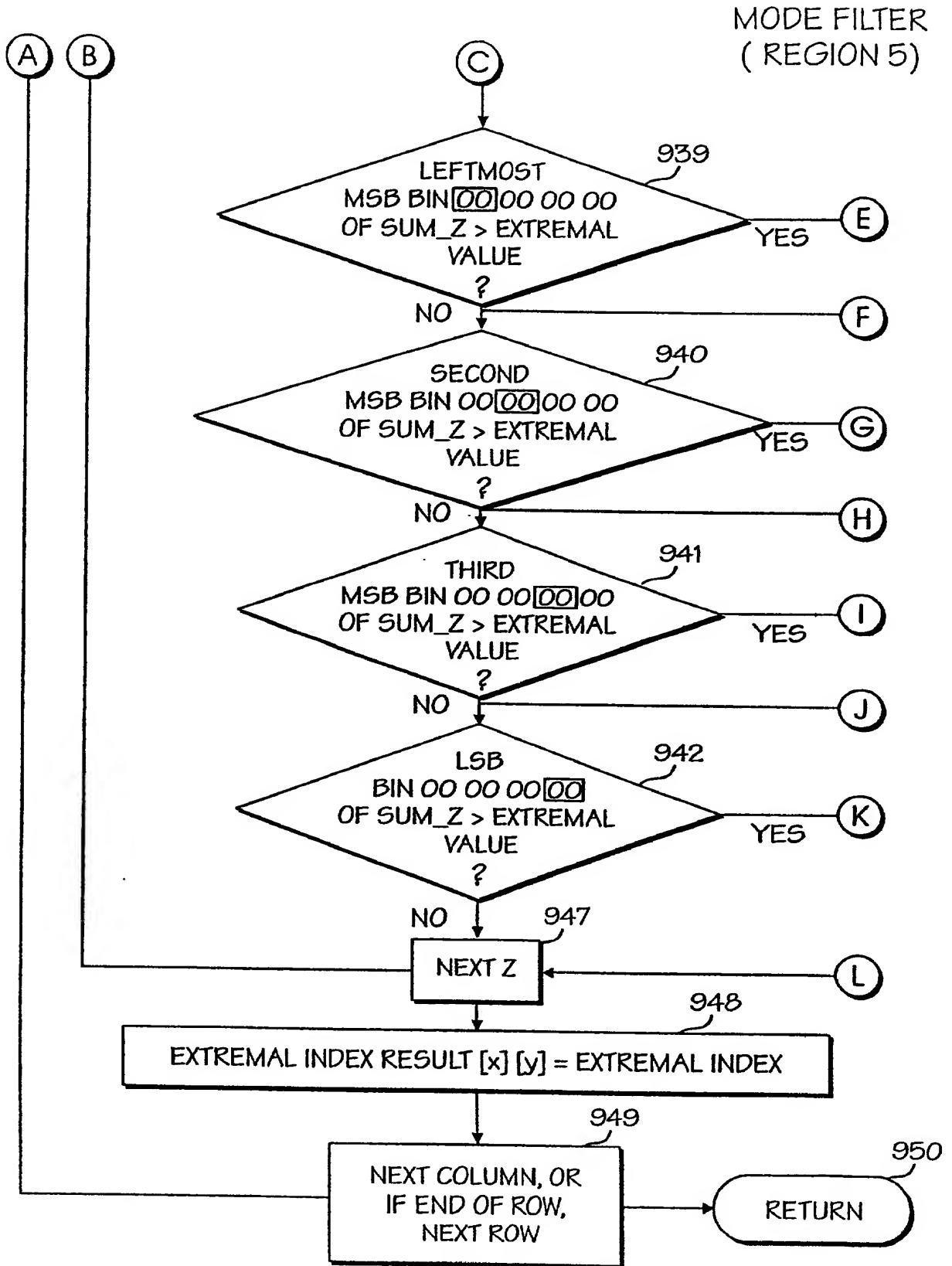


FIG. 41(C)

MODE FILTER  
( REGION 5)

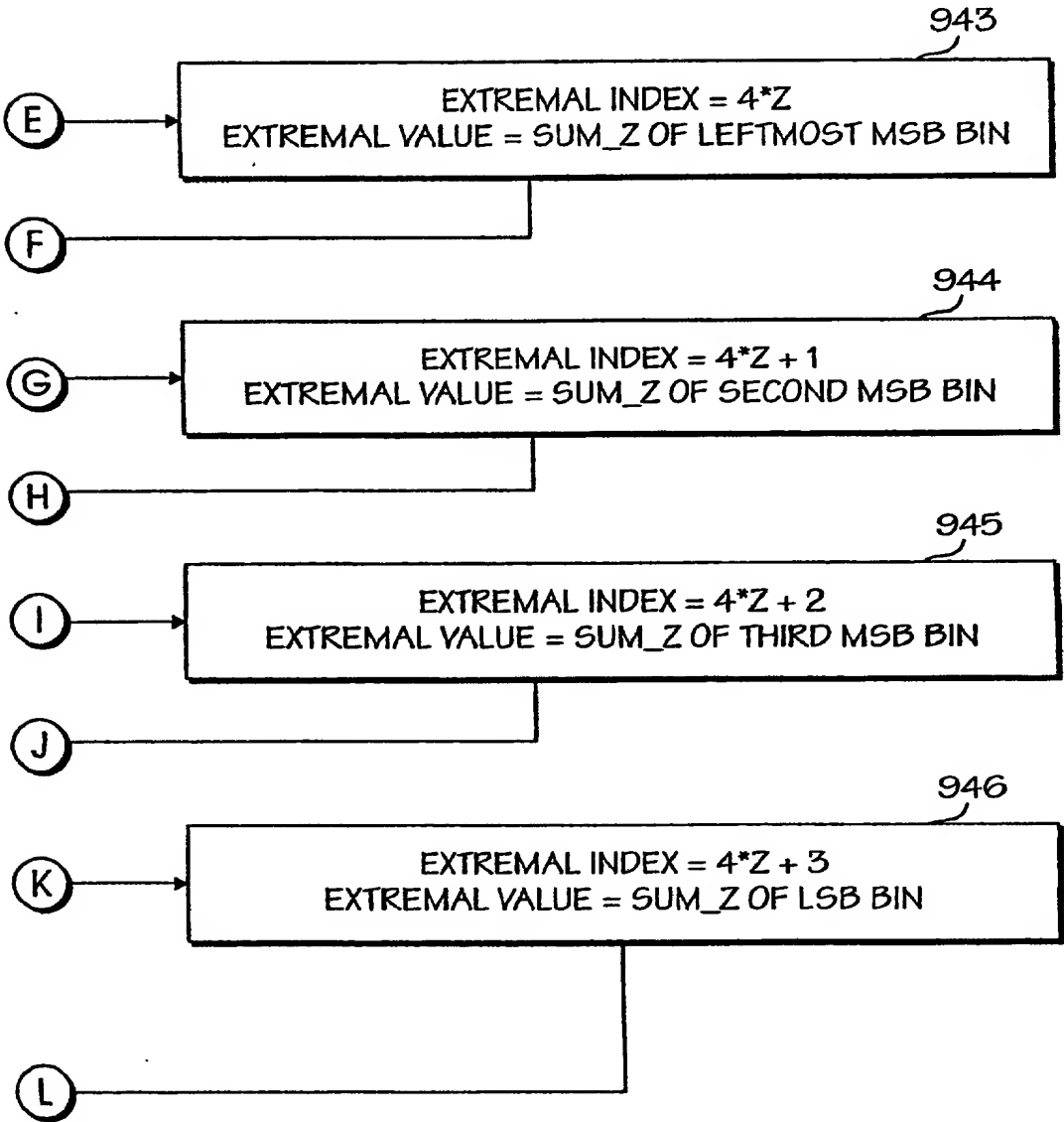


FIG. 41(D)

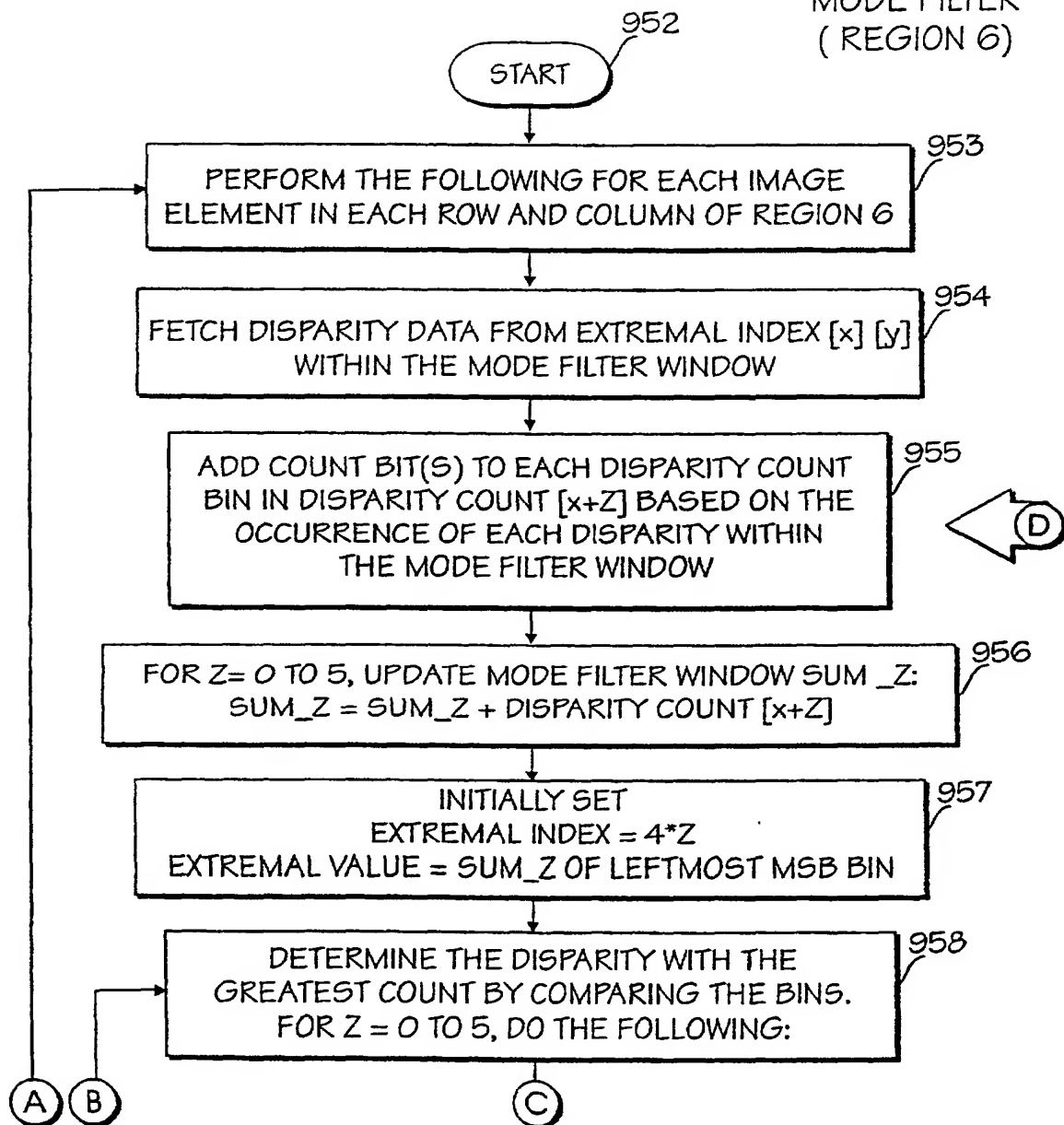


FIG. 42(A)

100203E 134401

MODE FILTER  
( REGION 6)

DISP	DISPARITY COUNT [x+Z]			
0	[x]	00	00	00
1	[x]	00	00	00
2	[x]	00	00	00
3	[x]	00	00	00
4	[x+1]	00	00	00
5	[x+1]	00	00	00
6	[x+1]	00	00	00
7	[x+1]	00	00	00
8	[x+2]	00	00	00
9	[x+2]	00	00	00
10	[x+2]	00	00	00
11	[x+2]	00	00	00
12	[x+3]	00	00	00
13	[x+3]	00	00	00
14	[x+3]	00	00	00
15	[x+3]	00	00	00
16	[x+4]	00	00	00
17	[x+4]	00	00	00
18	[x+4]	00	00	00
19	[x+4]	00	00	00
20	[x+5]	00	00	00
21	[x+5]	00	00	00
22	[x+5]	00	00	00
23	[x+5]	00	00	00

971

D

FIG. 42(B)

10020863 121401

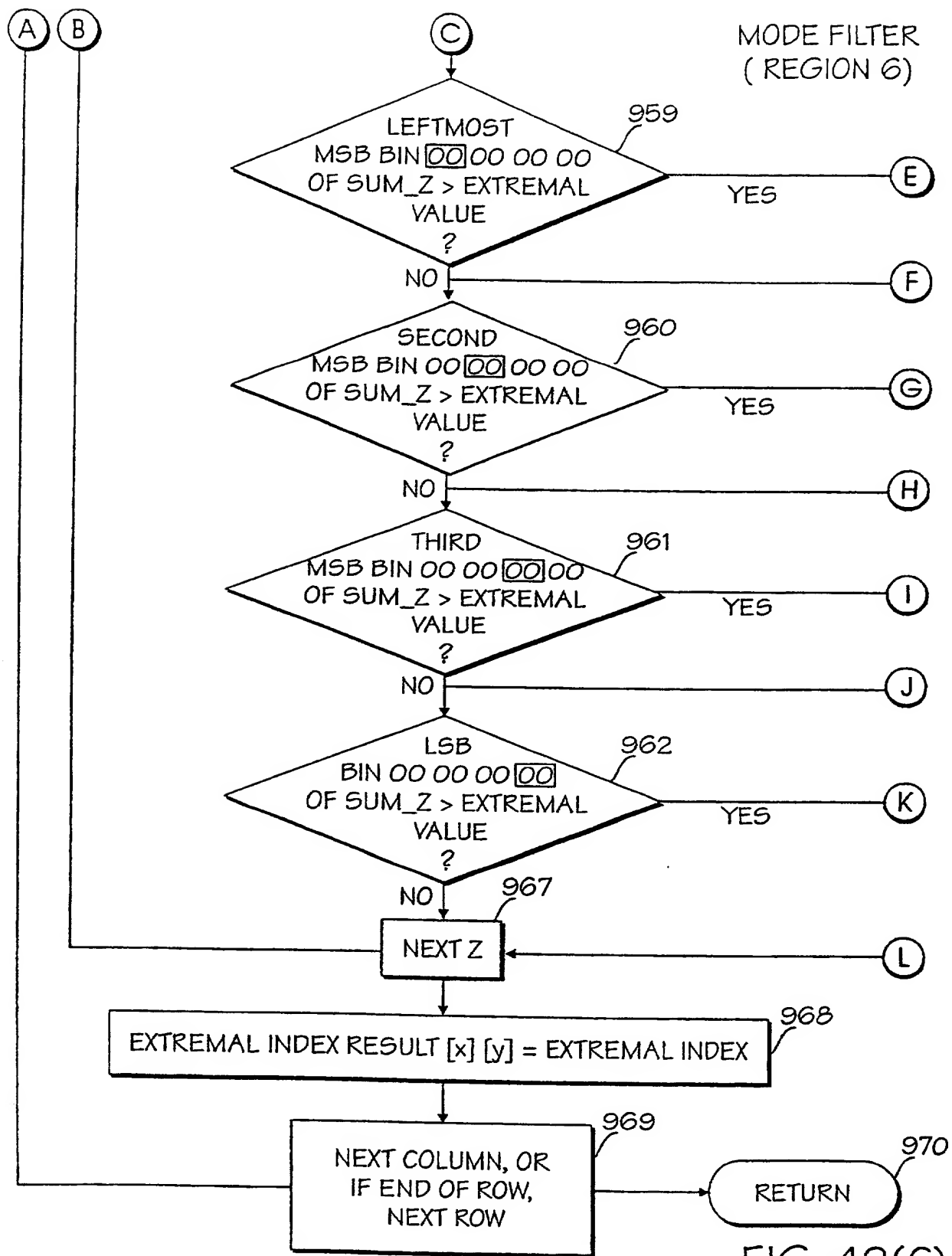


FIG. 42(C)

MODE FILTER  
( REGION 6 )

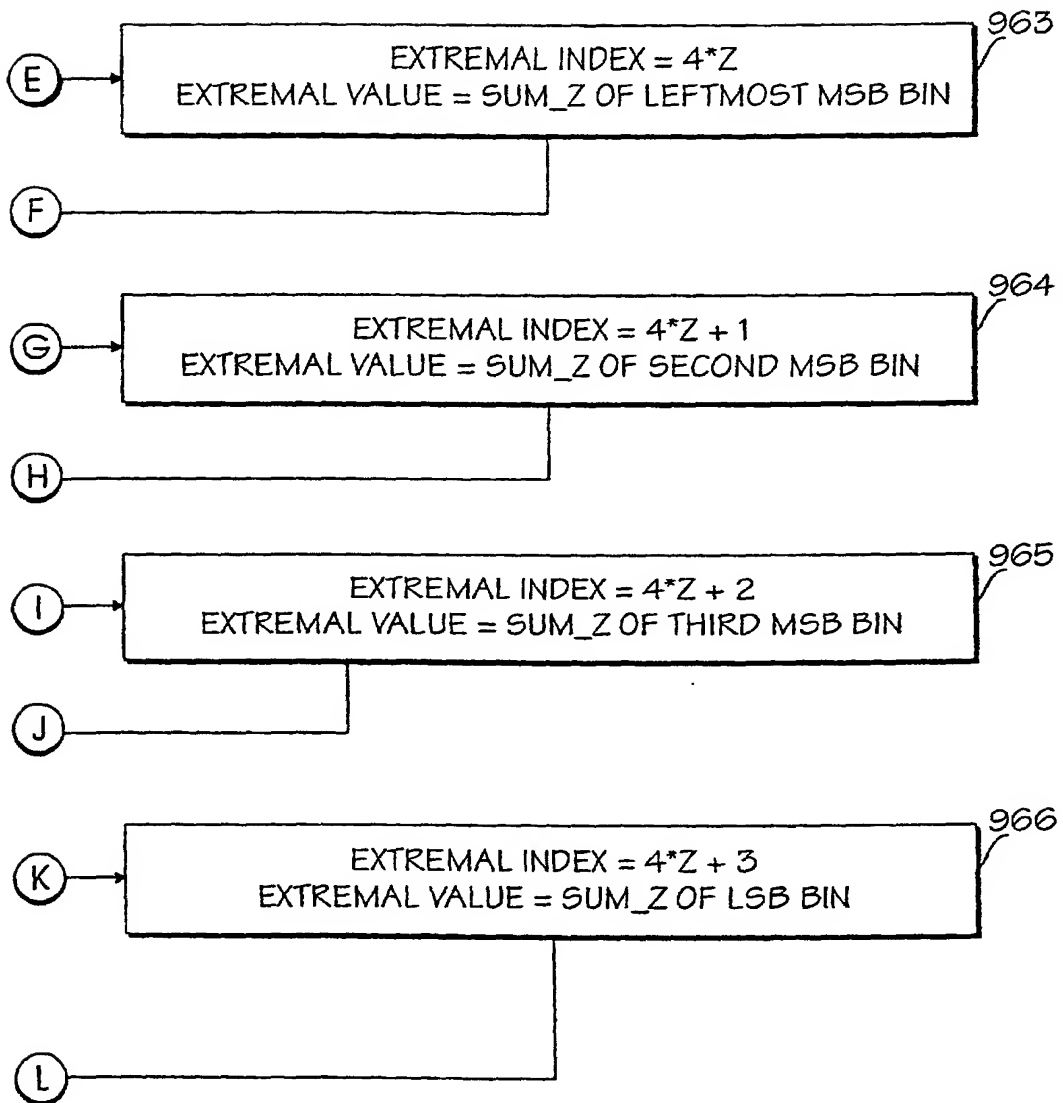


FIG. 42(D)

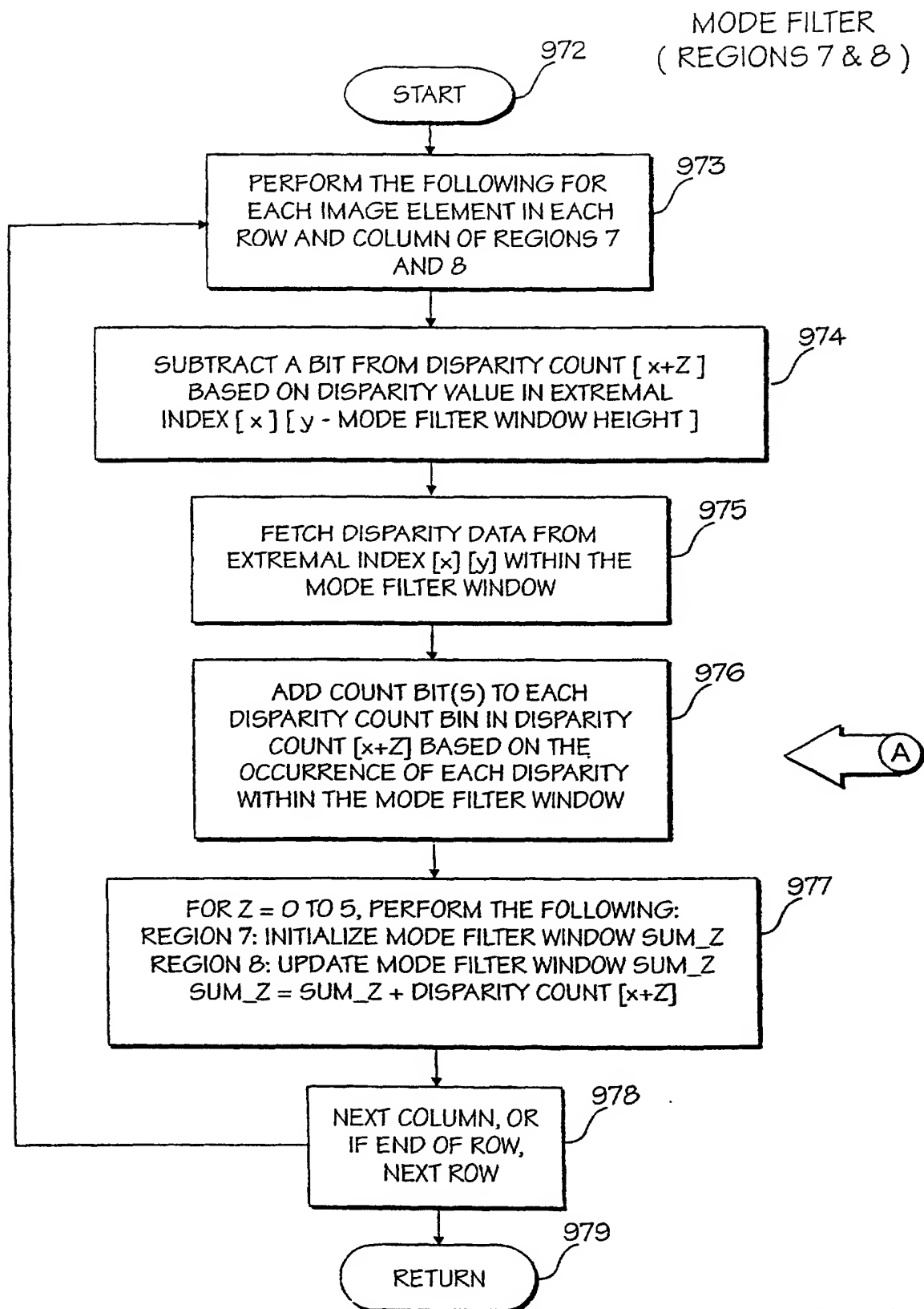


FIG. 43(A)



MODE FILTER  
( REGIONS 7 & 8 )

980

A

<u>DISP</u>		<u>DISPARITY COUNT [x+Z]</u>
0	[x]	<span style="border: 1px solid black;">00</span> 00 00 00
1	[x]	00 <span style="border: 1px solid black;">00</span> 00 00
2	[x]	00 00 <span style="border: 1px solid black;">00</span> 00
3	[x]	00 00 00 <span style="border: 1px solid black;">00</span>
4	[x+1]	<span style="border: 1px solid black;">00</span> 00 00 00
5	[x+1]	00 <span style="border: 1px solid black;">00</span> 00 00
6	[x+1]	00 00 <span style="border: 1px solid black;">00</span> 00
7	[x+1]	00 00 00 <span style="border: 1px solid black;">00</span>
8	[x+2]	<span style="border: 1px solid black;">00</span> 00 00 00
9	[x+2]	00 <span style="border: 1px solid black;">00</span> 00 00
10	[x+2]	00 00 <span style="border: 1px solid black;">00</span> 00
11	[x+2]	00 00 00 <span style="border: 1px solid black;">00</span>
12	[x+3]	<span style="border: 1px solid black;">00</span> 00 00 00
13	[x+3]	00 <span style="border: 1px solid black;">00</span> 00 00
14	[x+3]	00 00 <span style="border: 1px solid black;">00</span> 00
15	[x+3]	00 00 00 <span style="border: 1px solid black;">00</span>
16	[x+4]	<span style="border: 1px solid black;">00</span> 00 00 00
17	[x+4]	00 <span style="border: 1px solid black;">00</span> 00 00
18	[x+4]	00 00 <span style="border: 1px solid black;">00</span> 00
19	[x+4]	00 00 00 <span style="border: 1px solid black;">00</span>
20	[x+5]	<span style="border: 1px solid black;">00</span> 00 00 00
21	[x+5]	00 <span style="border: 1px solid black;">00</span> 00 00
22	[x+5]	00 00 <span style="border: 1px solid black;">00</span> 00
23	[x+5]	00 00 00 <span style="border: 1px solid black;">00</span>

FIG. 43(B)

10020362-121401

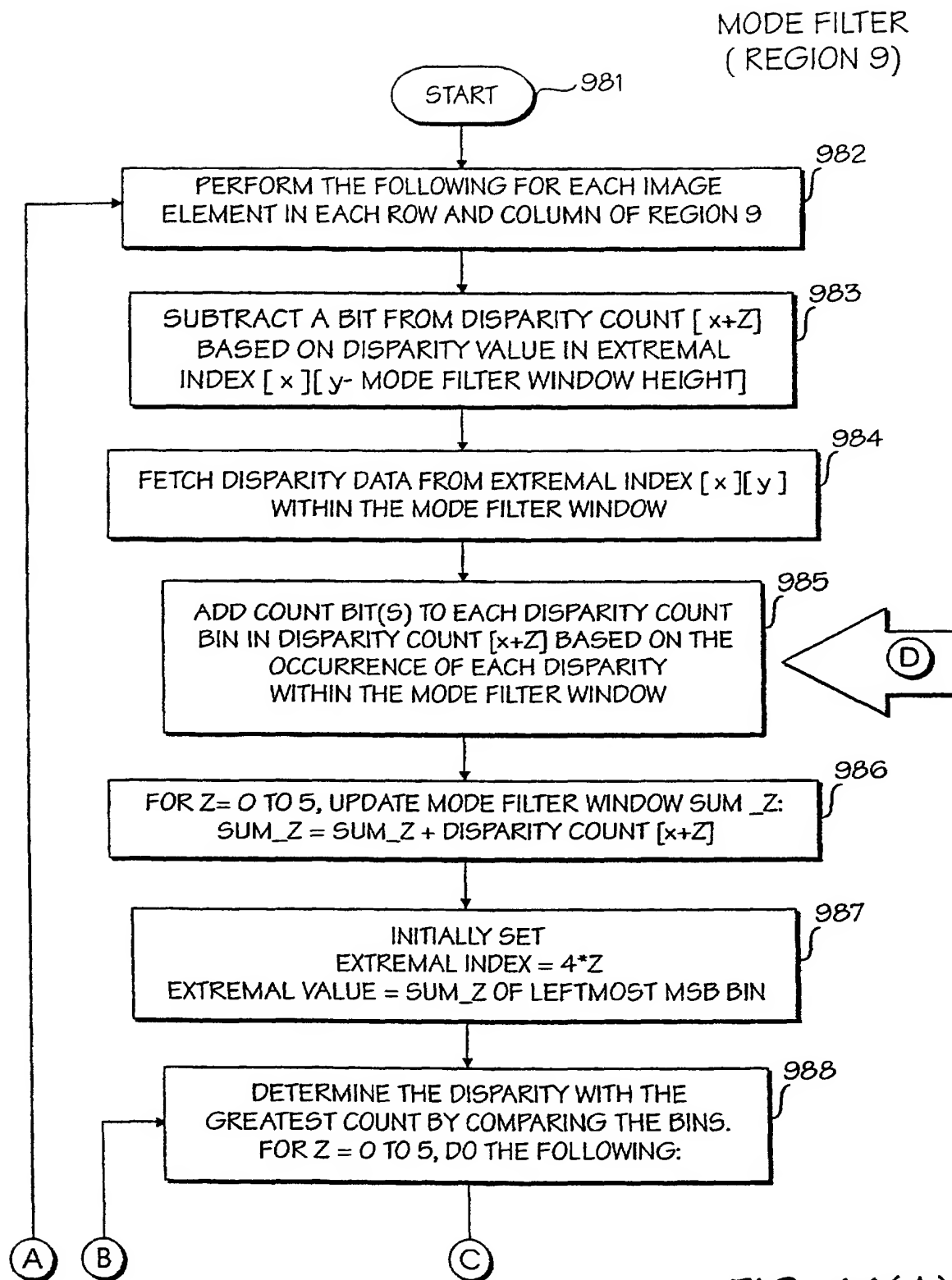


FIG. 44(A)

MODE FILTER  
( REGION 9)

(D)

DISP	DISPARITY COUNT [x+Z]	1001
0	[x] 00 00 00 00	
1	[x] 00 00 00 00	
2	[x] 00 00 00 00	
3	[x] 00 00 00 00	
4	[x+1] 00 00 00 00	
5	[x+1] 00 00 00 00	
6	[x+1] 00 00 00 00	
7	[x+1] 00 00 00 00	
8	[x+2] 00 00 00 00	
9	[x+2] 00 00 00 00	
10	[x+2] 00 00 00 00	
11	[x+2] 00 00 00 00	
12	[x+3] 00 00 00 00	
13	[x+3] 00 00 00 00	
14	[x+3] 00 00 00 00	
15	[x+3] 00 00 00 00	
16	[x+4] 00 00 00 00	
17	[x+4] 00 00 00 00	
18	[x+4] 00 00 00 00	
19	[x+4] 00 00 00 00	
20	[x+5] 00 00 00 00	
21	[x+5] 00 00 00 00	
22	[x+5] 00 00 00 00	
23	[x+5] 00 00 00 00	

FIG. 44(B)

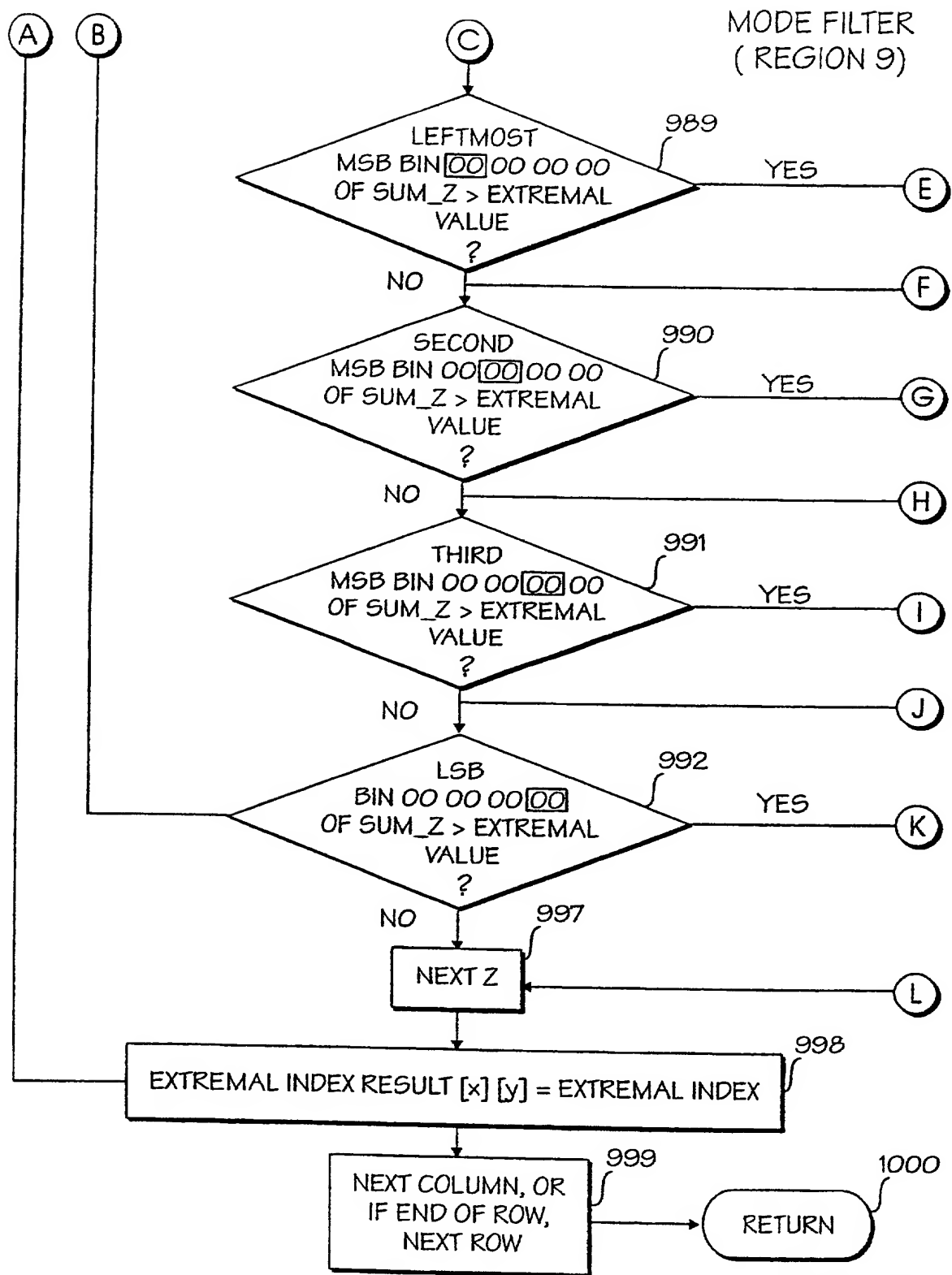


FIG. 44(C)

MODE FILTER  
( REGION 9)

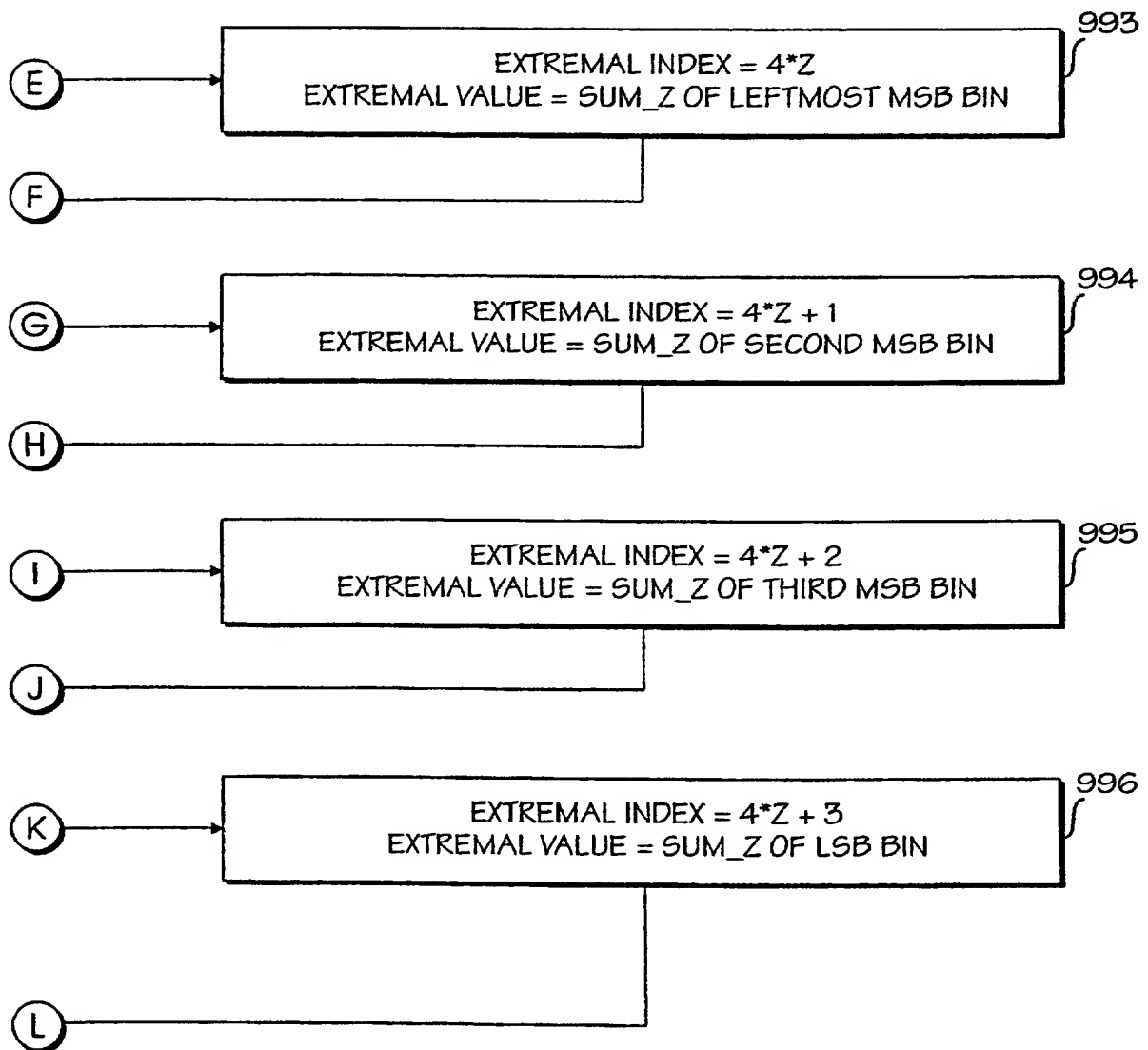


FIG. 44(D)

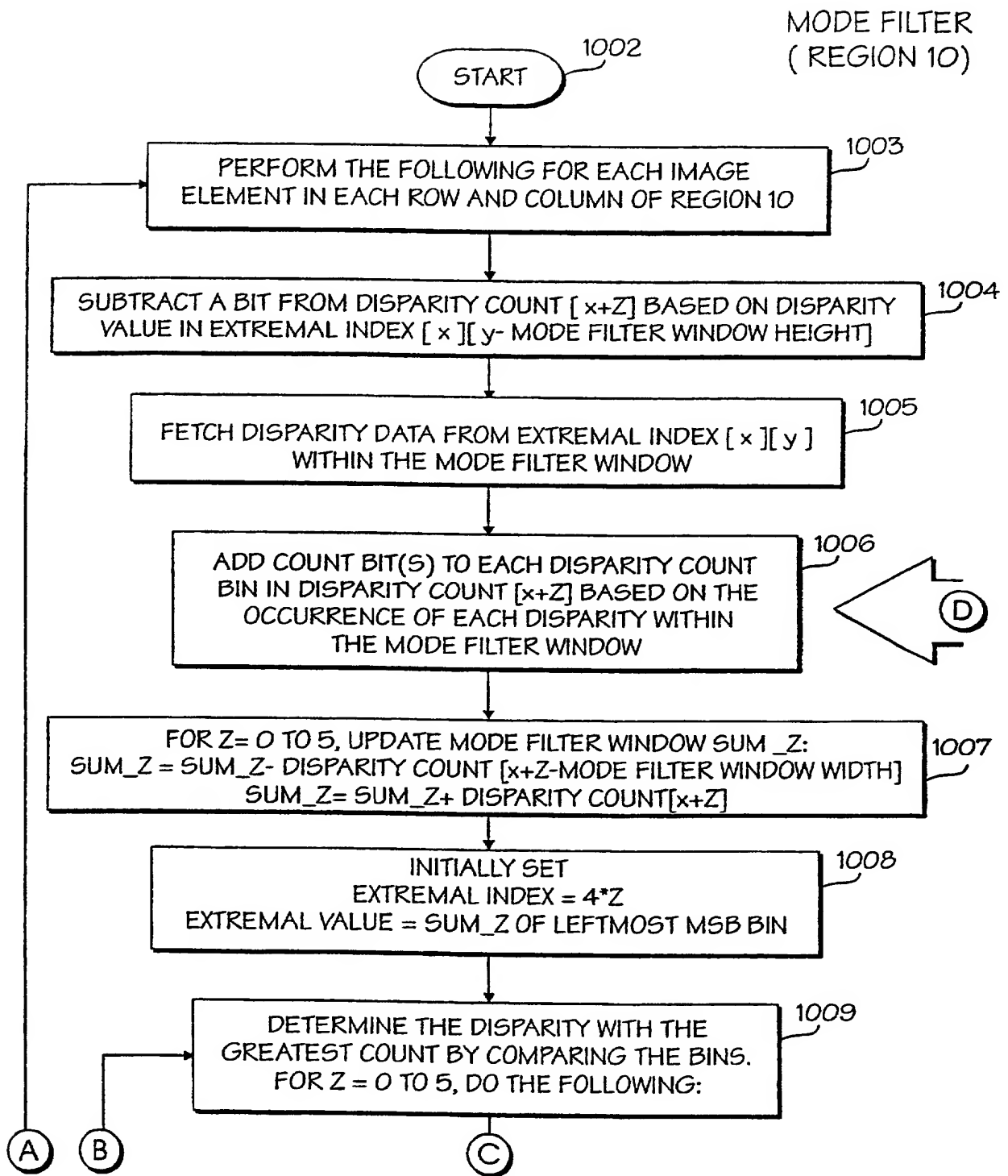


FIG. 45(A)

MODE FILTER  
( REGION 10)

(D)

DISP	DISPARITY COUNT [x+Z]	1022
0	[x] <span style="border: 1px solid black;">00</span> 00 00 00	
1	[x]    00 <span style="border: 1px solid black;">00</span> 00 00	
2	[x]    00 00 <span style="border: 1px solid black;">00</span> 00	
3	[x]    00 00 00 <span style="border: 1px solid black;">00</span>	
4	[x+1] <span style="border: 1px solid black;">00</span> 00 00 00	
5	[x+1] 00 <span style="border: 1px solid black;">00</span> 00 00	
6	[x+1] 00 00 <span style="border: 1px solid black;">00</span> 00	
7	[x+1] 00 00 00 <span style="border: 1px solid black;">00</span>	
8	[x+2] <span style="border: 1px solid black;">00</span> 00 00 00	
9	[x+2] 00 <span style="border: 1px solid black;">00</span> 00 00	
10	[x+2] 00 00 <span style="border: 1px solid black;">00</span> 00	
11	[x+2] 00 00 00 <span style="border: 1px solid black;">00</span>	
12	[x+3] <span style="border: 1px solid black;">00</span> 00 00 00	
13	[x+3] 00 <span style="border: 1px solid black;">00</span> 00 00	
14	[x+3] 00 00 <span style="border: 1px solid black;">00</span> 00	
15	[x+3] 00 00 00 <span style="border: 1px solid black;">00</span>	
16	[x+4] <span style="border: 1px solid black;">00</span> 00 00 00	
17	[x+4] 00 <span style="border: 1px solid black;">00</span> 00 00	
18	[x+4] 00 00 <span style="border: 1px solid black;">00</span> 00	
19	[x+4] 00 00 00 <span style="border: 1px solid black;">00</span>	
20	[x+5] <span style="border: 1px solid black;">00</span> 00 00 00	
21	[x+5] 00 <span style="border: 1px solid black;">00</span> 00 00	
22	[x+5] 00 00 <span style="border: 1px solid black;">00</span> 00	
23	[x+5] 00 00 00 <span style="border: 1px solid black;">00</span>	

FIG. 45(B)

1002036-1-1401

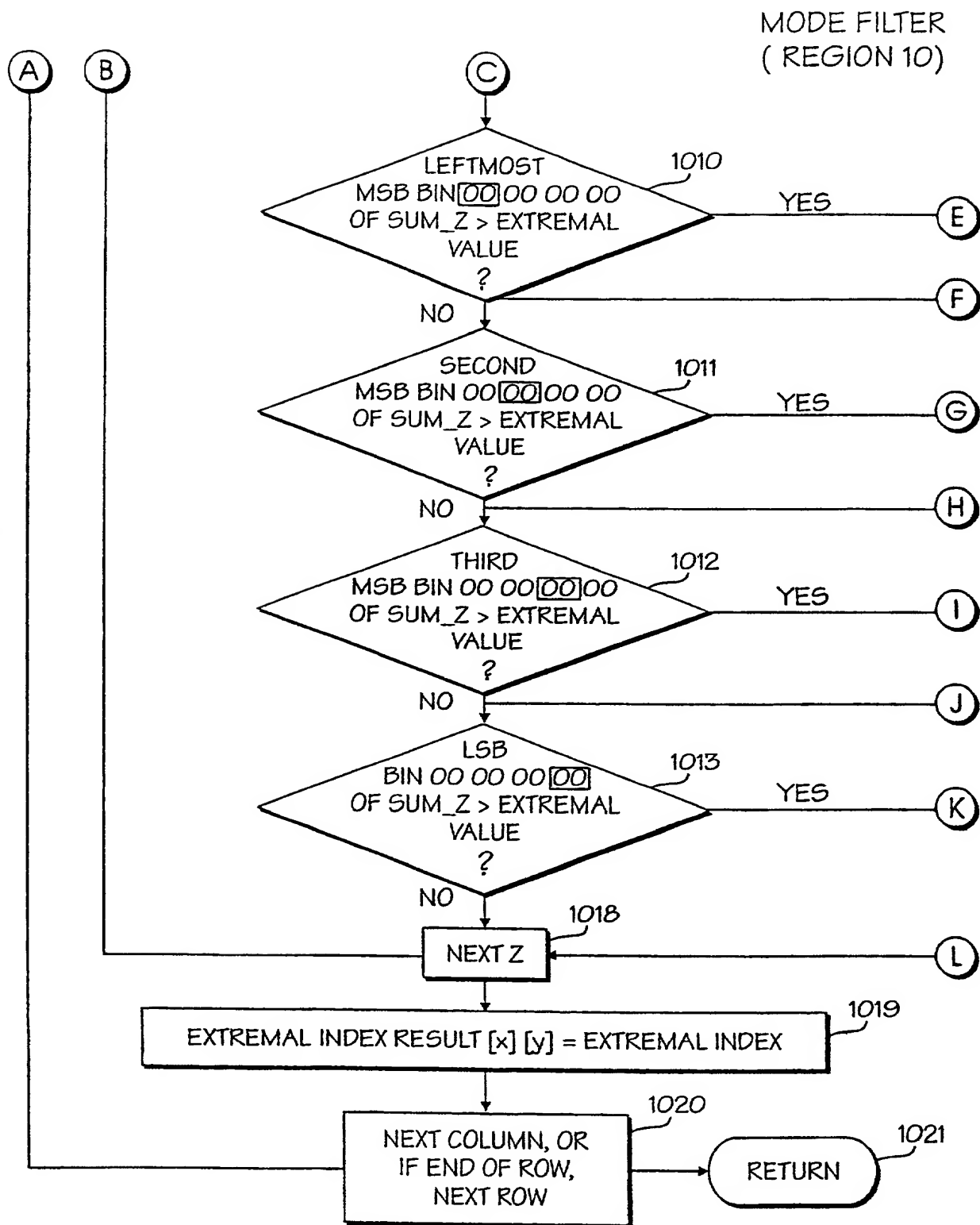


FIG. 45(C)



MODE FILTER  
( REGION 10)

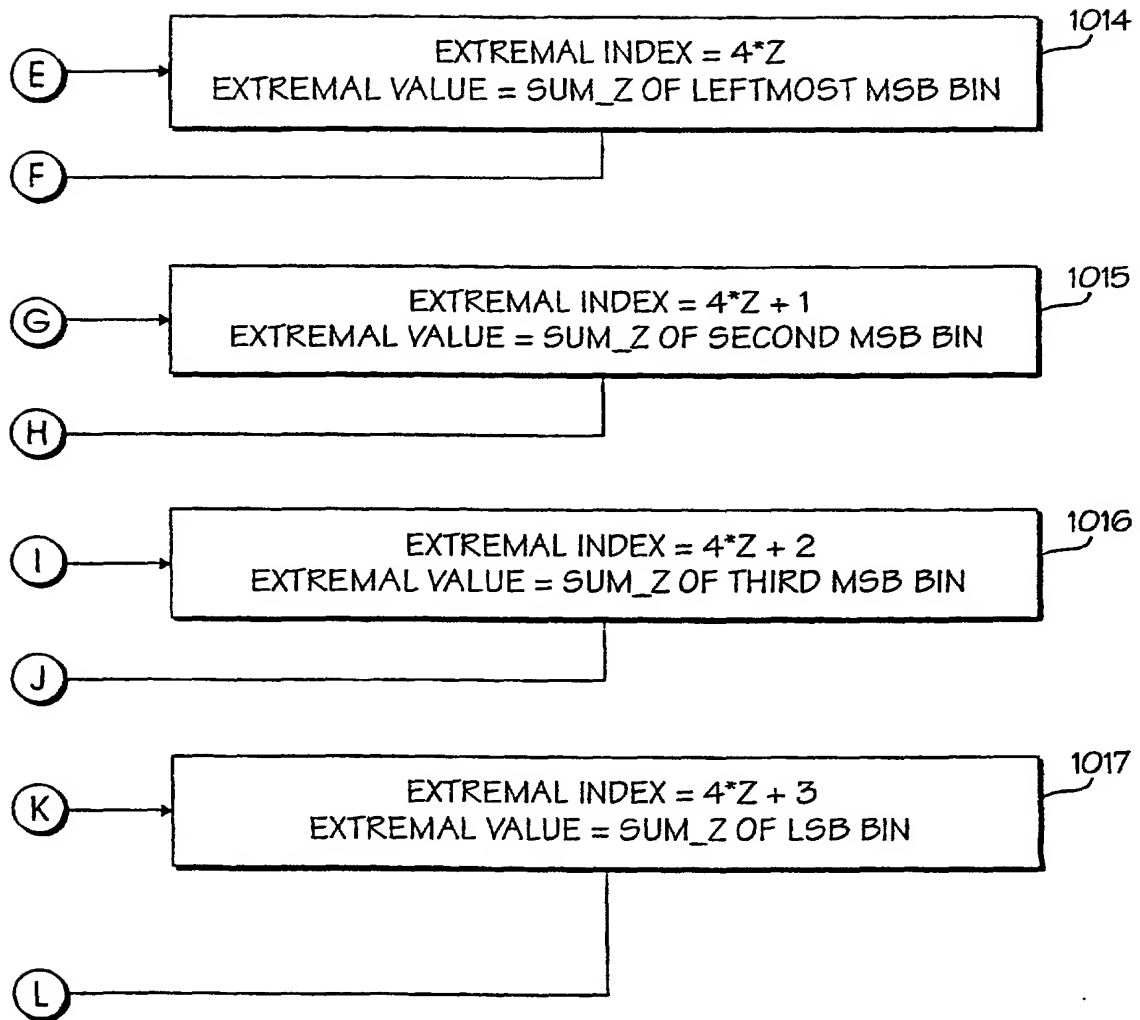


FIG. 45(D)

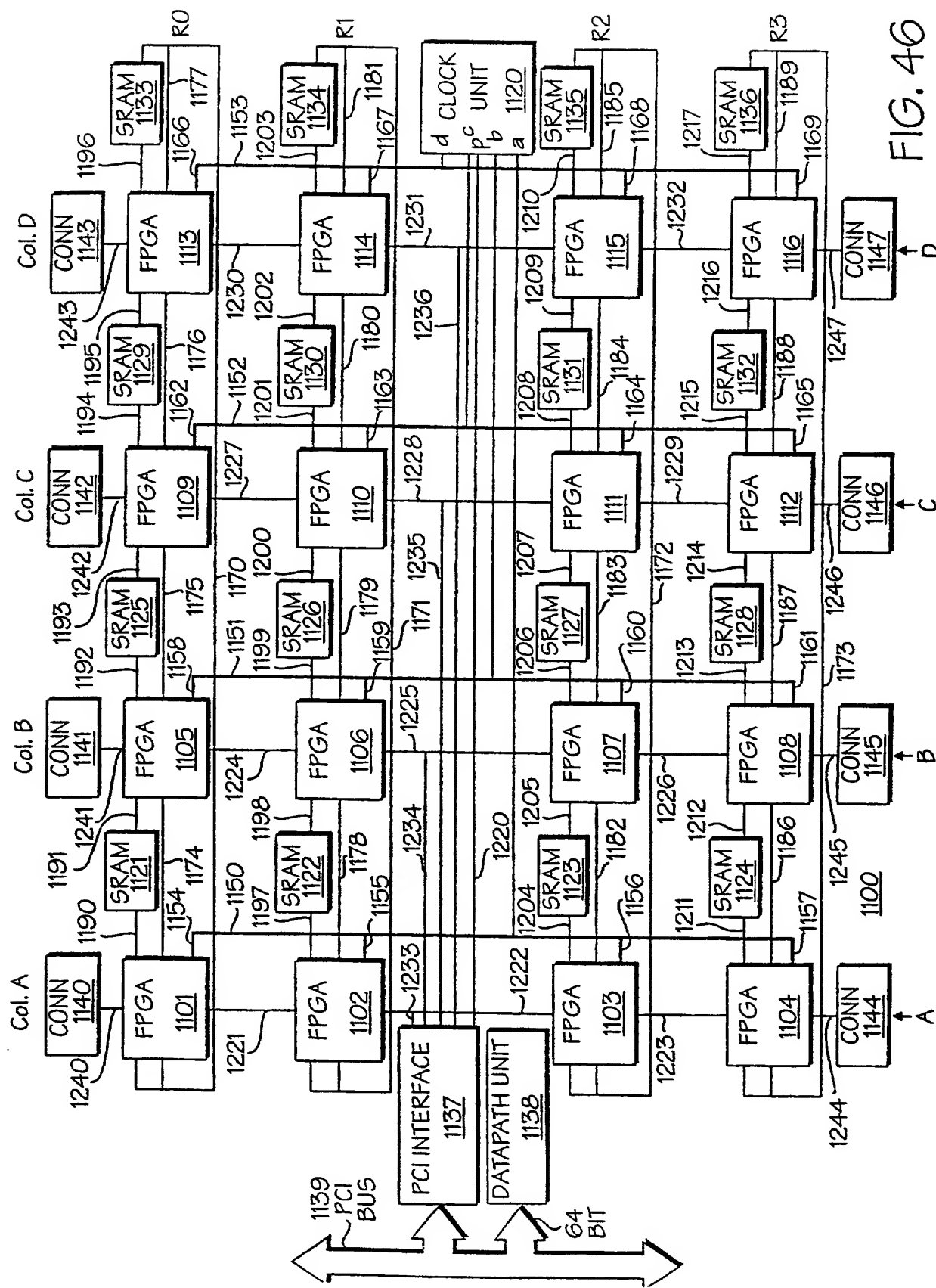


FIG. 46

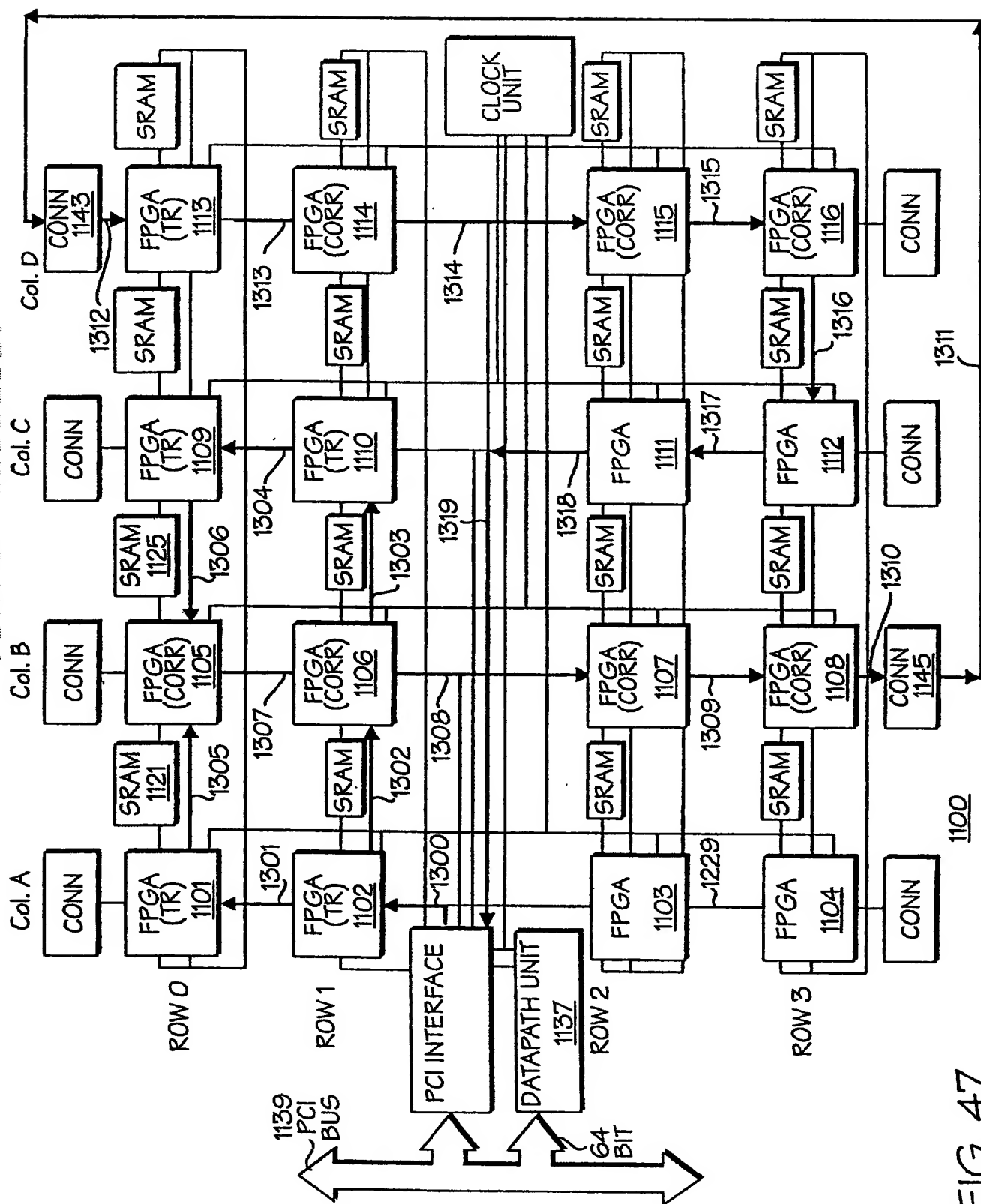


FIG. 47

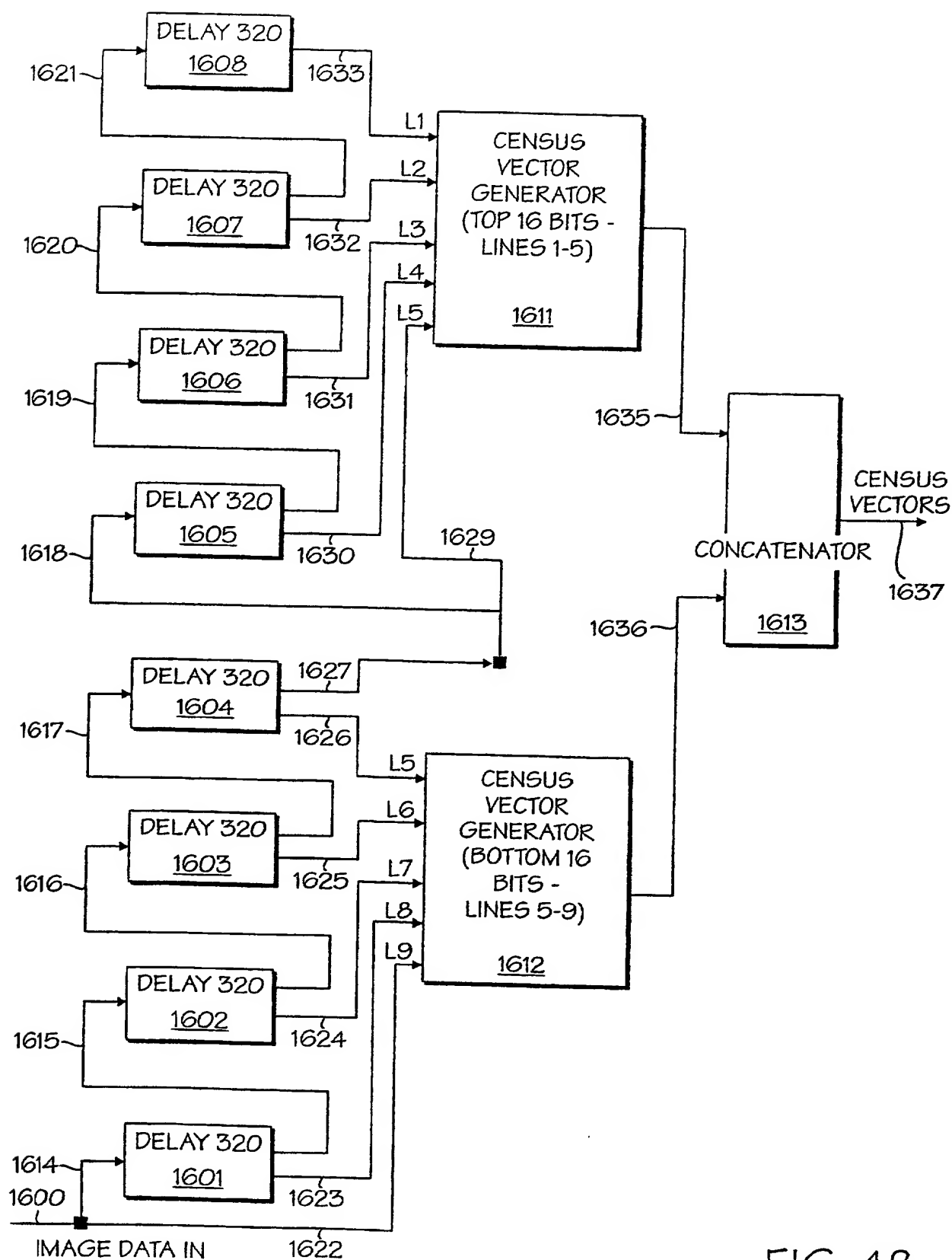


FIG. 48

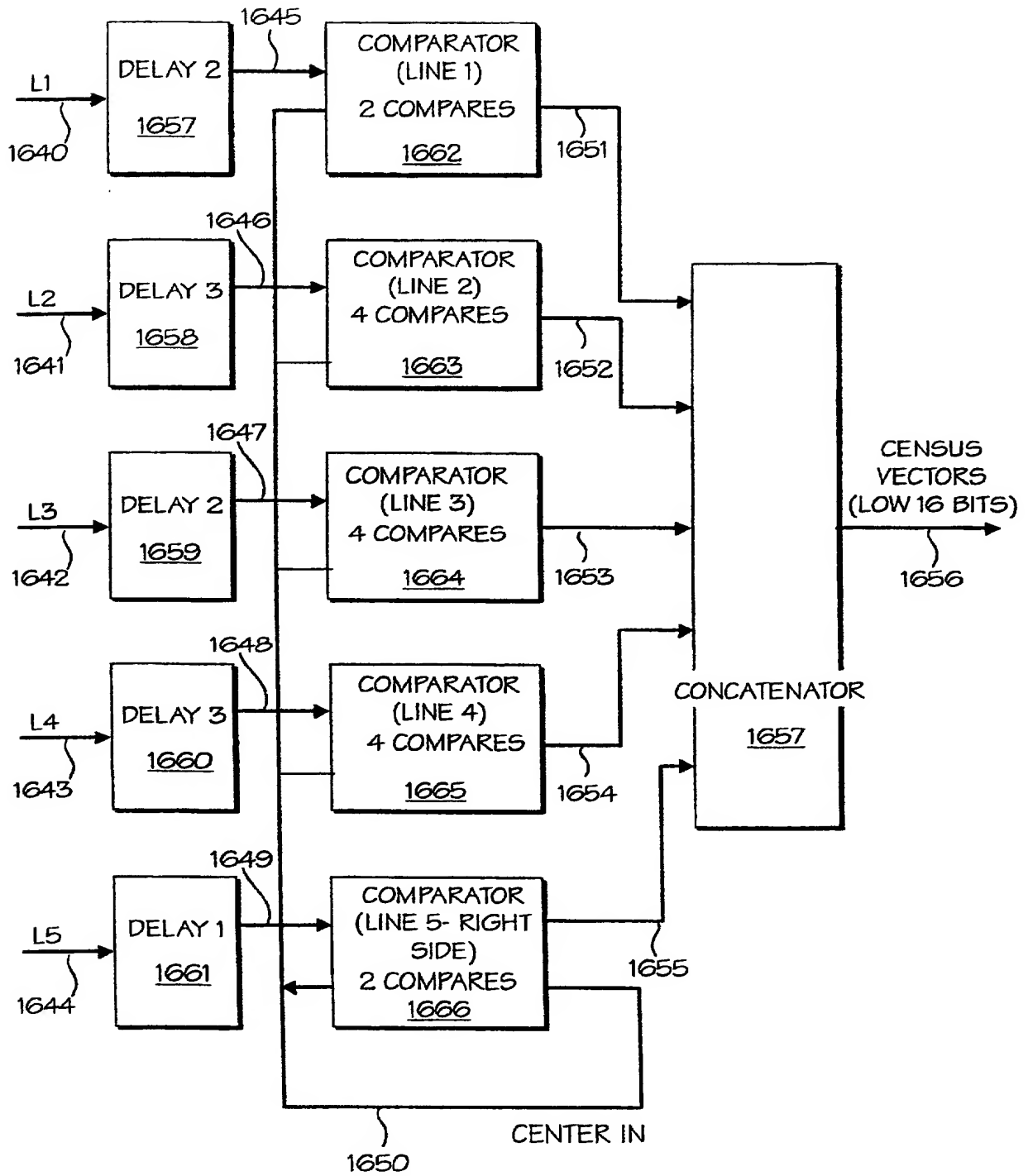


FIG. 49

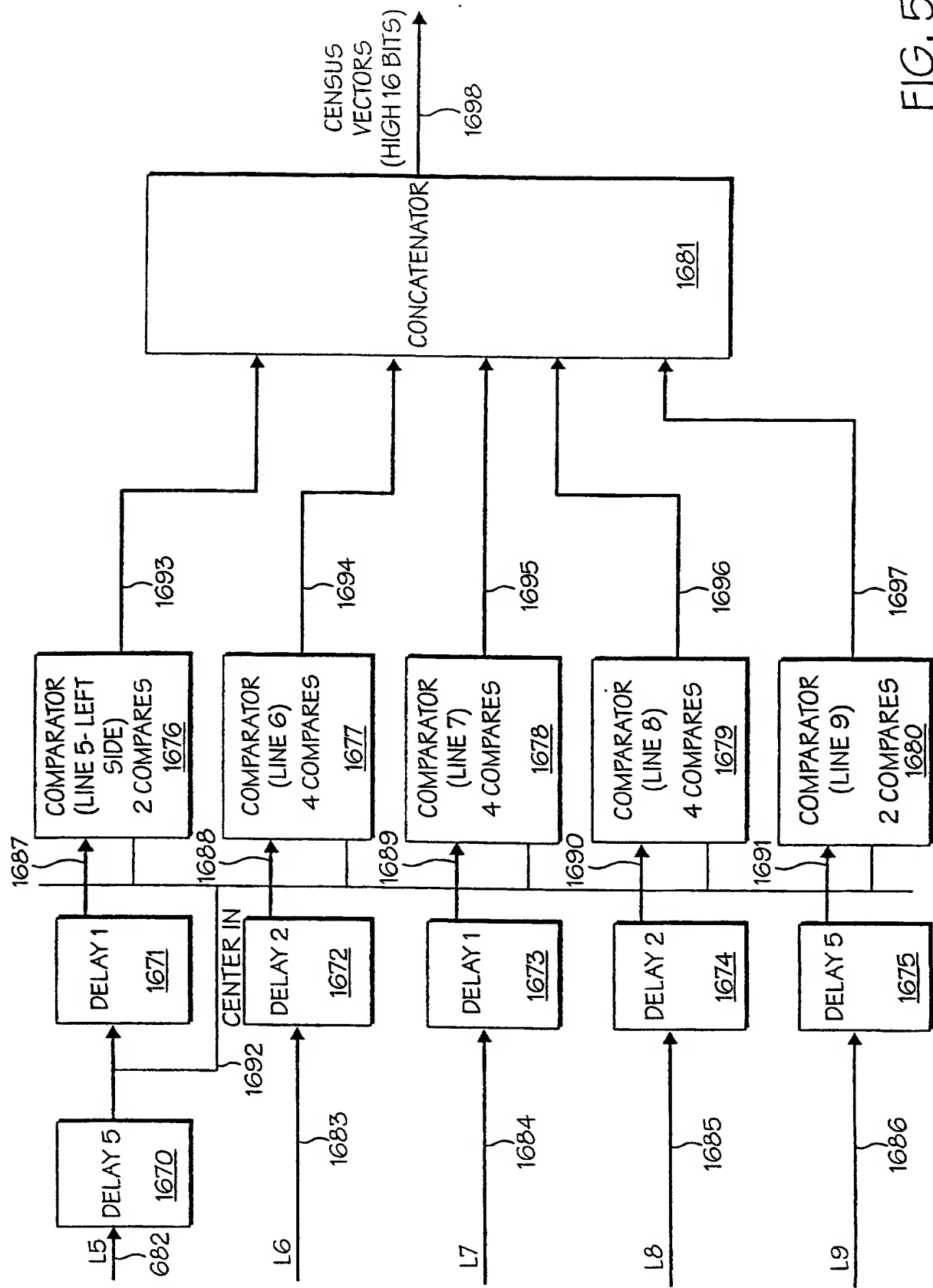


FIG. 50

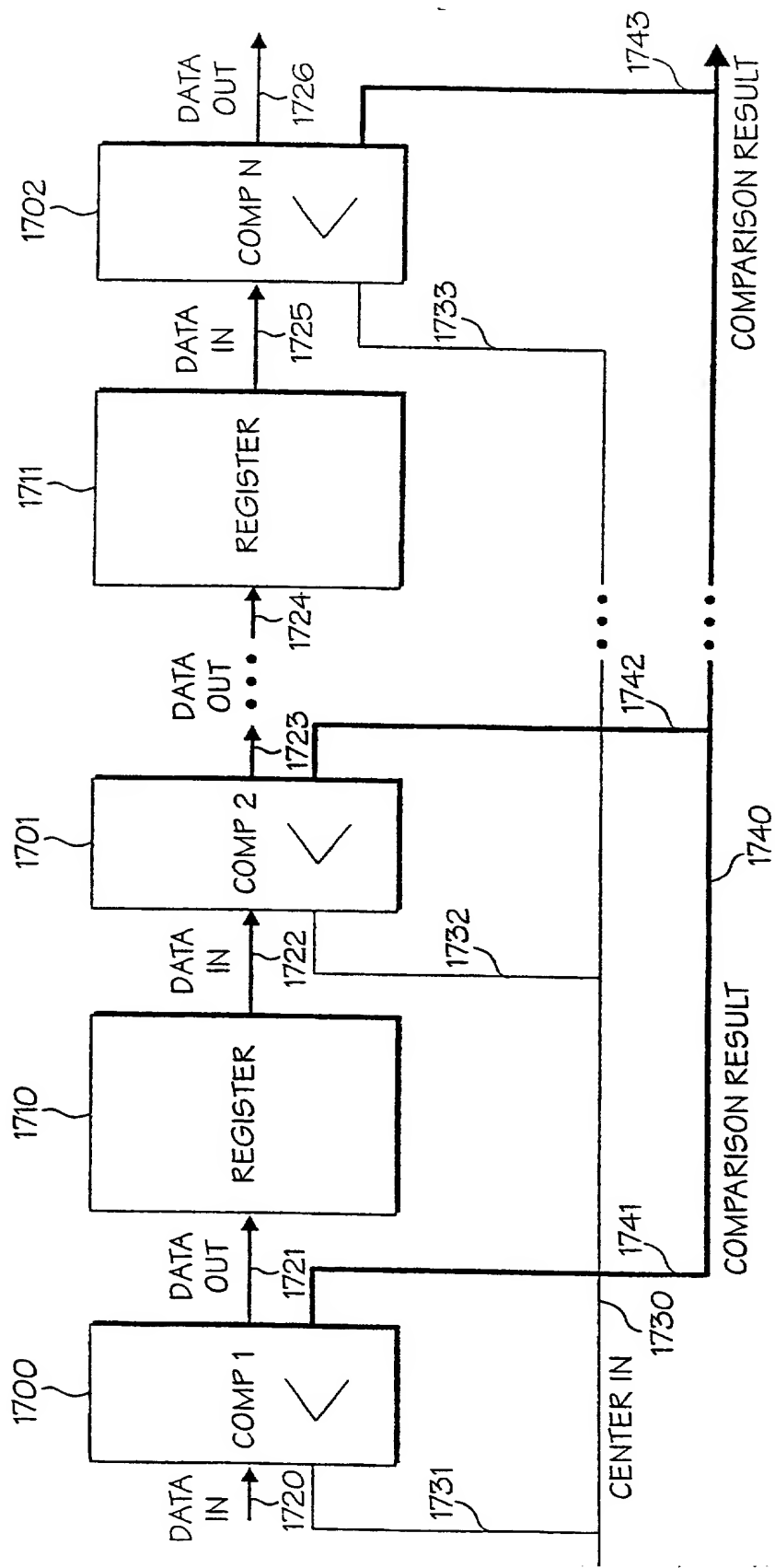


FIG. 51

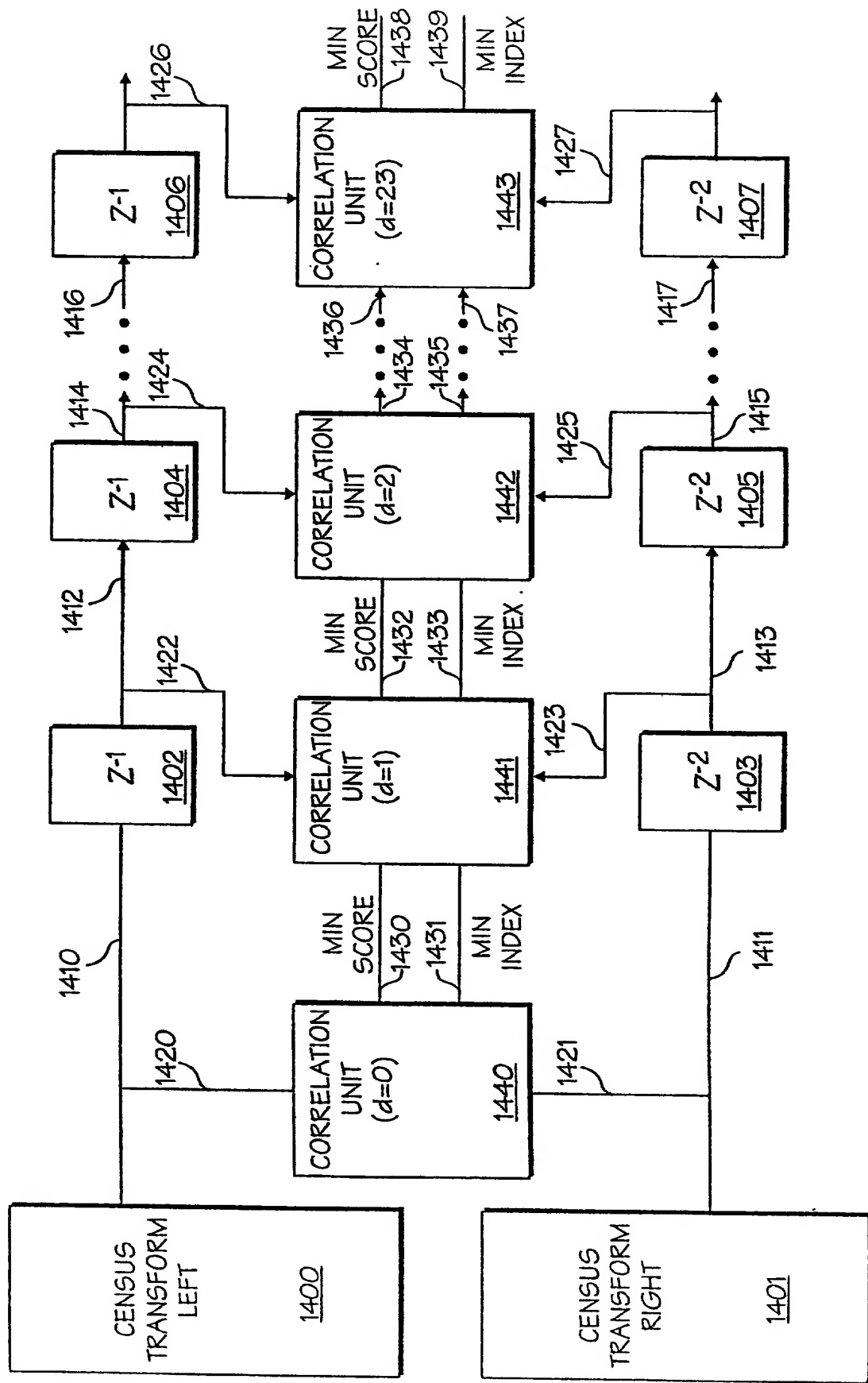


Fig. 52



CENSUS VECTORS  
LEFT IMAGE

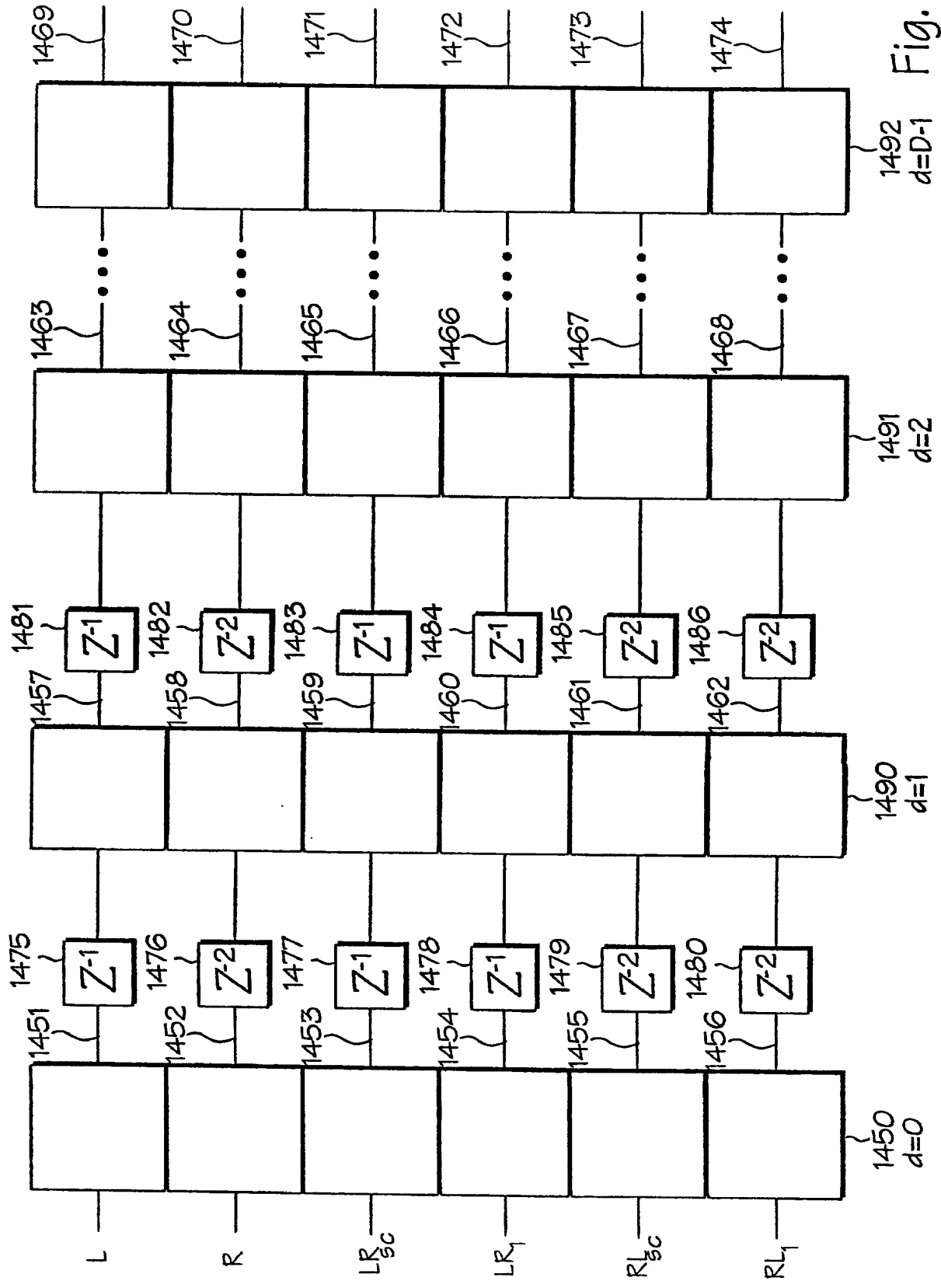
1	2	3	4	5	6	7	8	9	10	...
.										

Fig. 53(A)

CENSUS VECTORS  
RIGHT IMAGE

1'	2'	3'	4'	5'	6'	7'	8'	9'	10'	...
.										

Fig. 53(B)



15 IMAGE ELEMENTS (1 TO 15 FOR L; 1' TO 15' FOR R)

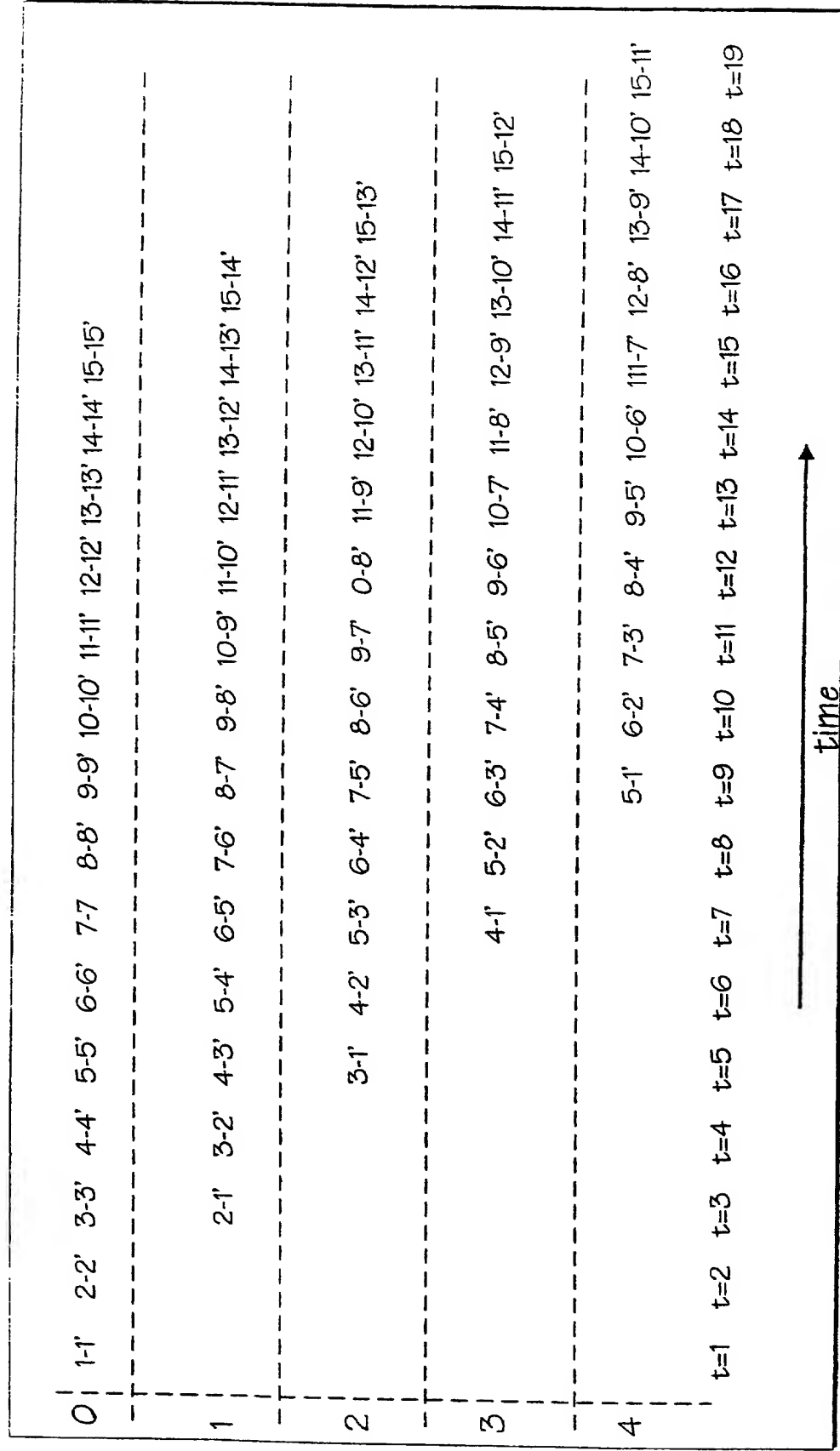
$$D=5 \ (d=\{0,1,2,3,4\})$$


FIG. 55

$LR_i$		$RL_i$	
$LR_i(9)$	5	$RL_i(5')$	
$LR_i(8)$	4	$RL_i(4')$	
$LR_i(7)$	3	$RL_i(3')$	
$LR_i(6)$	2	$RL_i(2')$	
$LR_i(5)$	1	$RL_i(1')$	

Fig. 56(A)

$LR_i$		$RL_i$	
$LR_i(10)$	5	$RL_i(6')$	
$LR_i(9)$	4	$RL_i(5')$	
$LR_i(8)$	3	$RL_i(4')$	
$LR_i(7)$	2	$RL_i(3')$	
$LR_i(6)$	1	$RL_i(2')$	

Fig. 56(B)

$LR_i$		$RL_i$	
$LR_i(11)$	5	$RL_i(7)$	
$LR_i(10)$	4	$RL_i(6')$	
$LR_i(9)$	3	$RL_i(5')$	
$LR_i(8)$	2	$RL_i(4')$	
$LR_i(7)$	1	$RL_i(3')$	

Fig. 56(C)

$LR_i$		$RL_i$	
	10		
	9		
	8		
	7		
	6		
	5		
	4		
	3		
	2		
	1		

Fig. 56(D)

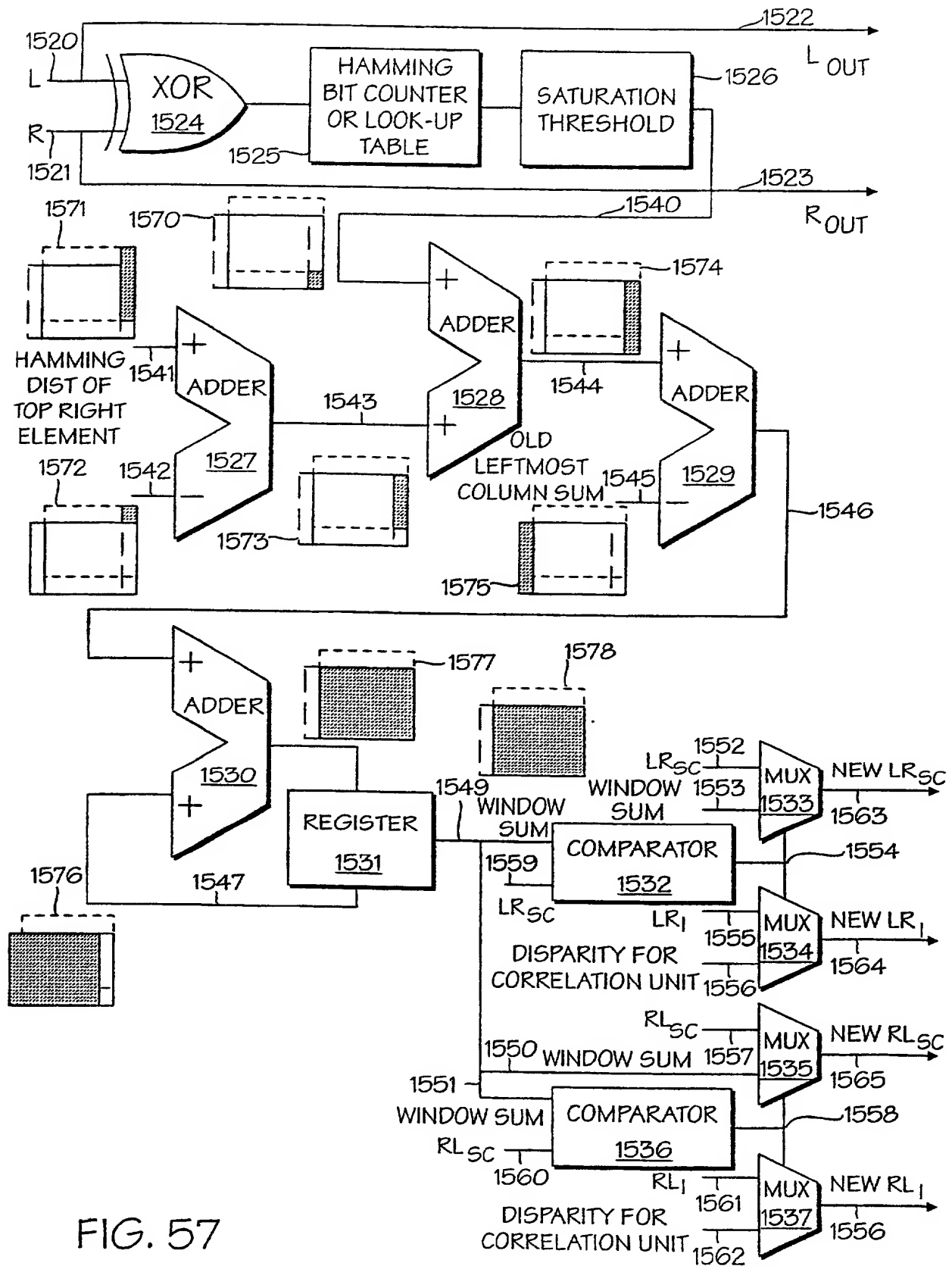


FIG. 57

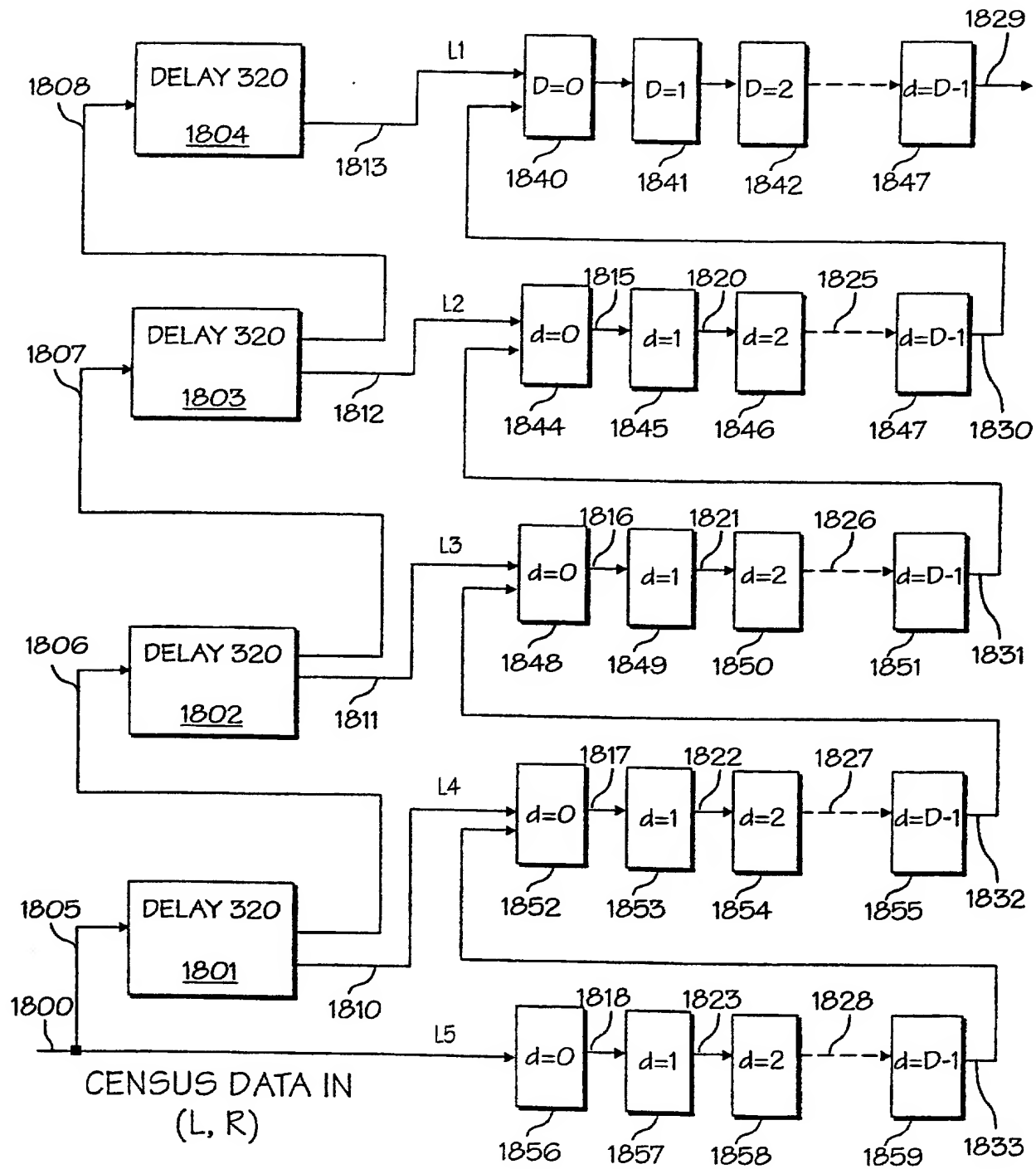


FIG. 58

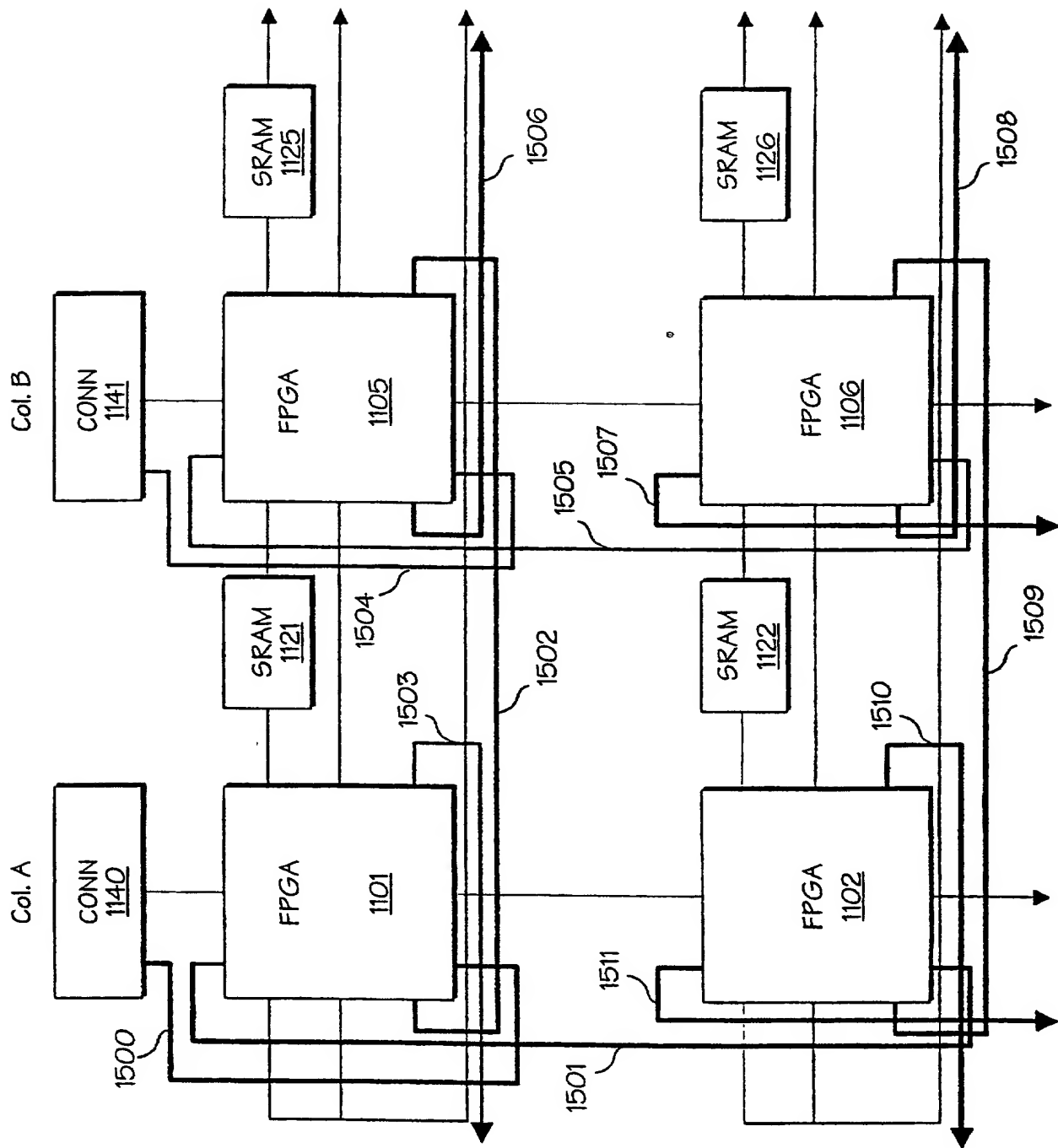


FIG. 59

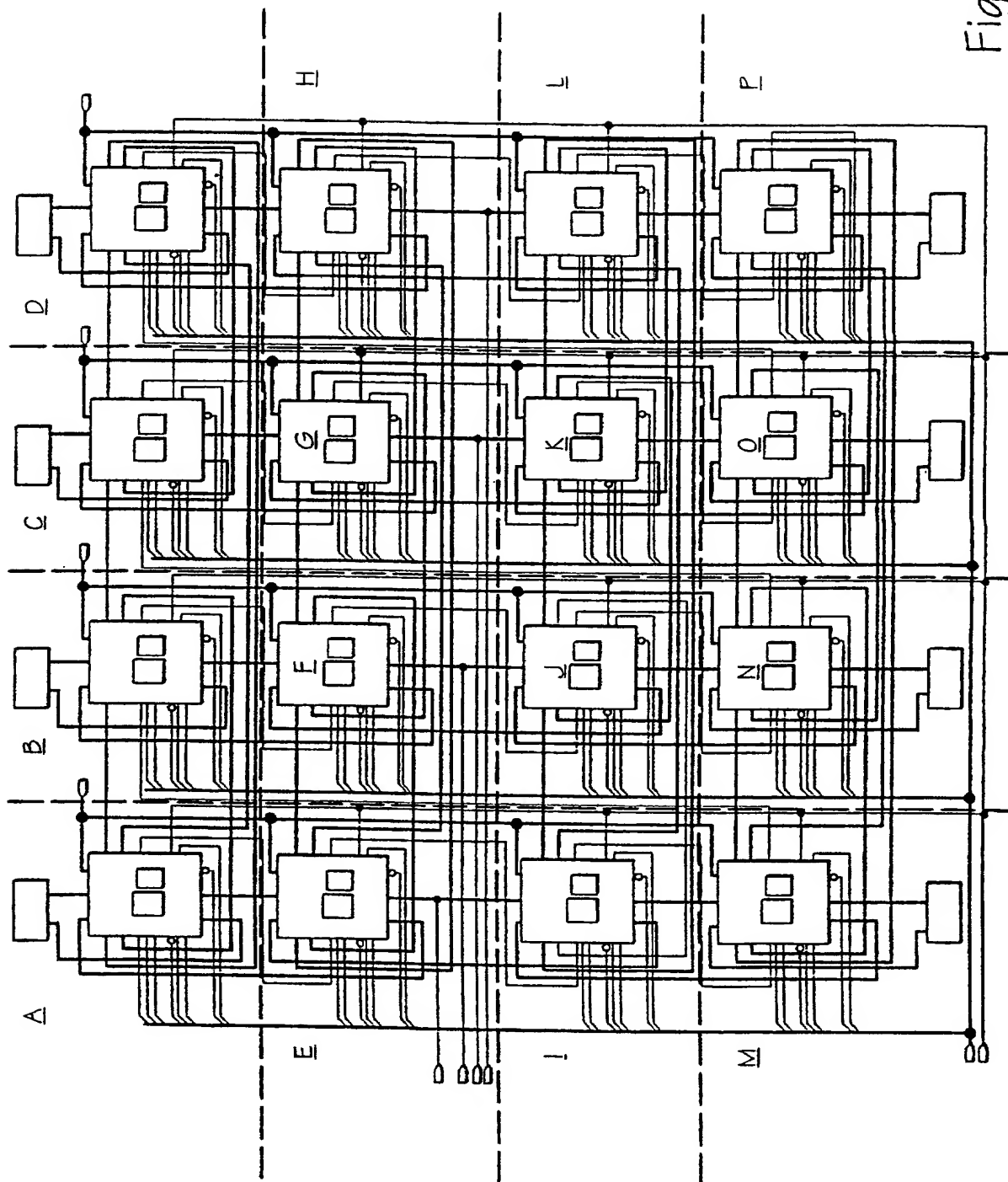
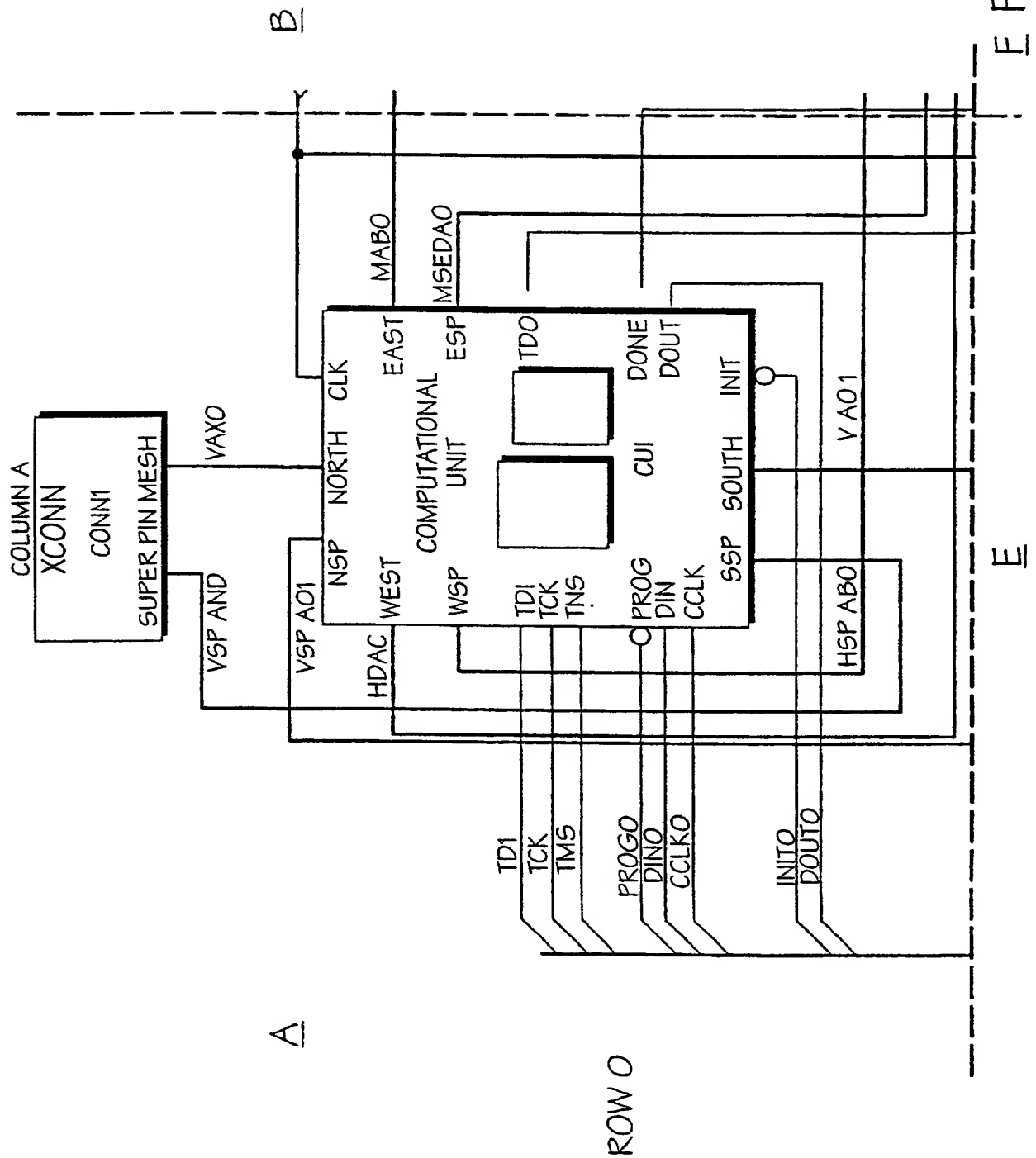


Fig. 60





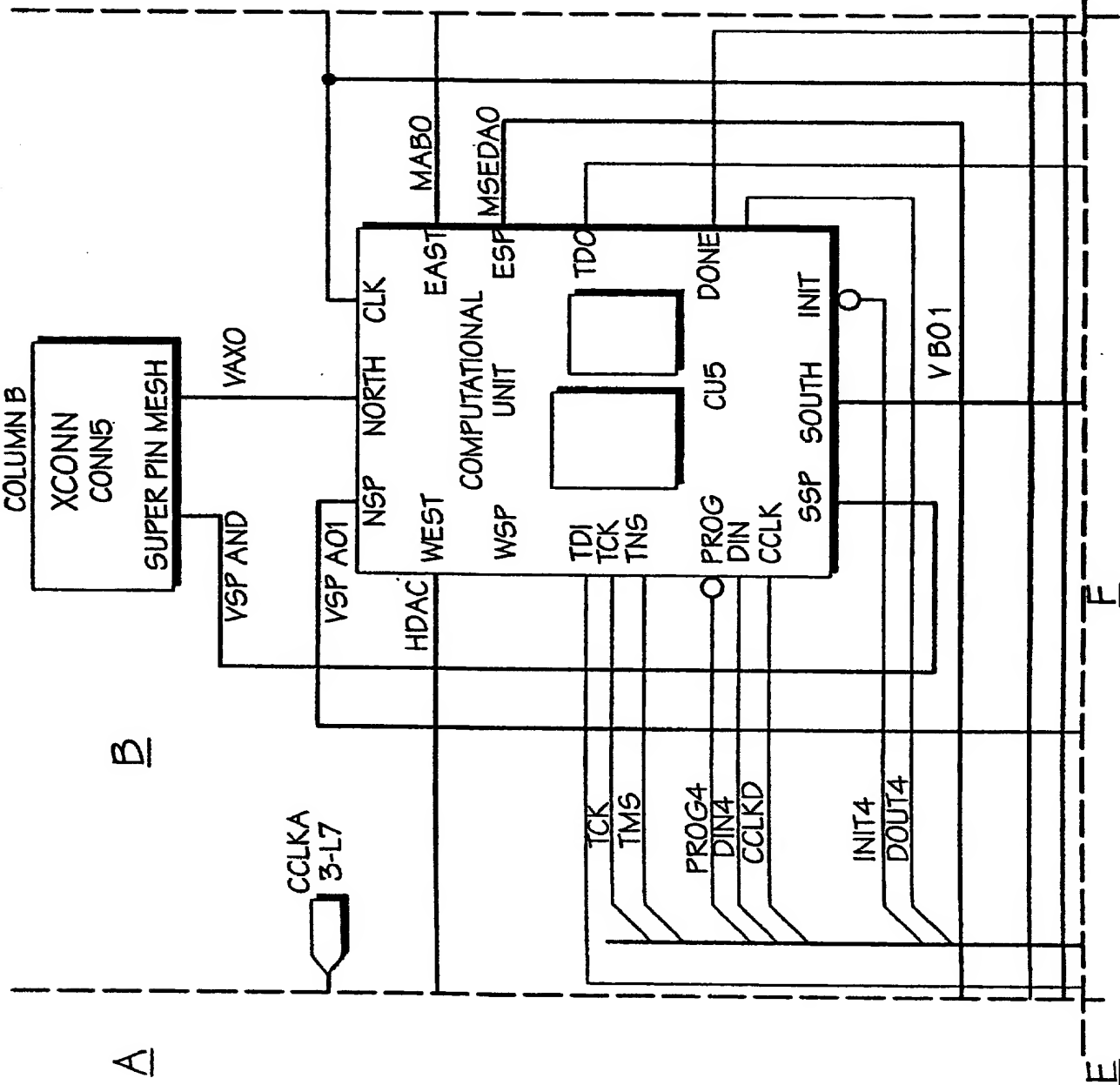


Fig. 60B

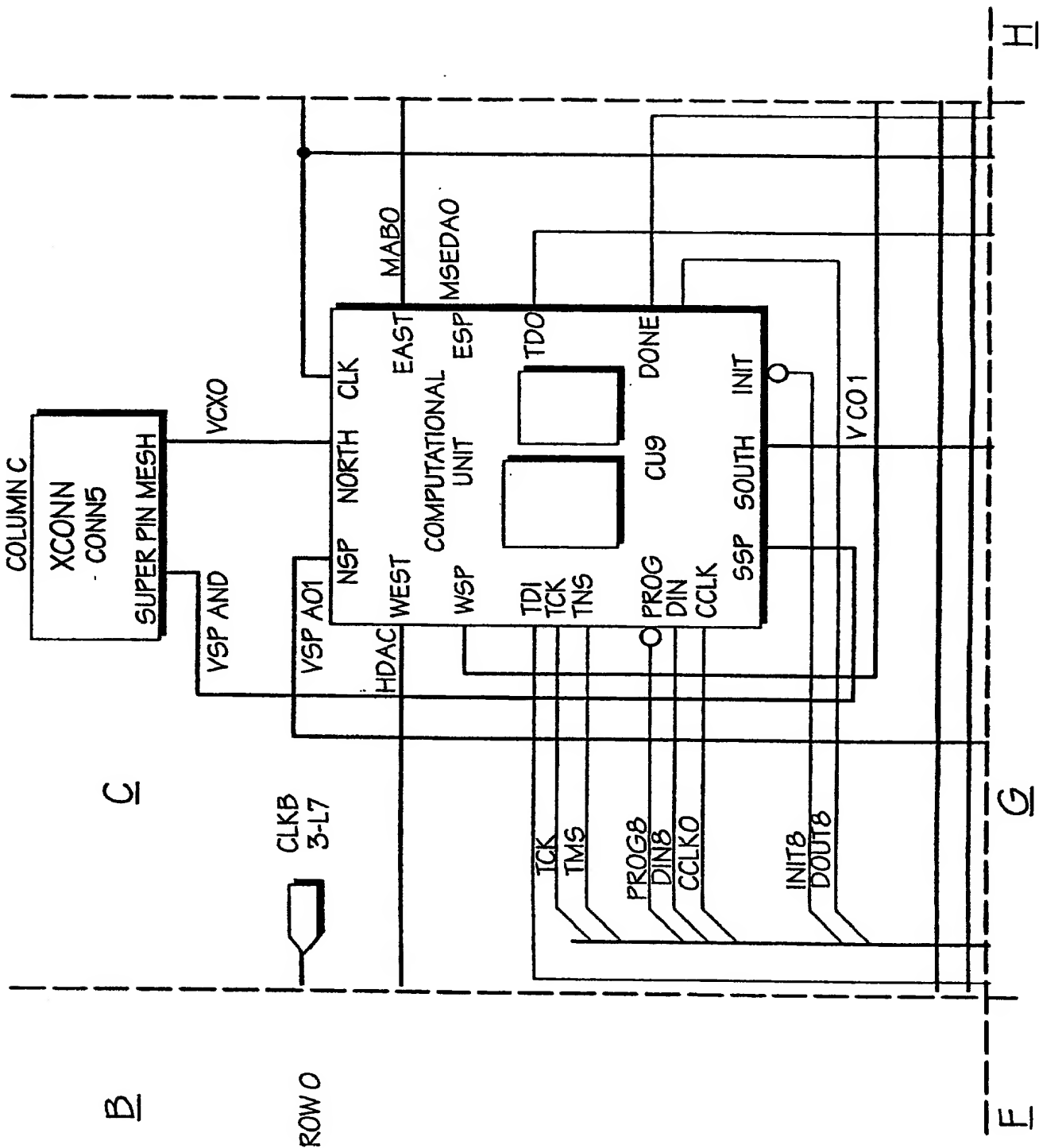


Fig. 60C

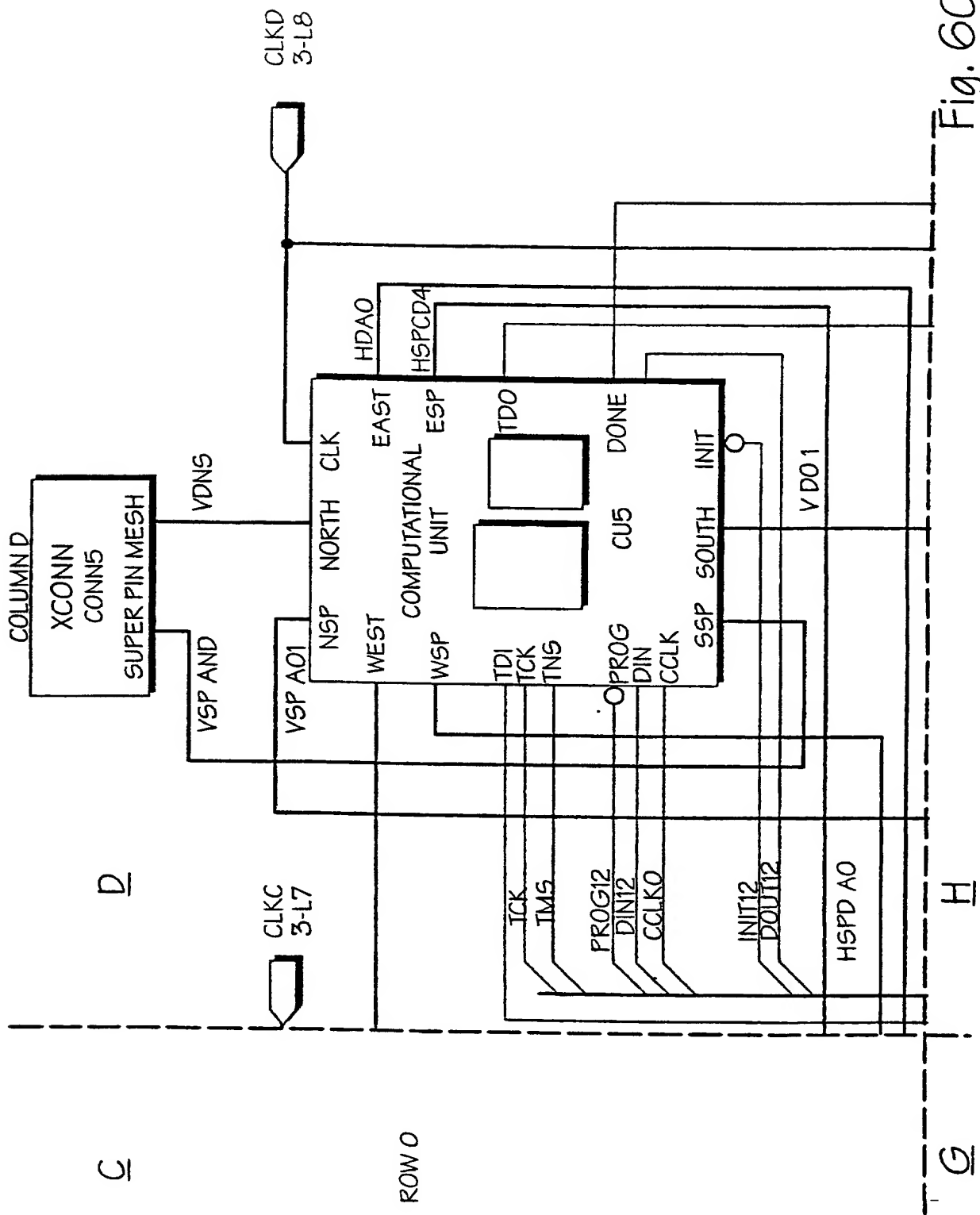


Fig. 60D

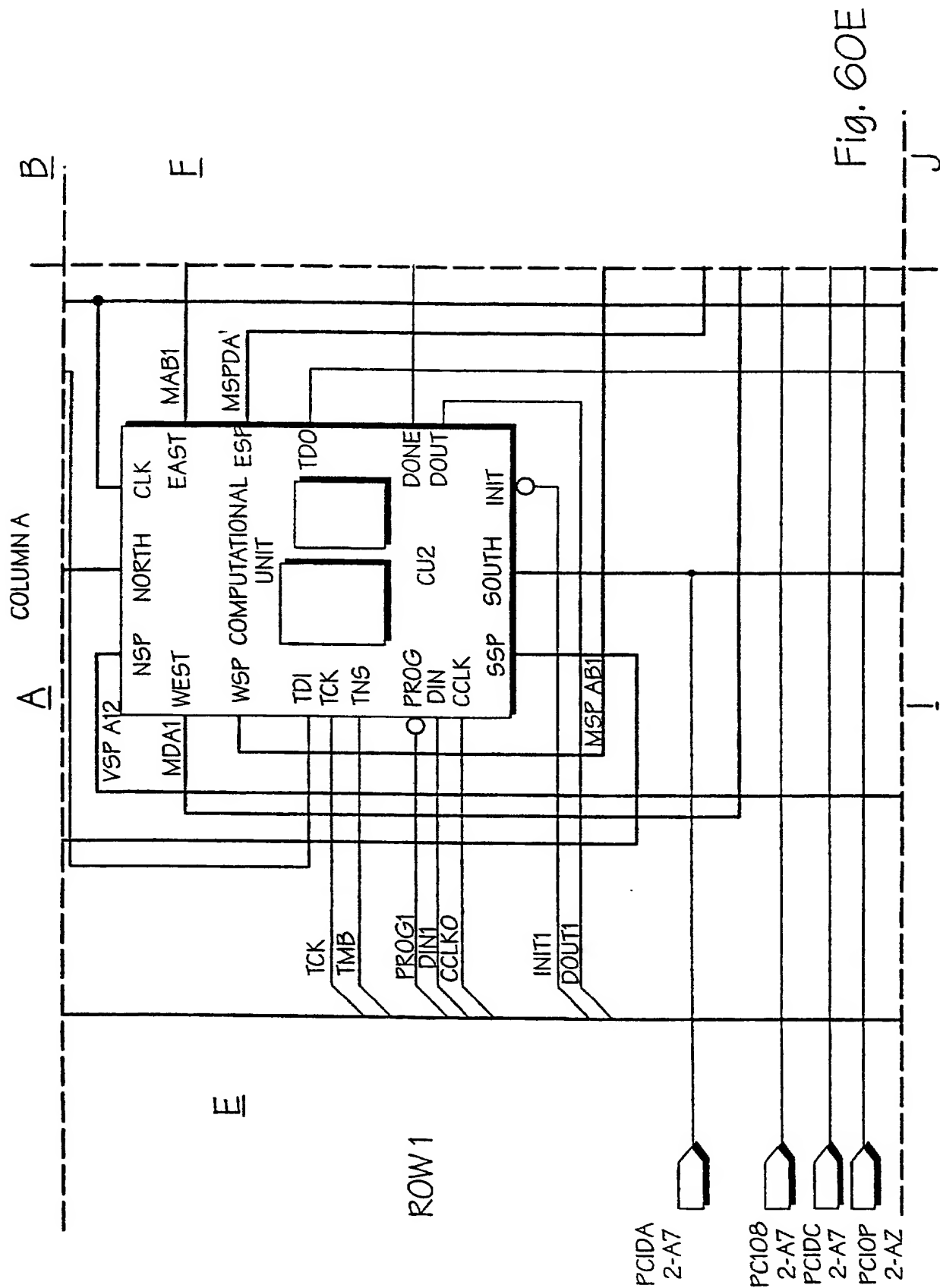


Fig. 60E

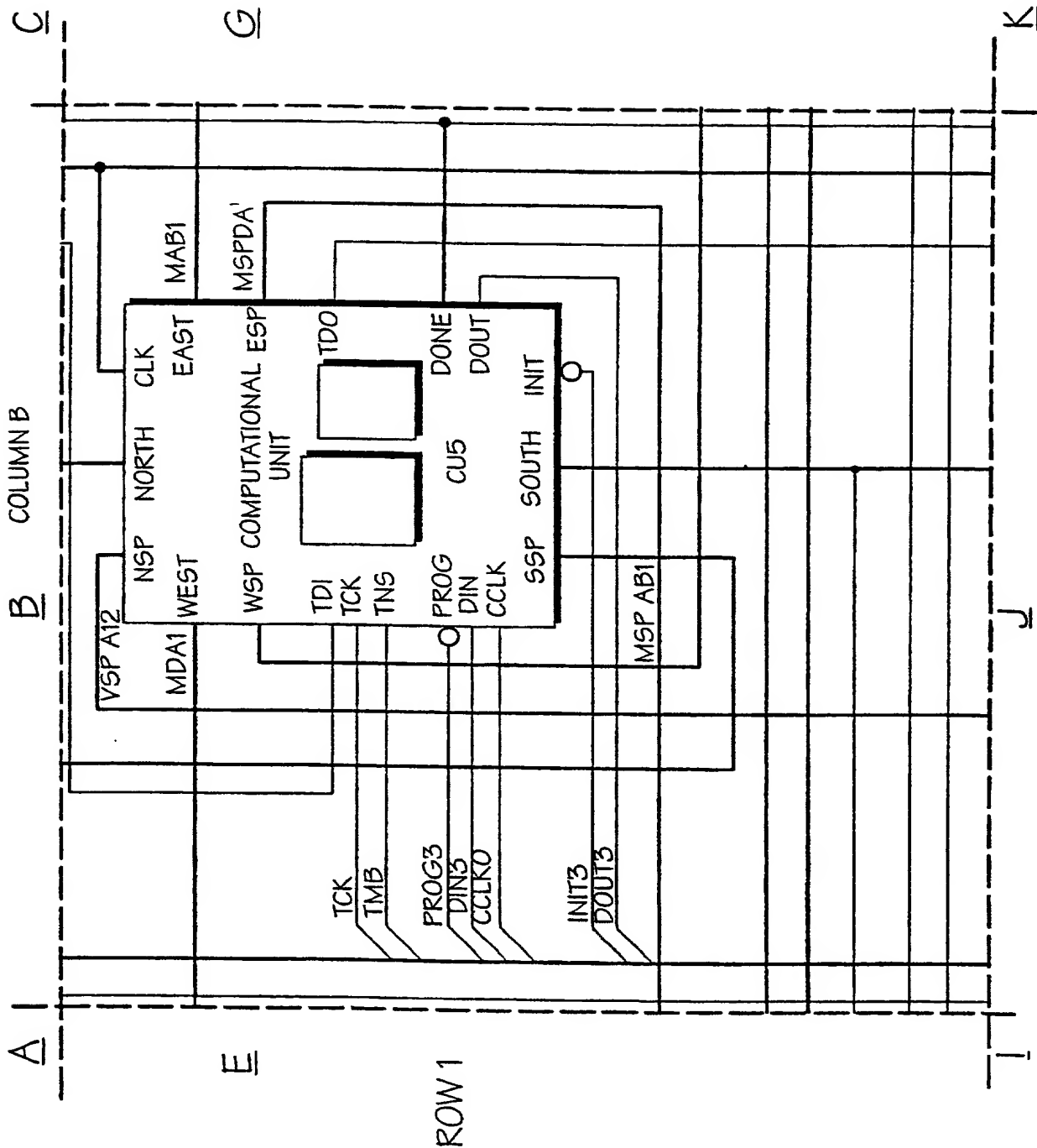


Fig. 60F

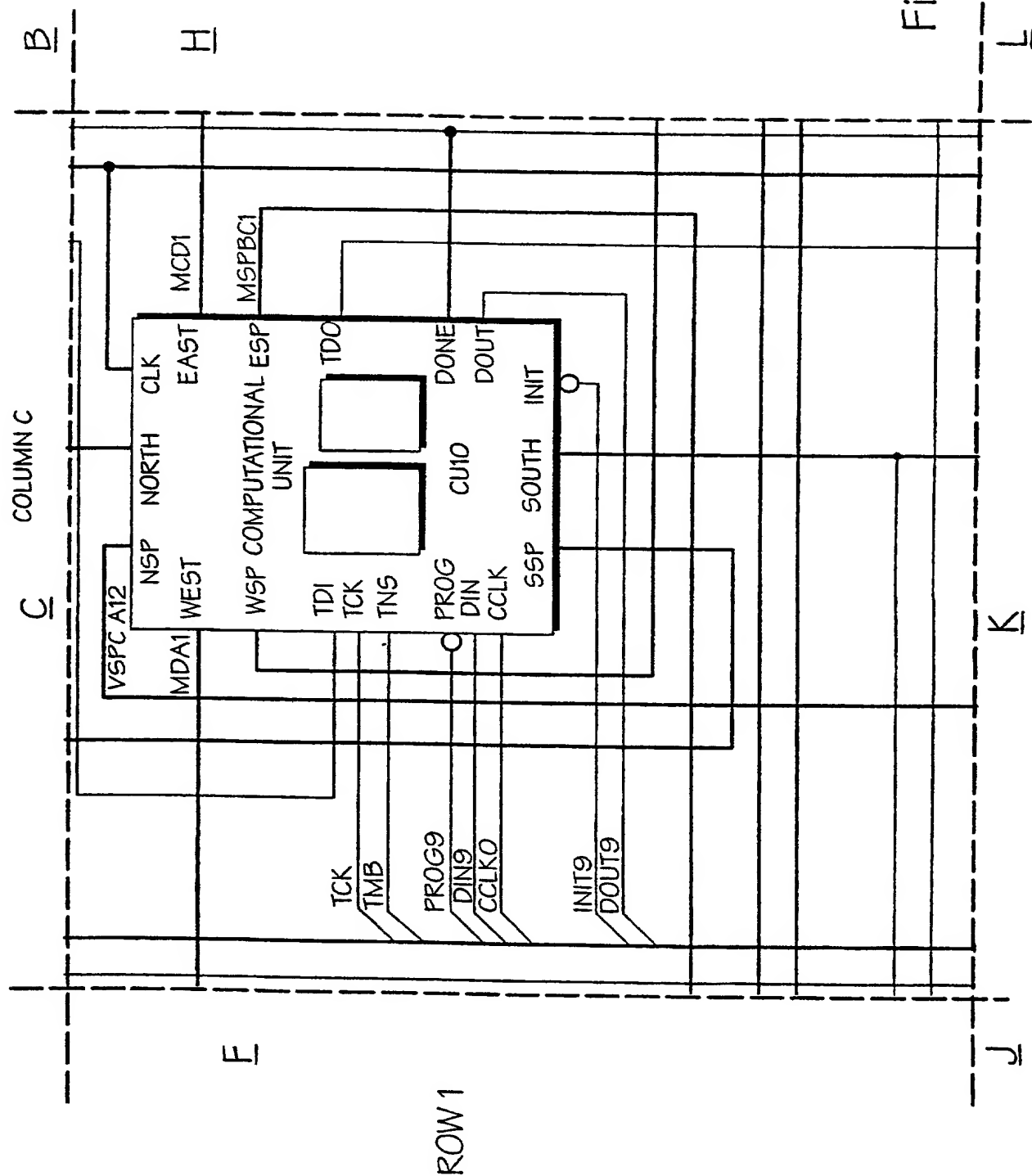


Fig. 60G

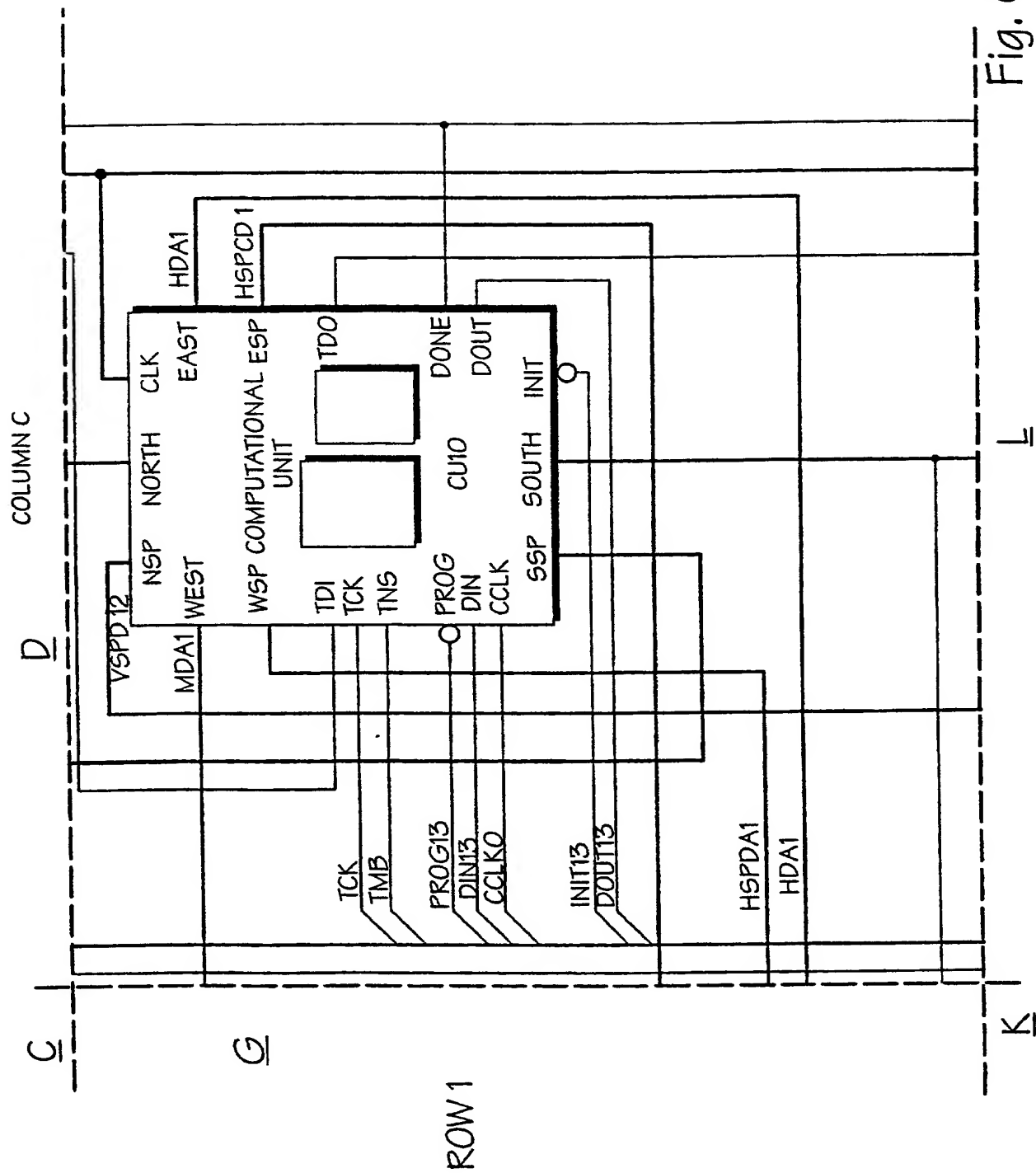


Fig. 60H



COLUMN C

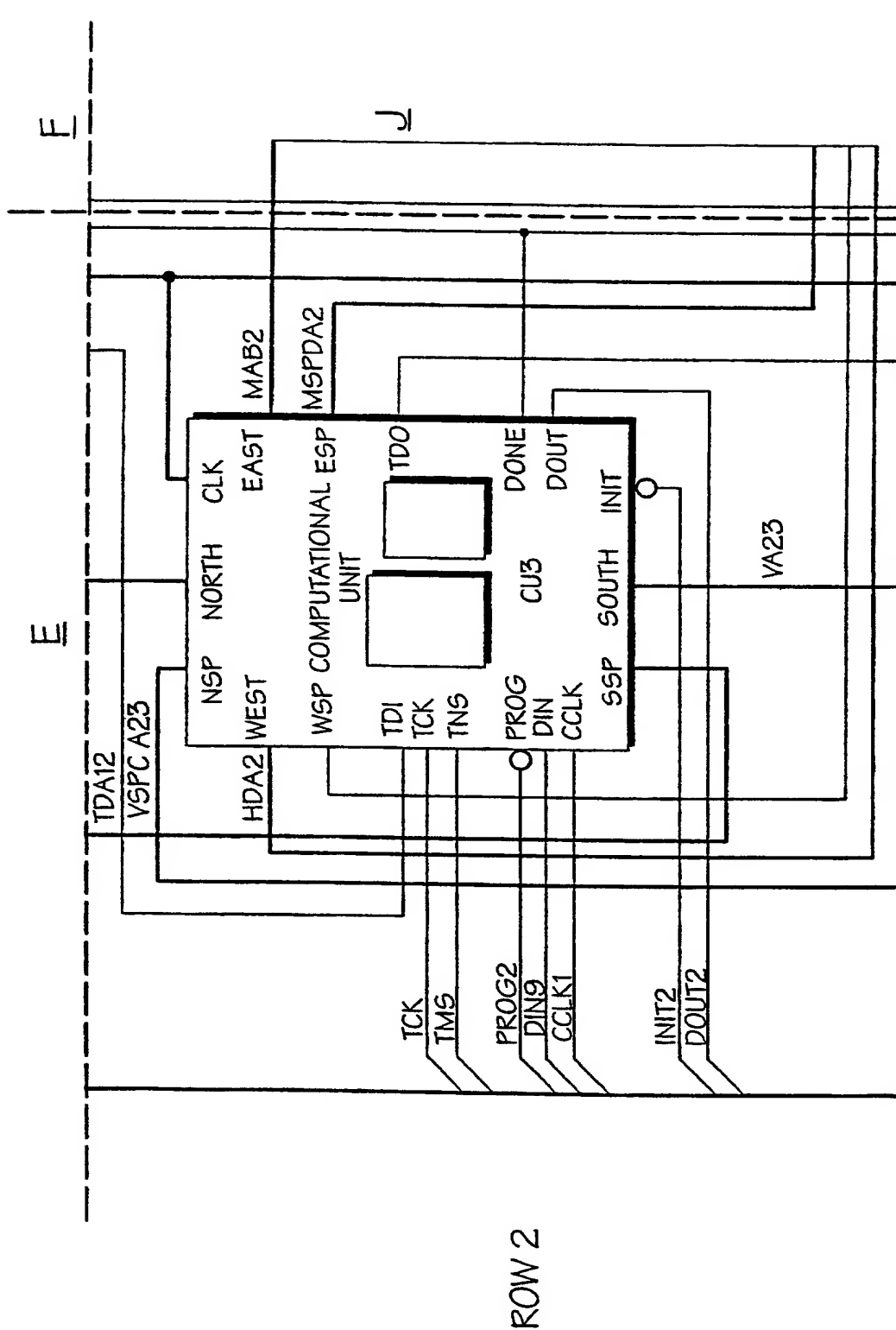
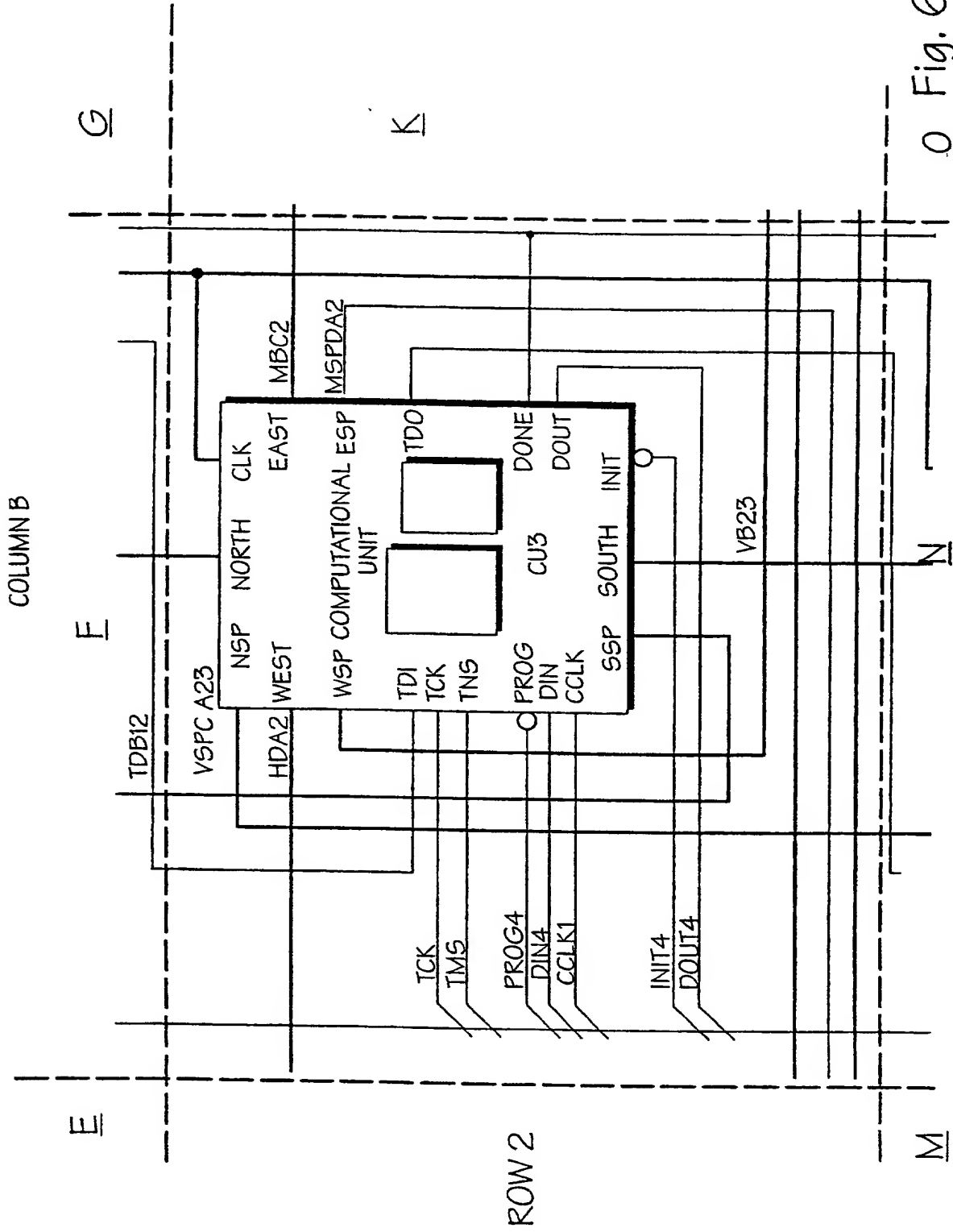


Fig. 601



COLUMN C

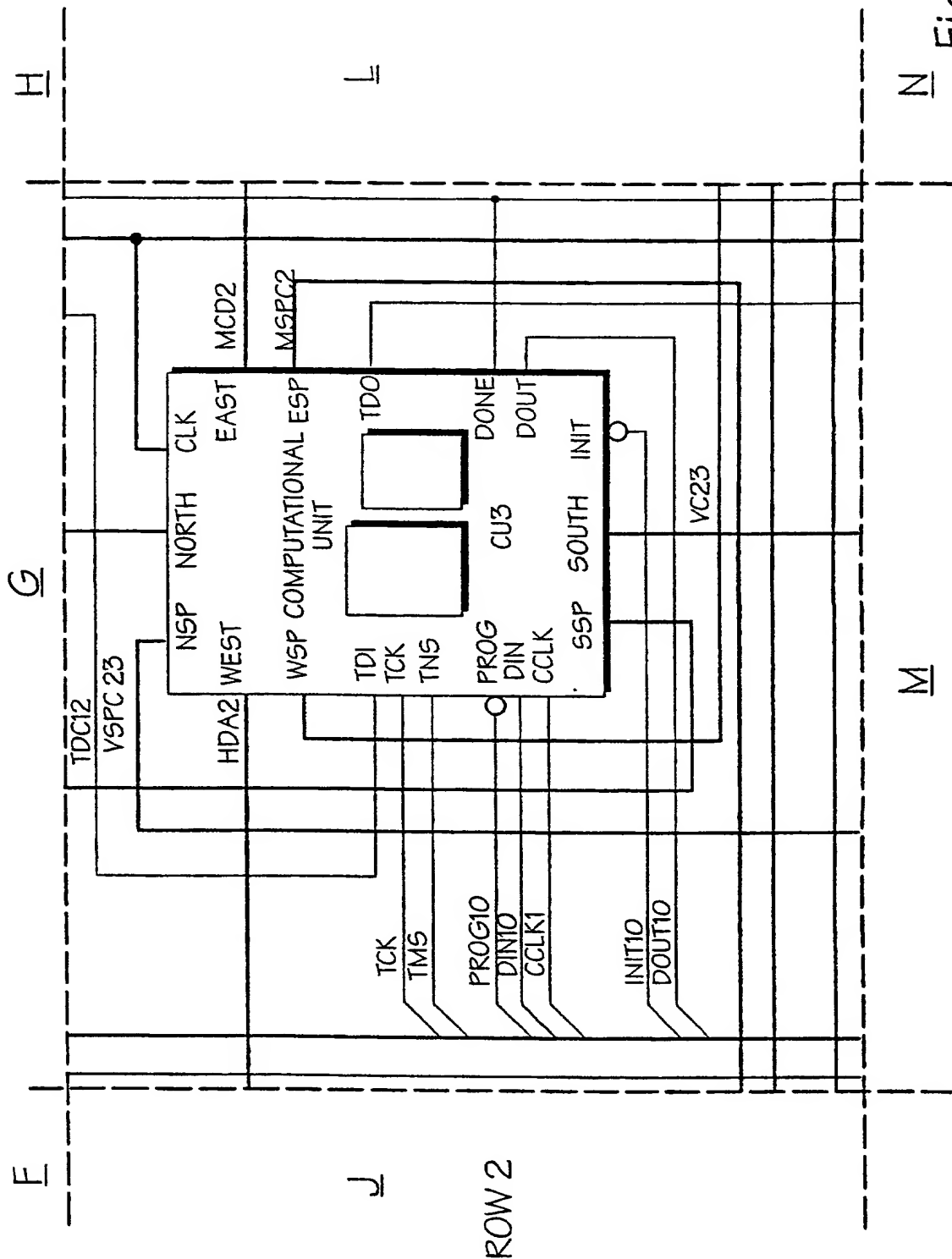


Fig. 60K

COLUMN D

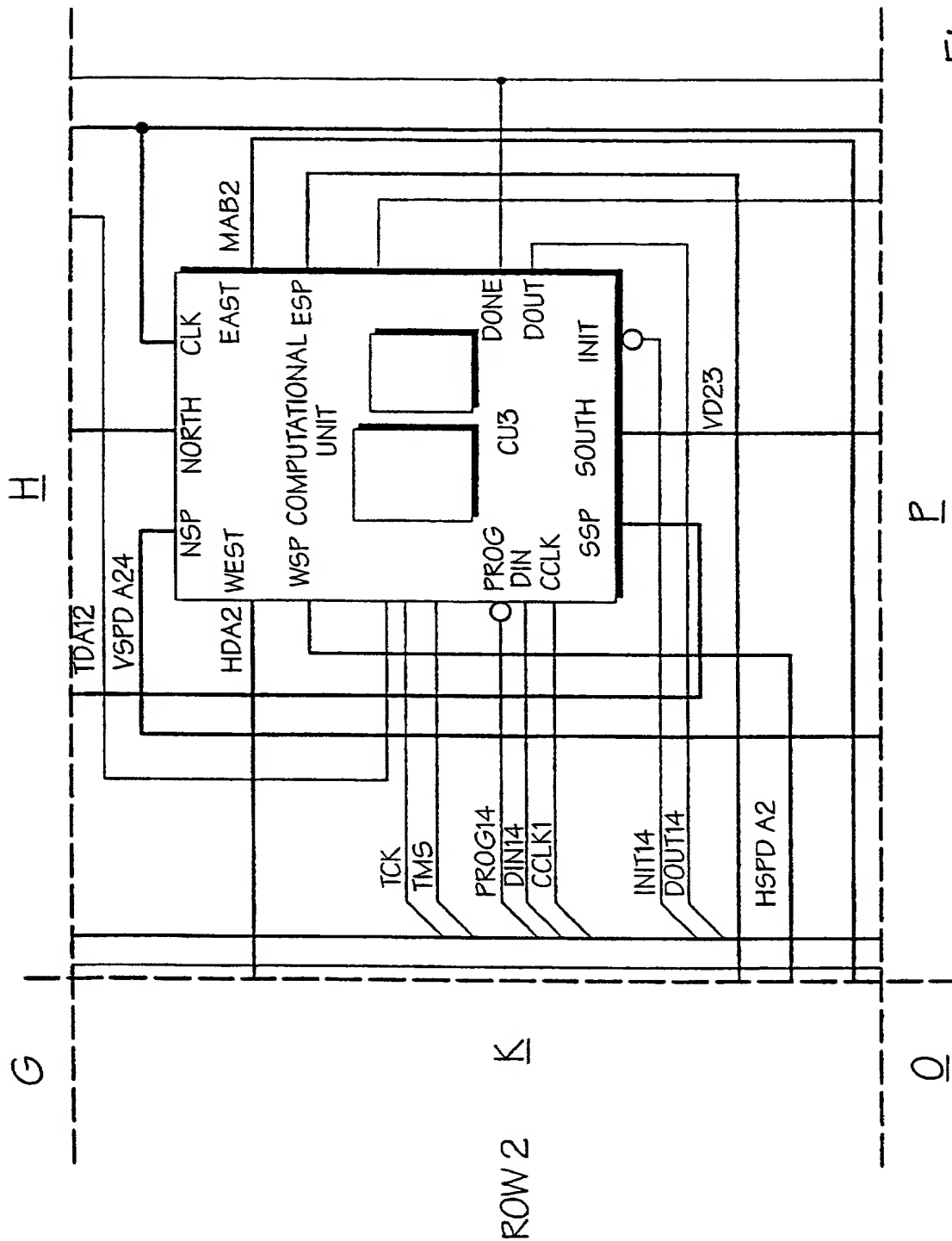


Fig. 60L

TOP SECRET

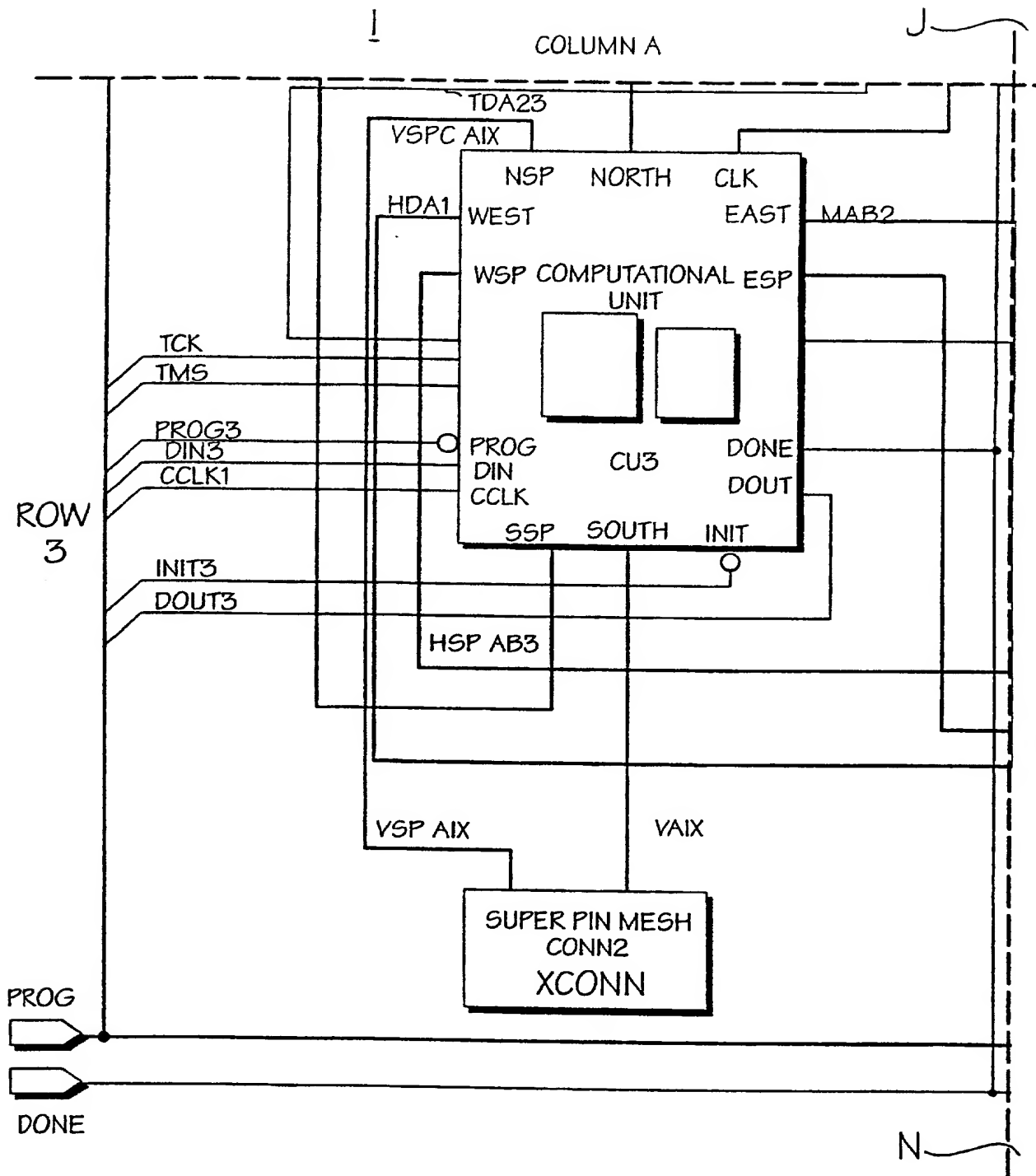


Fig. 60M

The diagram illustrates a hardware component, specifically a 'WSP COMPUTATIONAL UNIT' (CU3), which is a central processing unit. It is connected to a 'SUPER PIN MESH CONN2 XCONN' block at the bottom. The component is framed by a grid with labels 'I', 'J', 'K' at the top and 'M', 'O' at the bottom. The component is connected to a 'SUPER PIN MESH CONN2 XCONN' block at the bottom. The diagram includes labels for 'TCK', 'TMS', 'PROG7', 'DIN7', 'CCLK1', 'INIT7', 'DOUT7', 'VSP BIX', 'VBIX', 'HDA1', 'VSPC AIX', 'TDB23', 'MBC3', 'HSP AB3', 'TDB3CD', 'NSP NORTH CLK WEST EAST', 'WSP COMPUTATIONAL UNIT', 'CU3', 'DONE', 'DOUT', 'SSP SOUTH INIT', and 'XCONN'.

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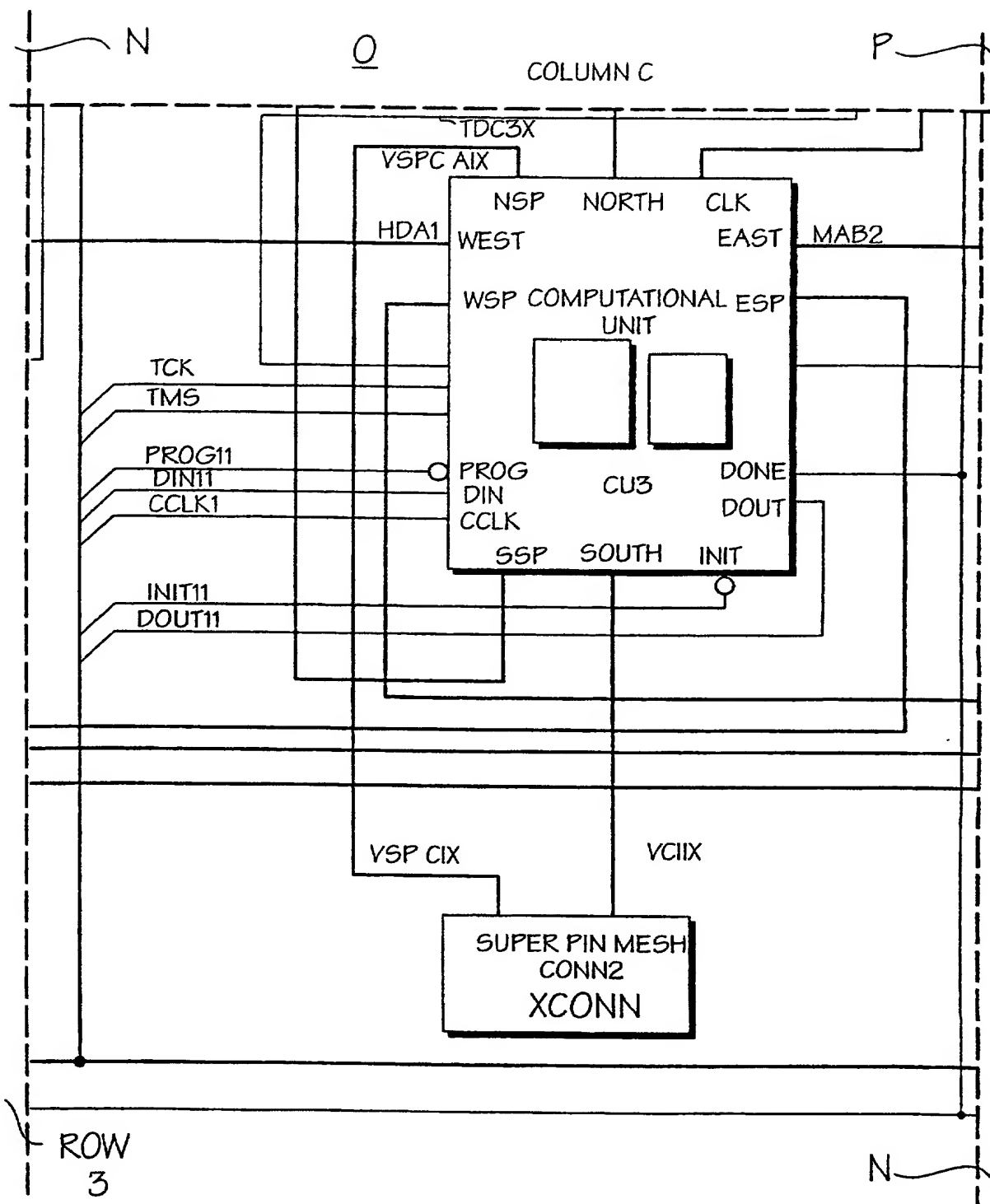


Fig. 600

Fig. 60P



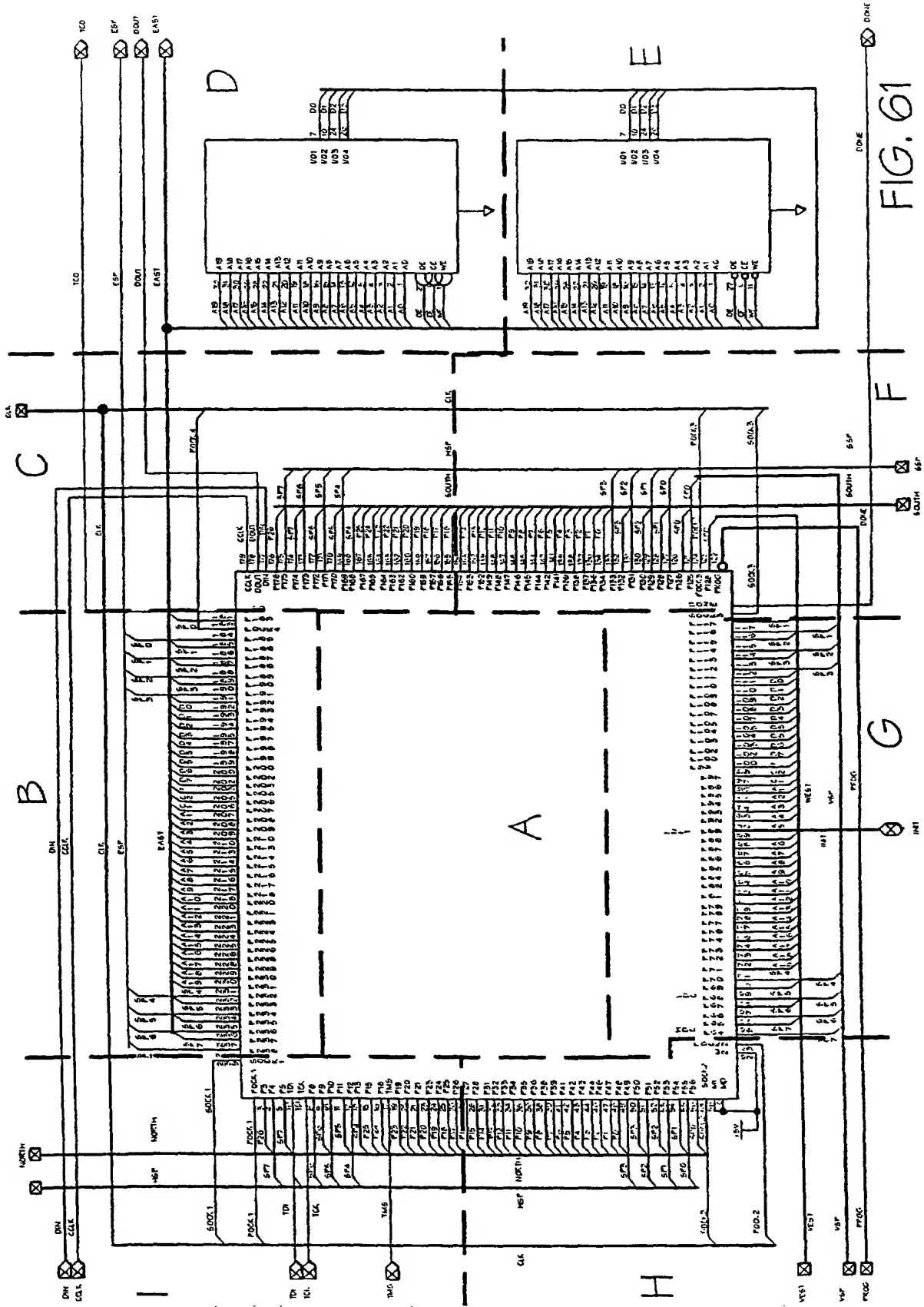


FIG. 61

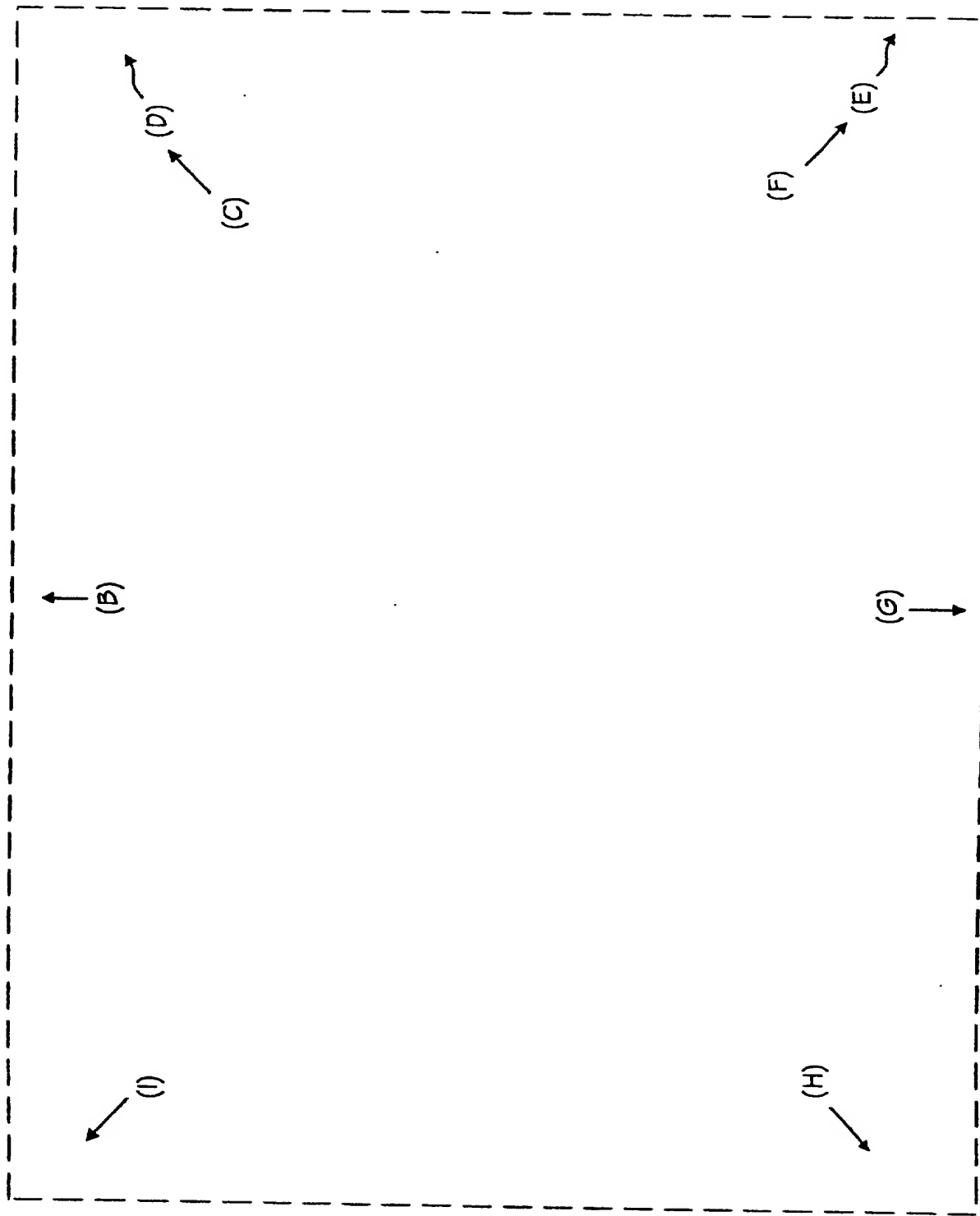


FIG. 61(A)

DIN	CCLK	CLK	ESP	EAST
			SP 0	SP 0
			SP 1	SP 1
			SP 2	SP 2
			SP 3	SP 3
				DO
				D1
				D2
				D3
				D4
				D5
				D6
				D7
				C?
				C1
				A2
				A3
				A4
				A5
				A6
				A7
				A8
				A9
				A10
				A11
				A12
				A13
				A14
				A15
				A16
				A17
				A18
				A19
				SP 4
				SP 5
				SP 6
				SP 7

FIG. 61(B)

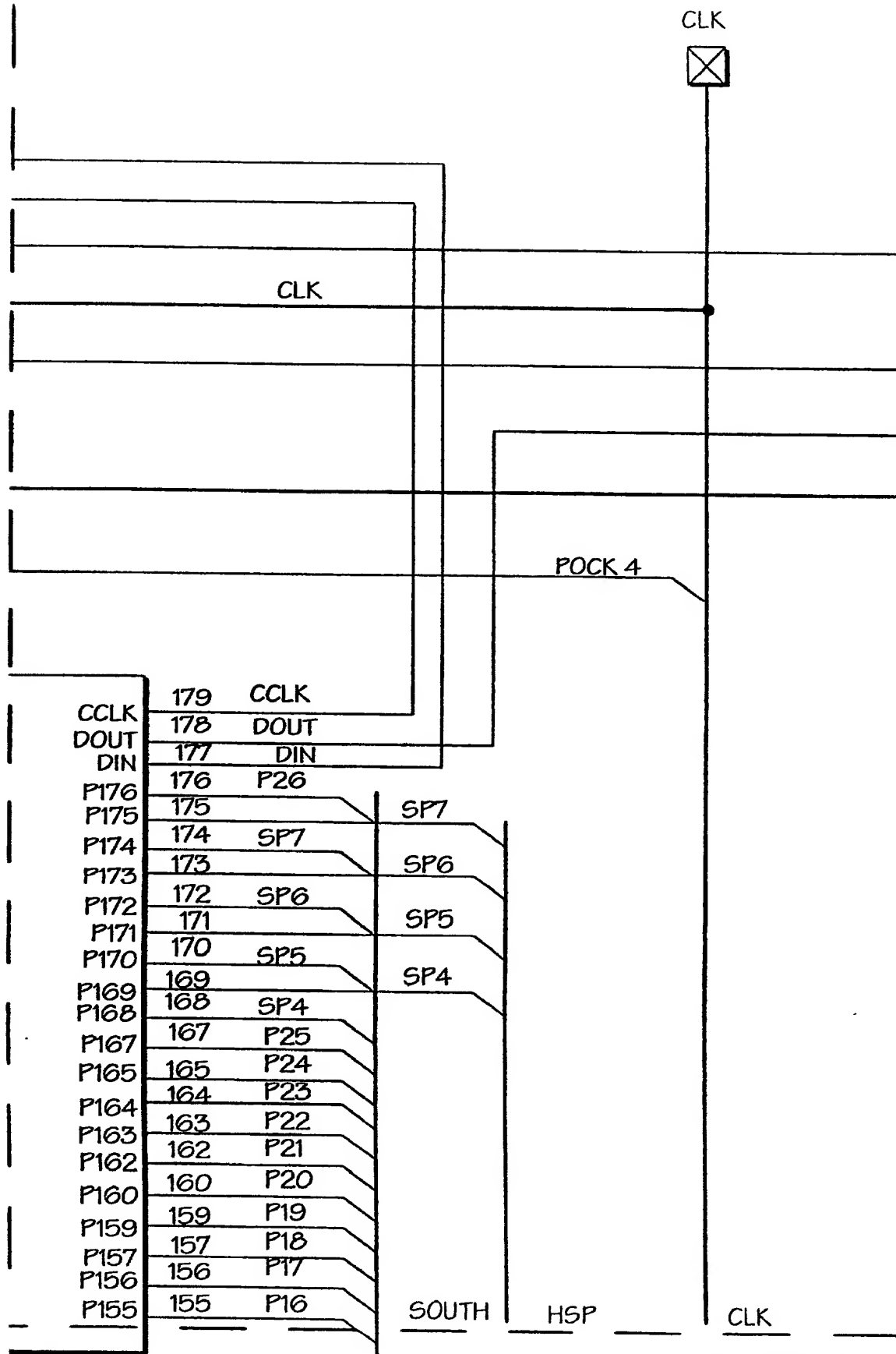


FIG. 61(C)

The diagram shows a 32-bit address decoder with the following connections:

- Inputs:**
  - A19 to A0: 32-bit address bus
  - OE: 27
  - CE: 6
  - WE: 11
- Outputs:**
  - I/O1: 7
  - I/O2: 10
  - I/O3: 24
  - I/O4: 26

The decoder is connected to a 32-bit address bus, which is also connected to the TCO, ESP, DOUT, and EAST signals.

FIG. 61(D)

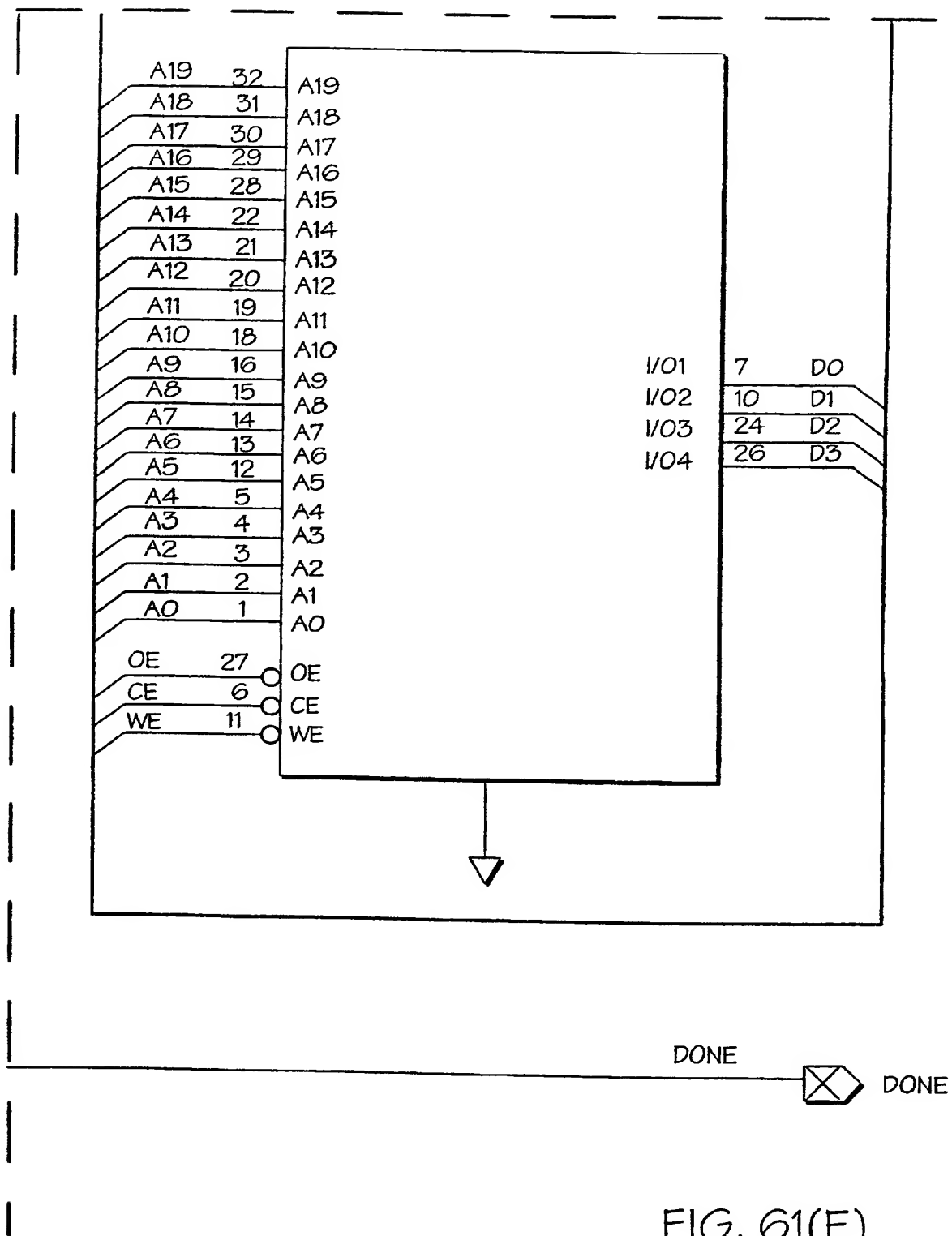


FIG. 61(E)

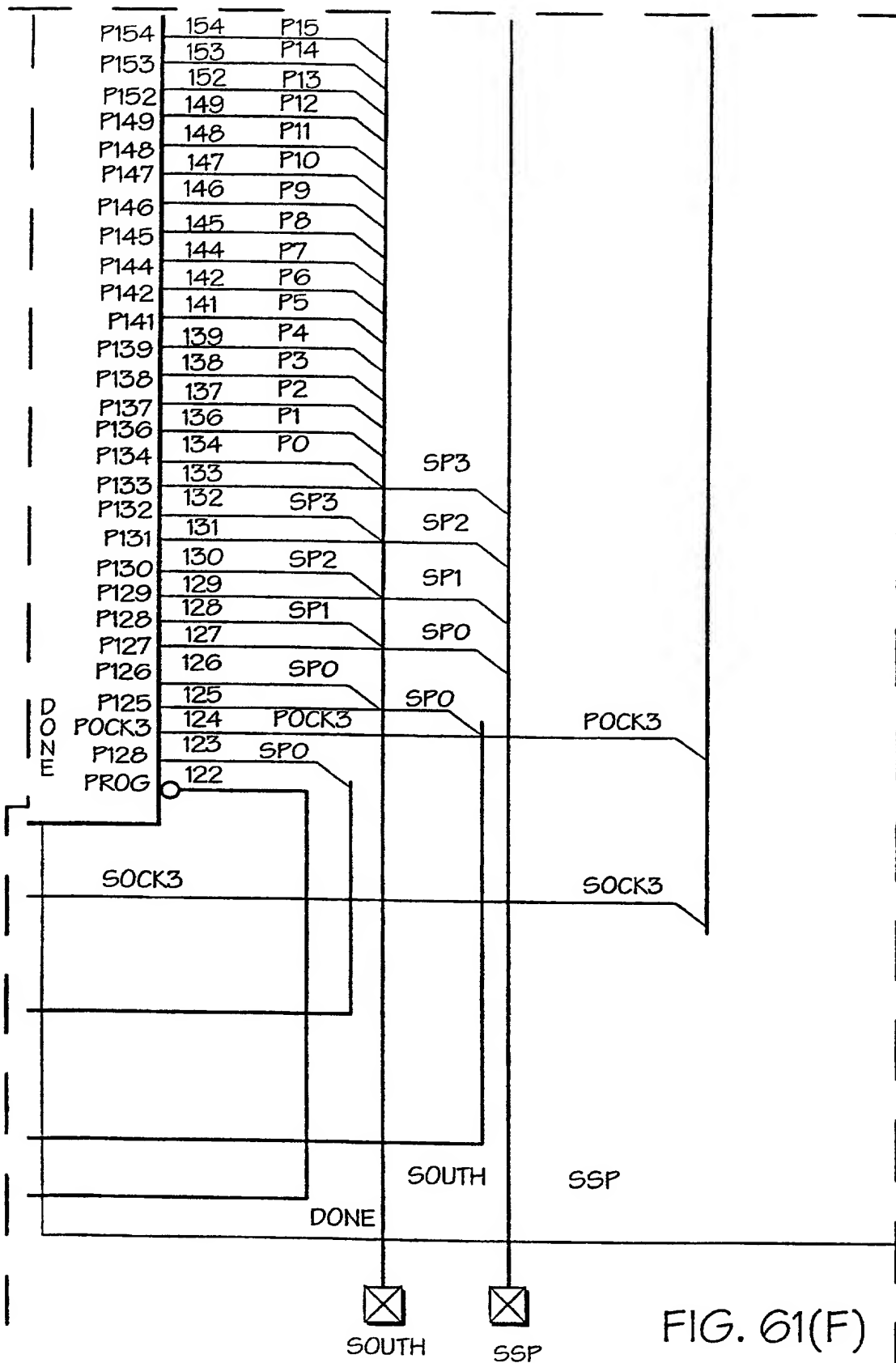
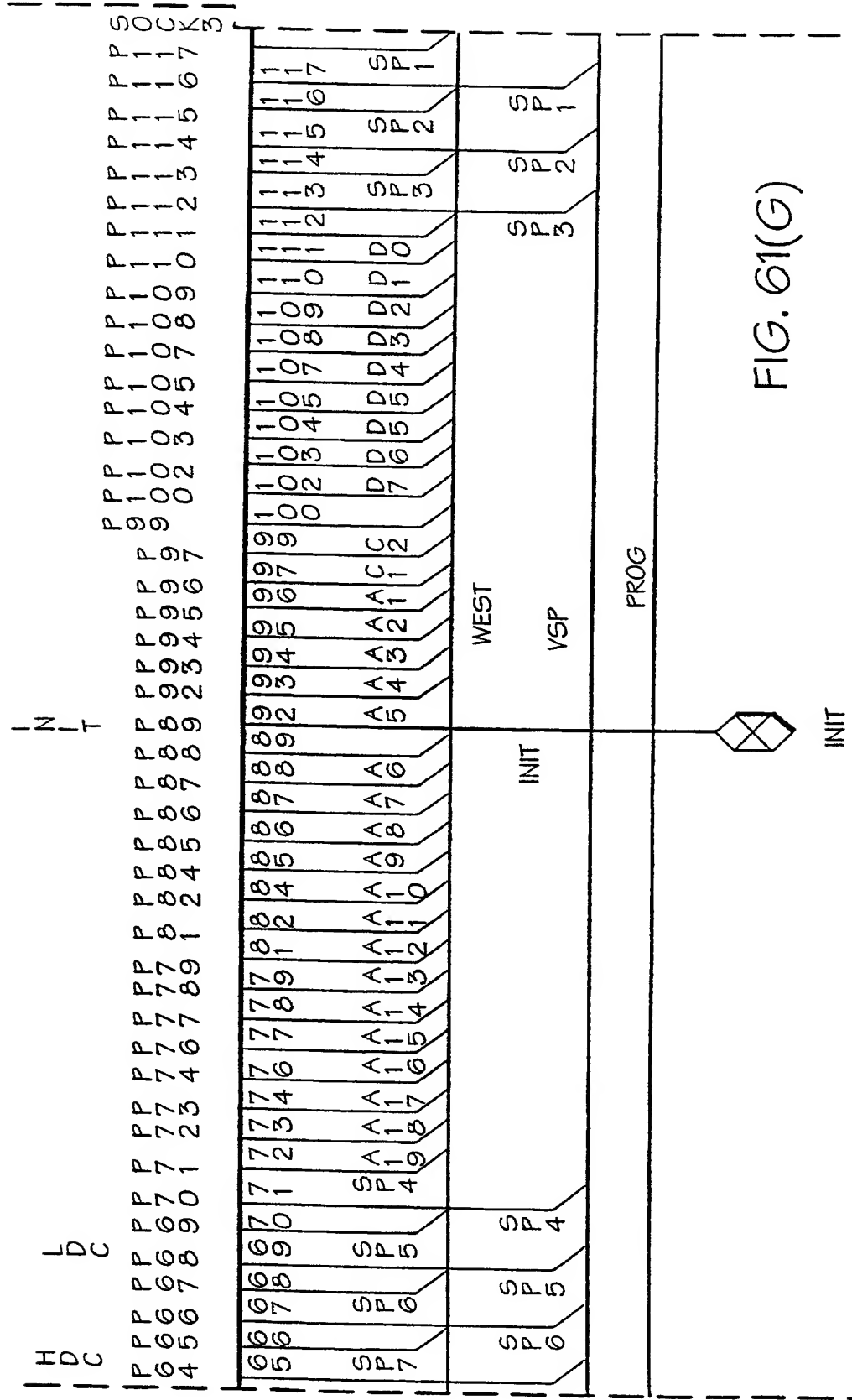


FIG. 61(F)

[illegible]



1002033-1-404

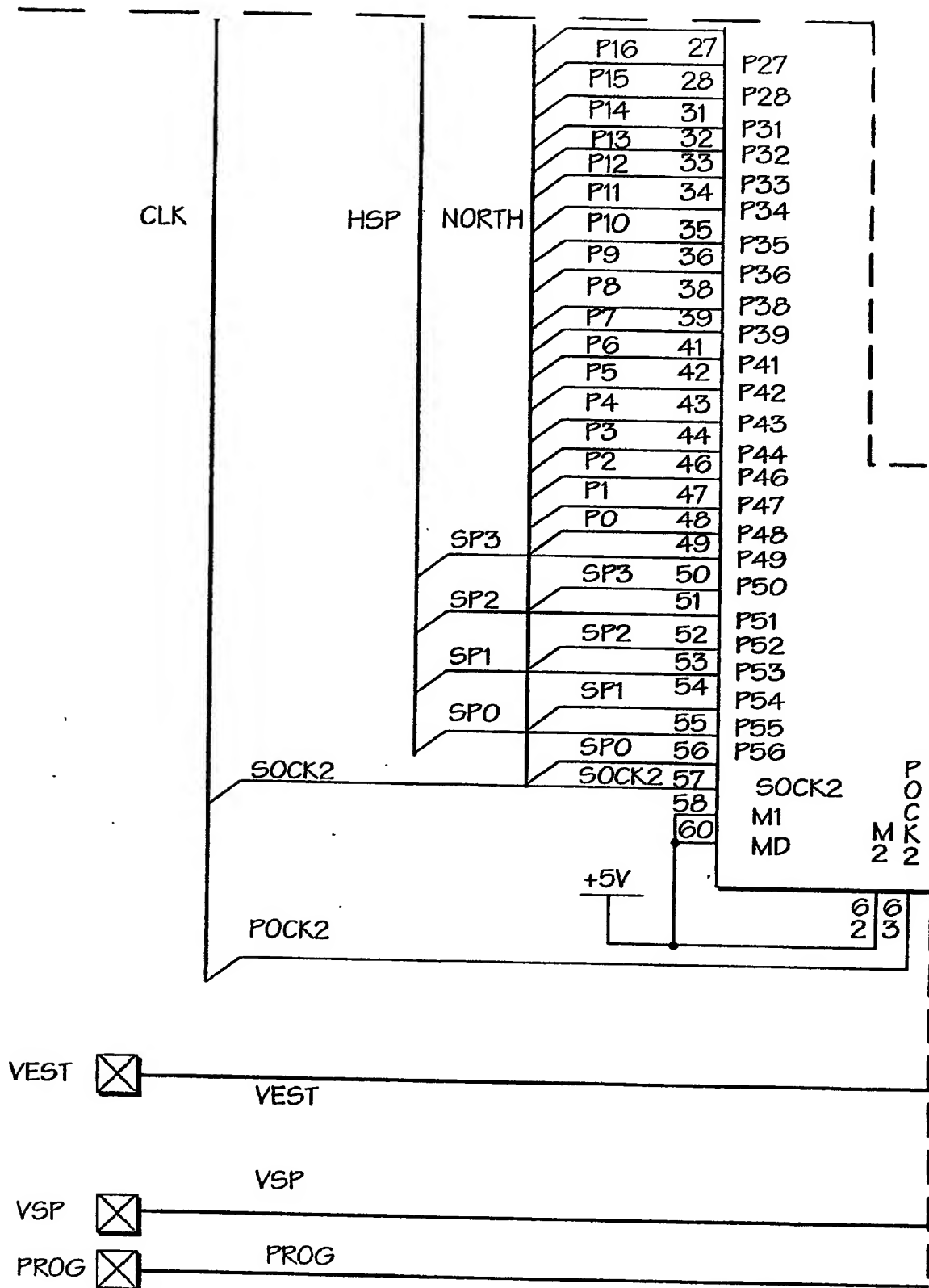


FIG. 61(H)

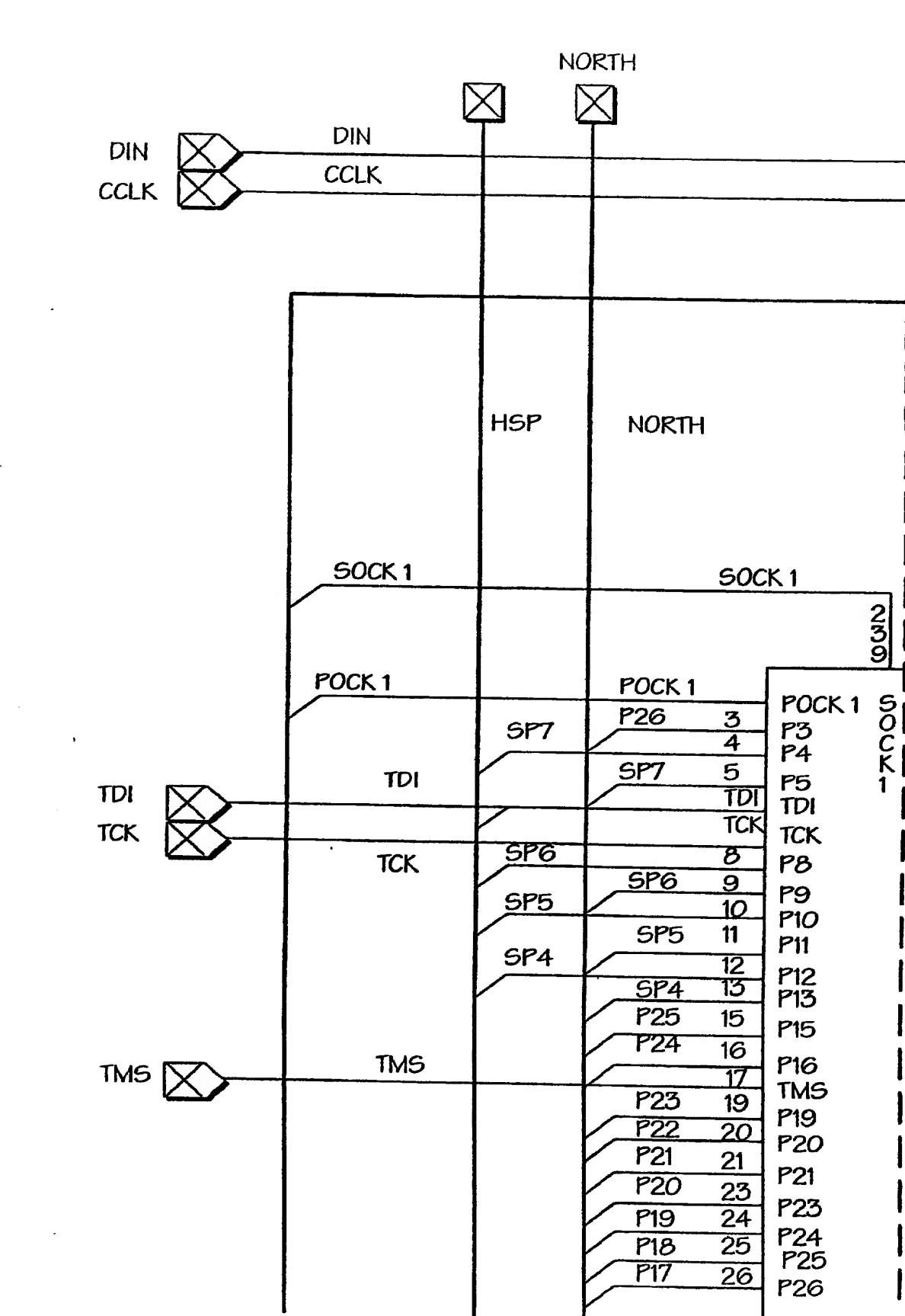


FIG. 61(I)

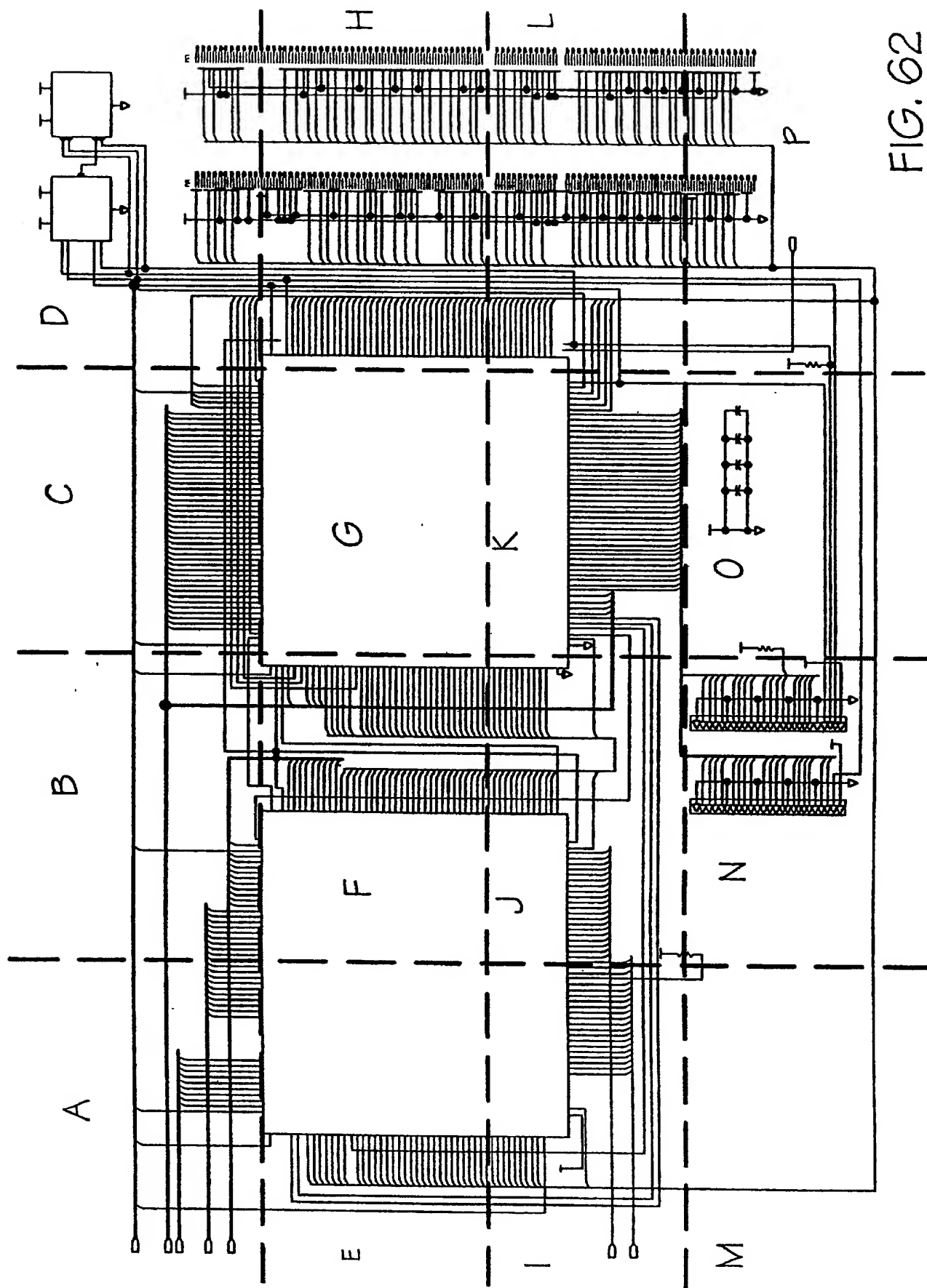
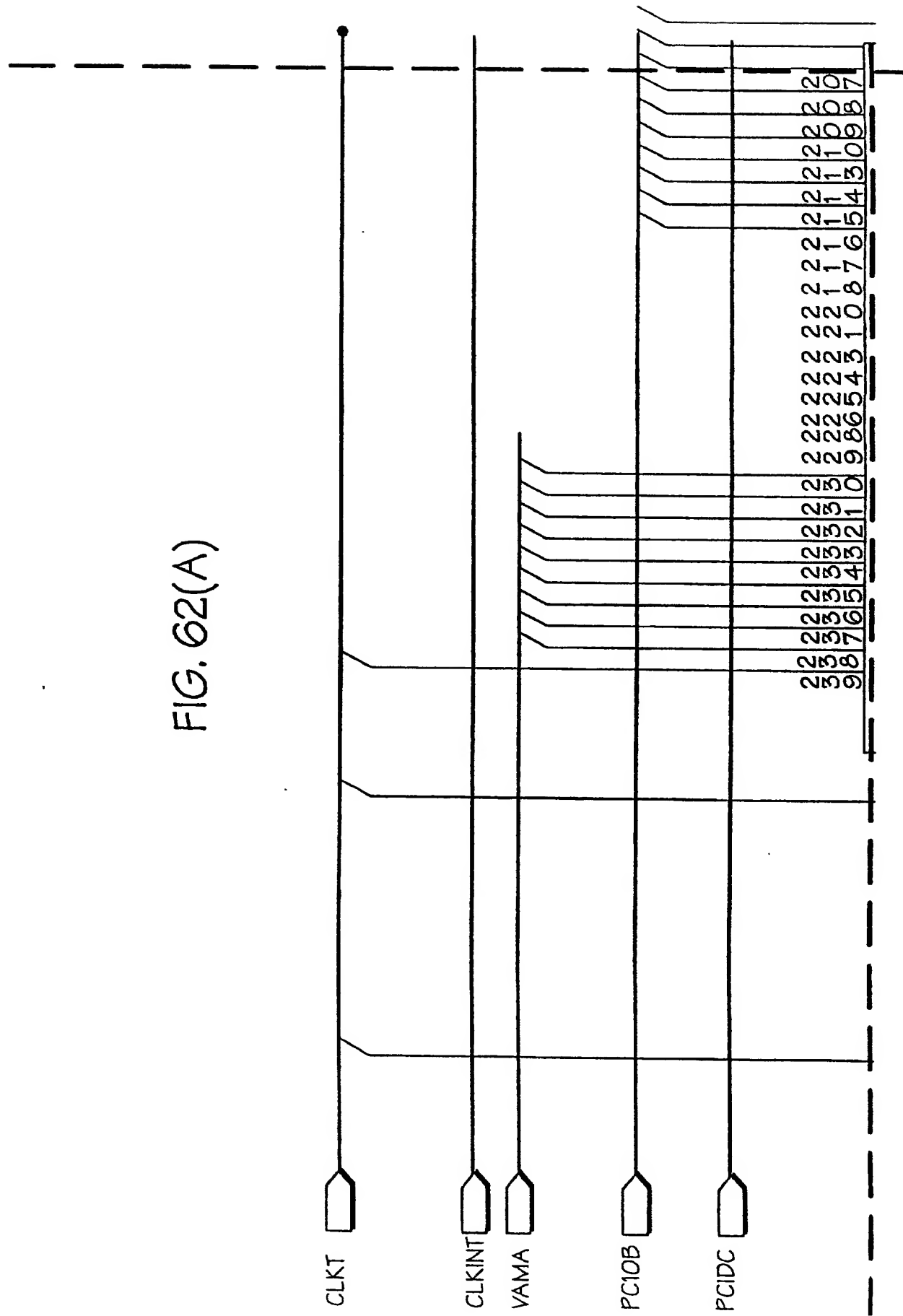


FIG. 62

FIG. 62(A)



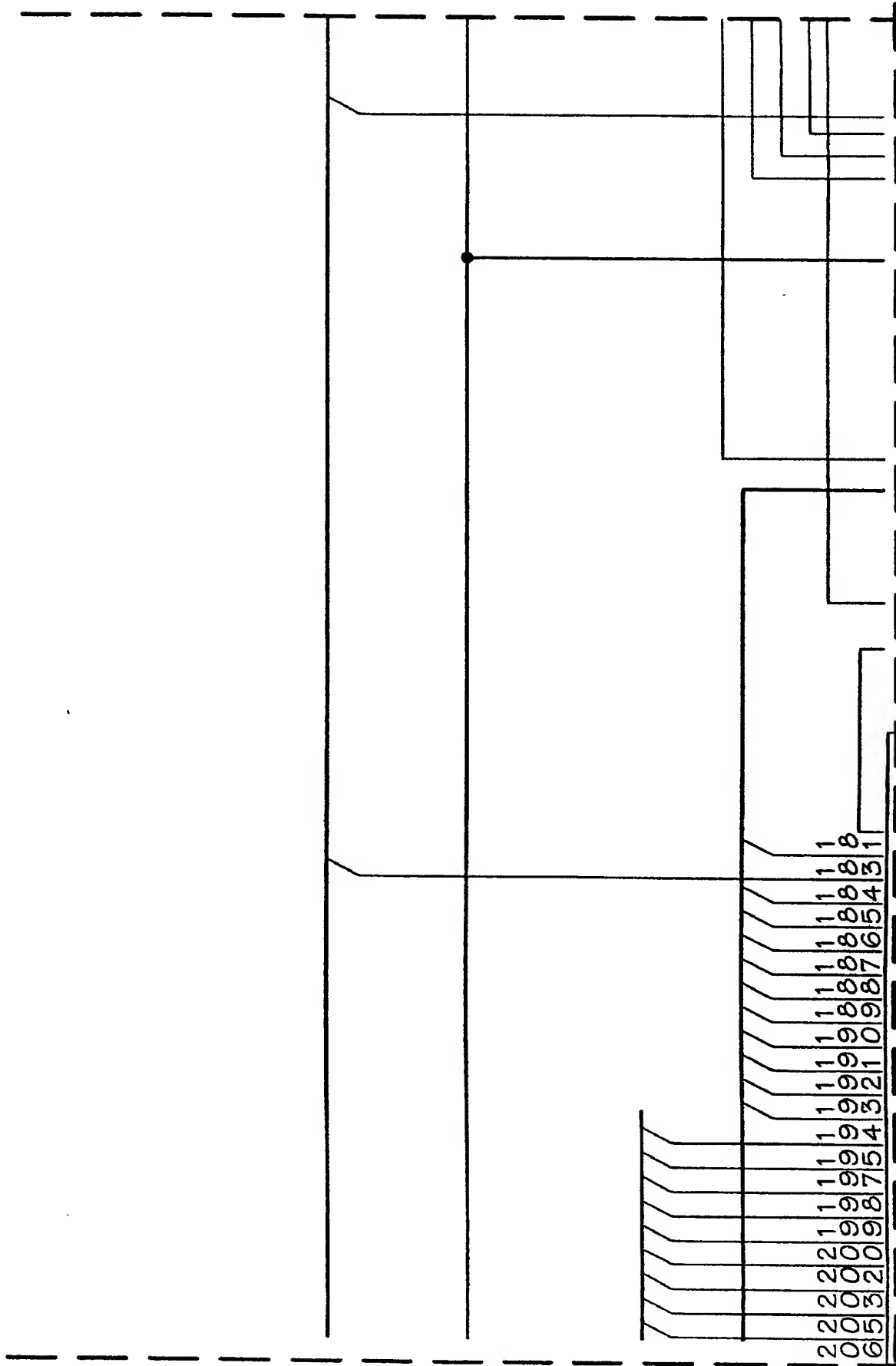


FIG. 62(B)

[illegible]

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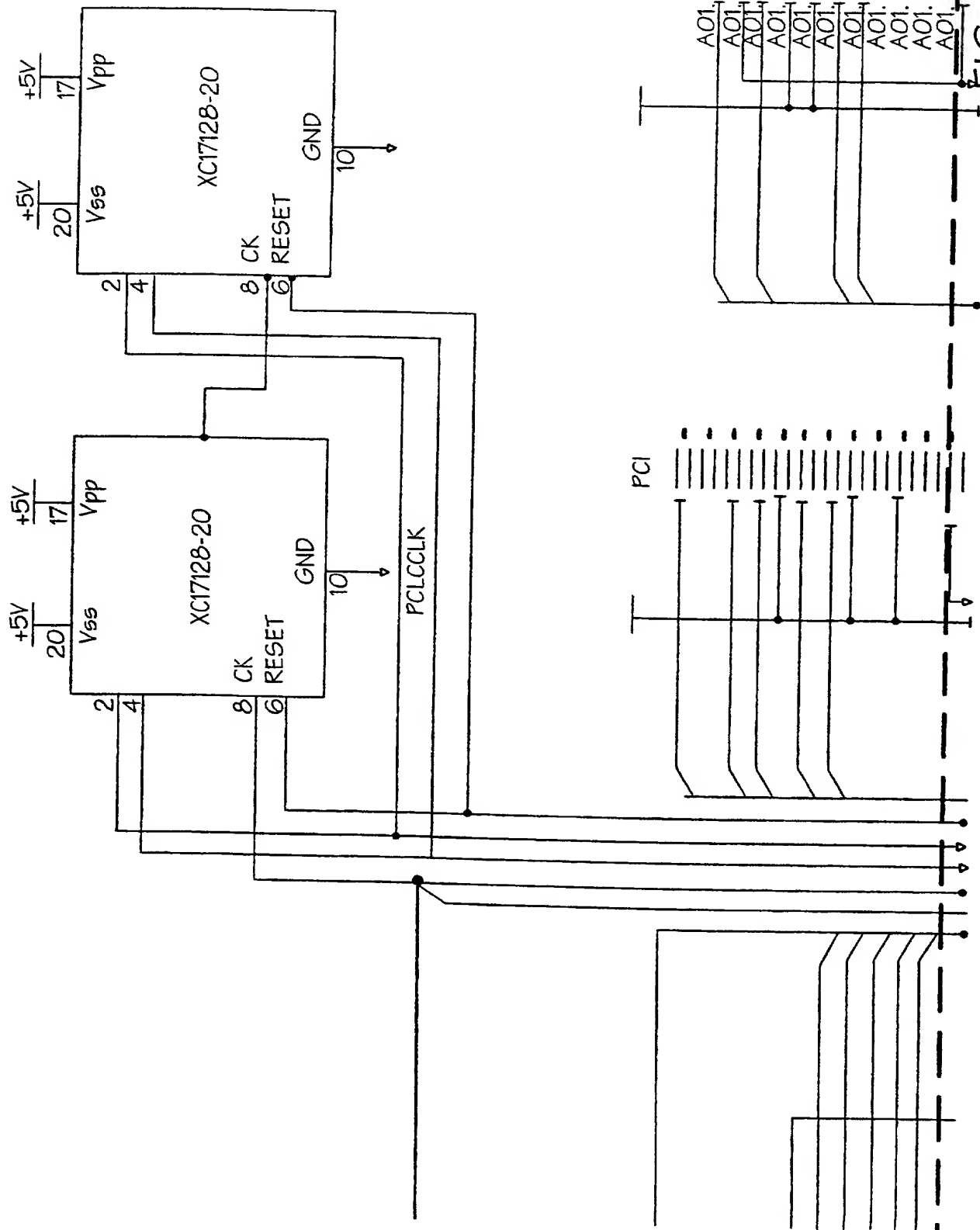


FIG. 62(D)

2	POCK1	S	PPP	22336	4
3	P3	O	22333	5	
4	P4	C	22333	6	
5	P5	K	22333	7	
6	P5		22333	8	
7	TDI		22333	9	
8	TCK		22333	10	
9	P8		22333	11	
10	P9		22333	12	
11	P10		22333	13	
12	P11		22333	14	
13	P12		22333	15	
14	P13		22333	16	
15	P14		22333	17	
16	P15		22333	18	
17	P16		22333	19	
18	TM5		22333	20	
19	P18		22333	21	
20	P20		22333	22	
21	P21		22333	23	
22	P23		22333	24	
23	P24		22333	25	
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25	P26		22333	27	
26	P27		22333	28	
27	P28		22333	29	
28	P31		22333	30	
29	P32		22333	31	
30	P33		22333	32	
31	P34		22333	33	
32	P35		22333	34	
33	P36		22333	35	
34	P38		22333	36	
35	P39		22333	37	
36	P41		22333	38	
37	P42		22333	39	
38	P43		22333	40	
39	P44		22333	41	
40	P46		22333	42	
41	P47		22333	43	
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43			22333	45	
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45			22333	47	
46			22333		
47			22333		



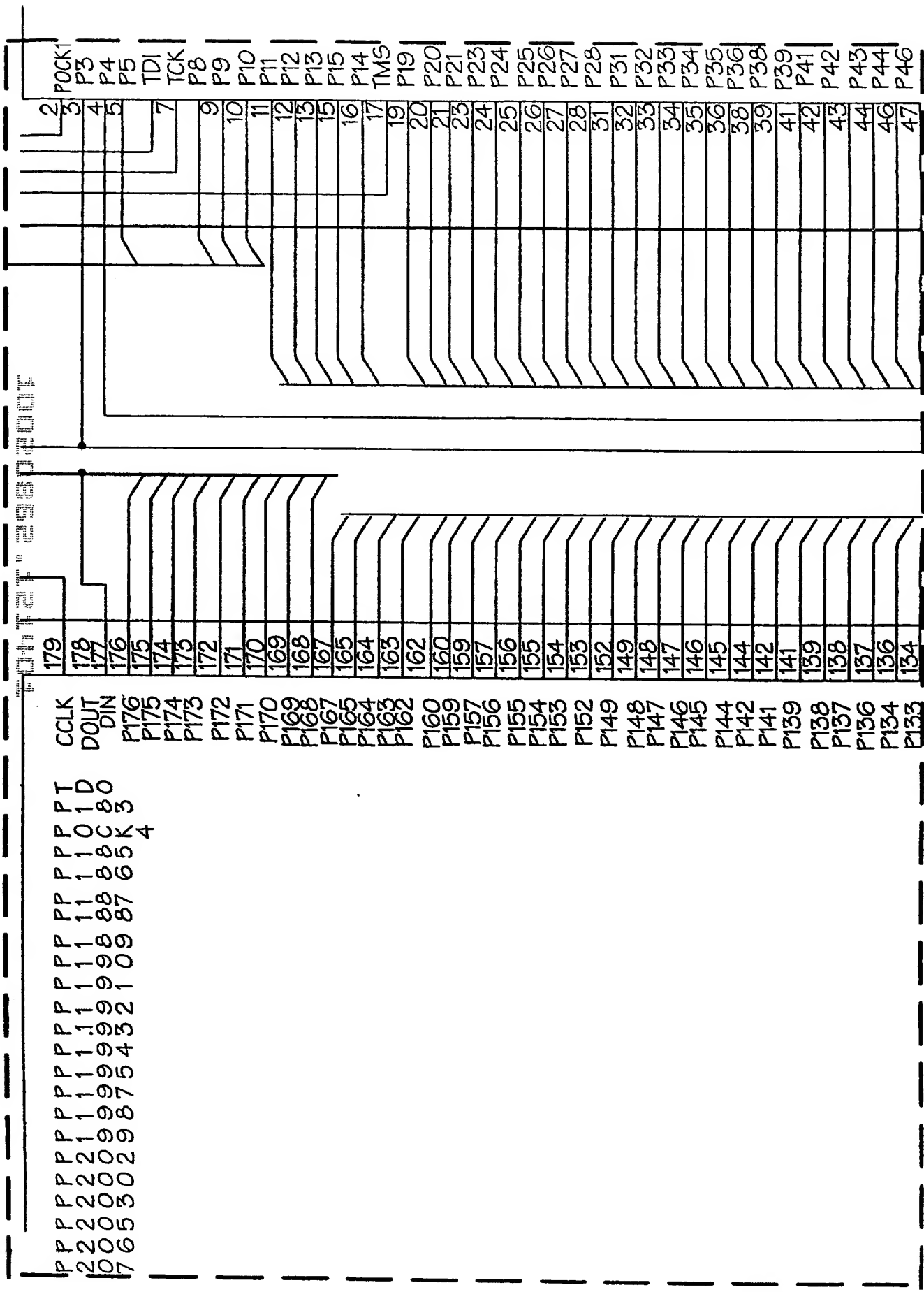


FIG. 62(F)

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FIG. 62(G)

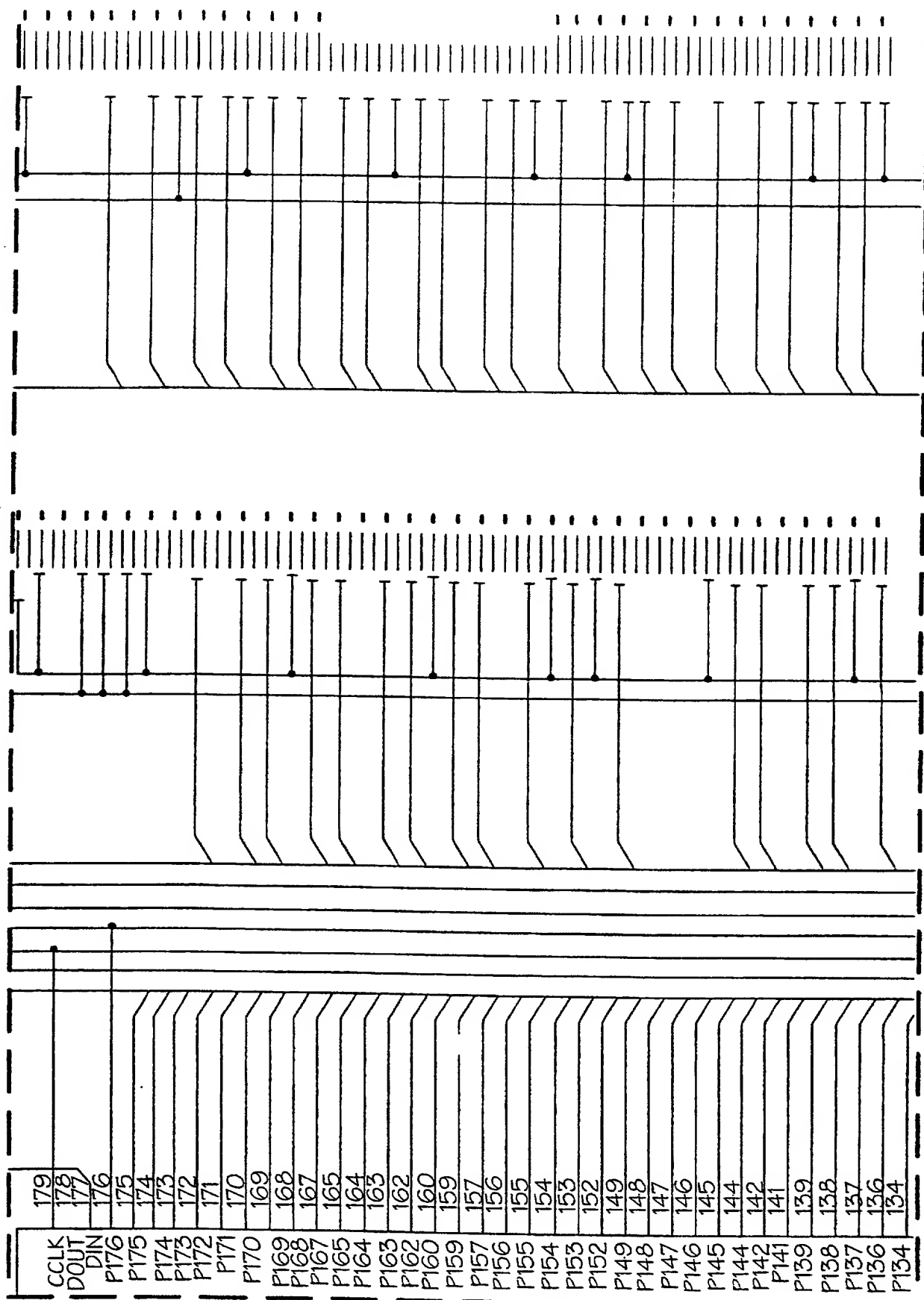


FIG. 62(H)

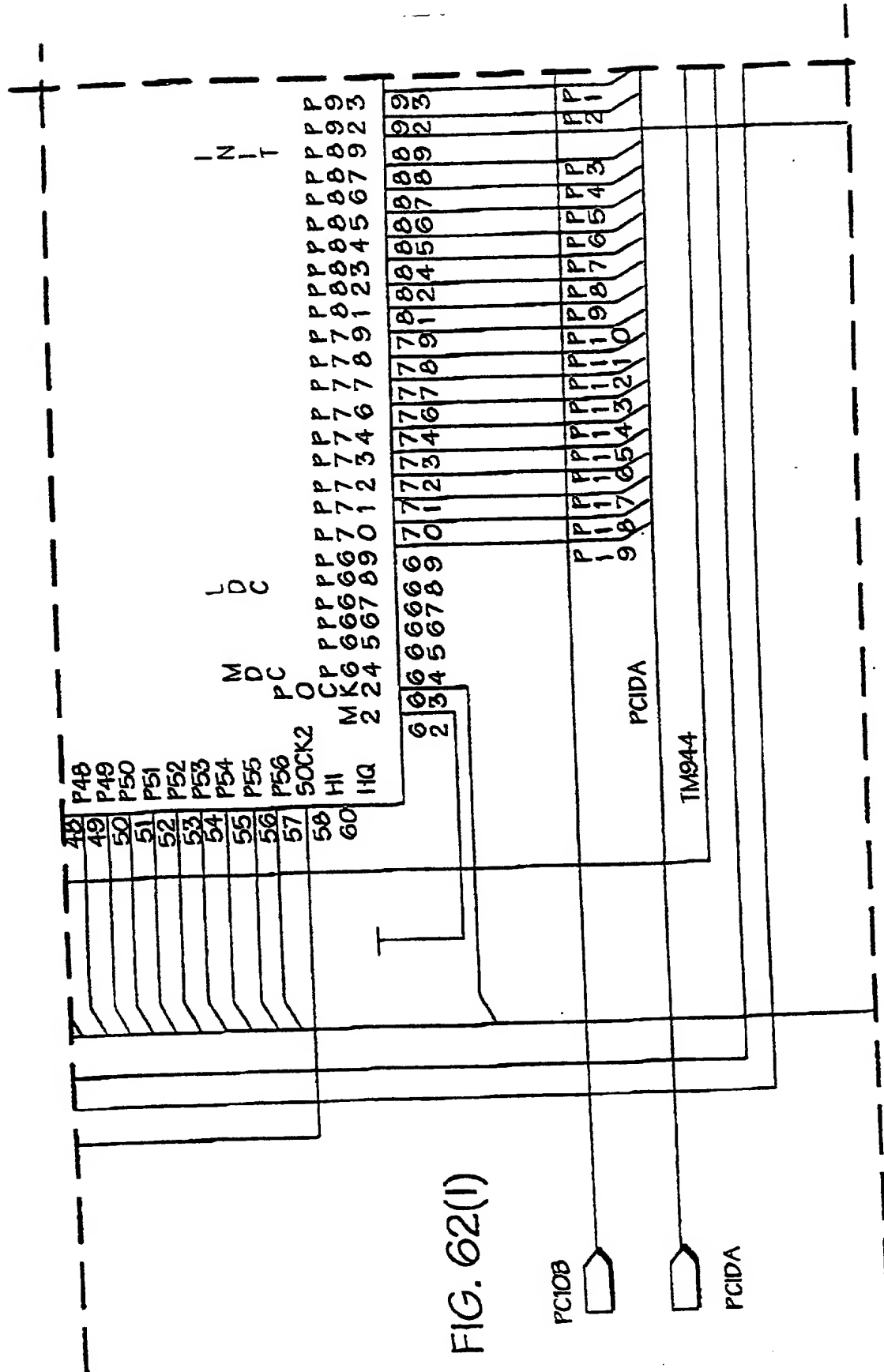


FIG. 62(J)

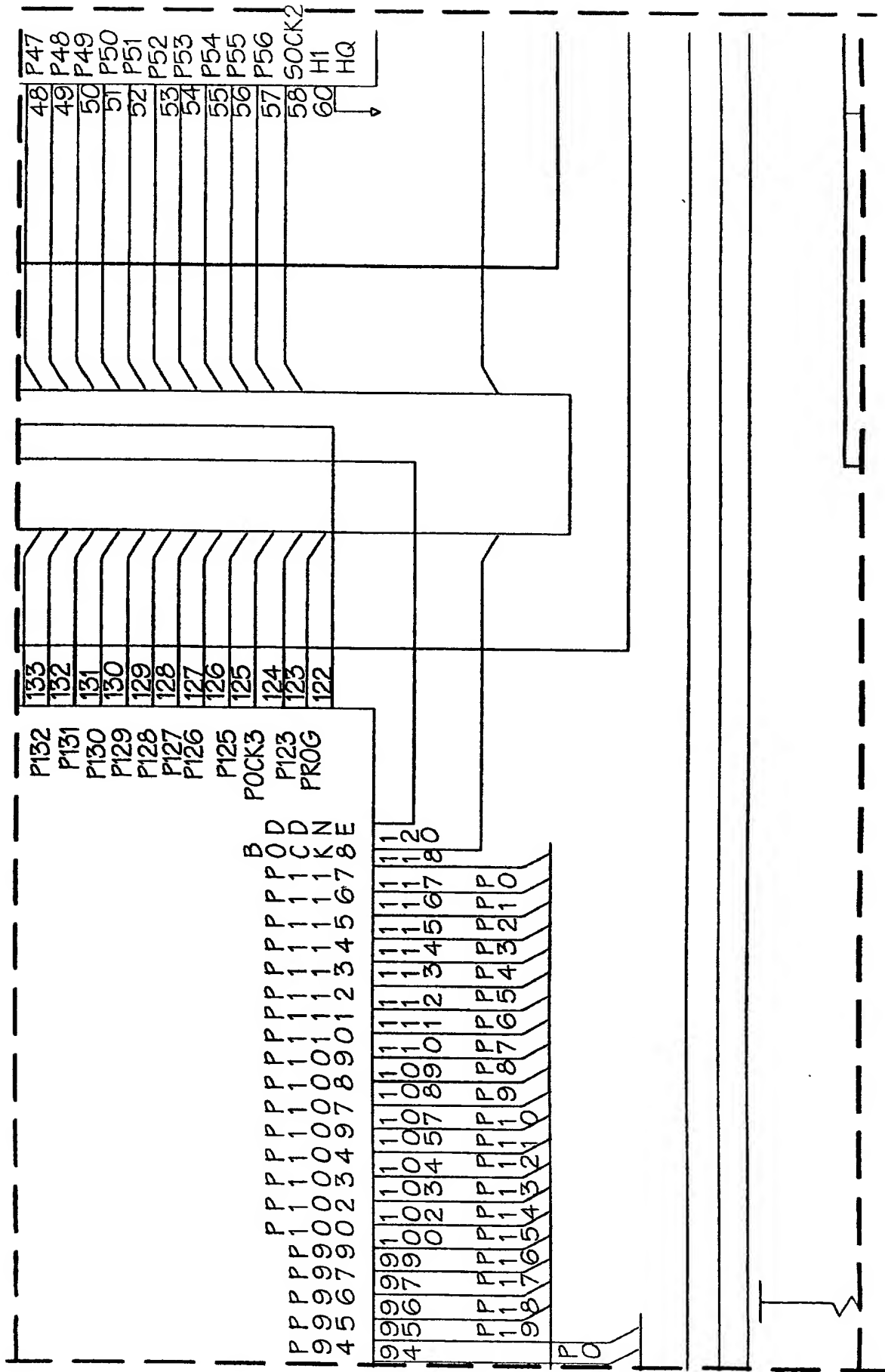
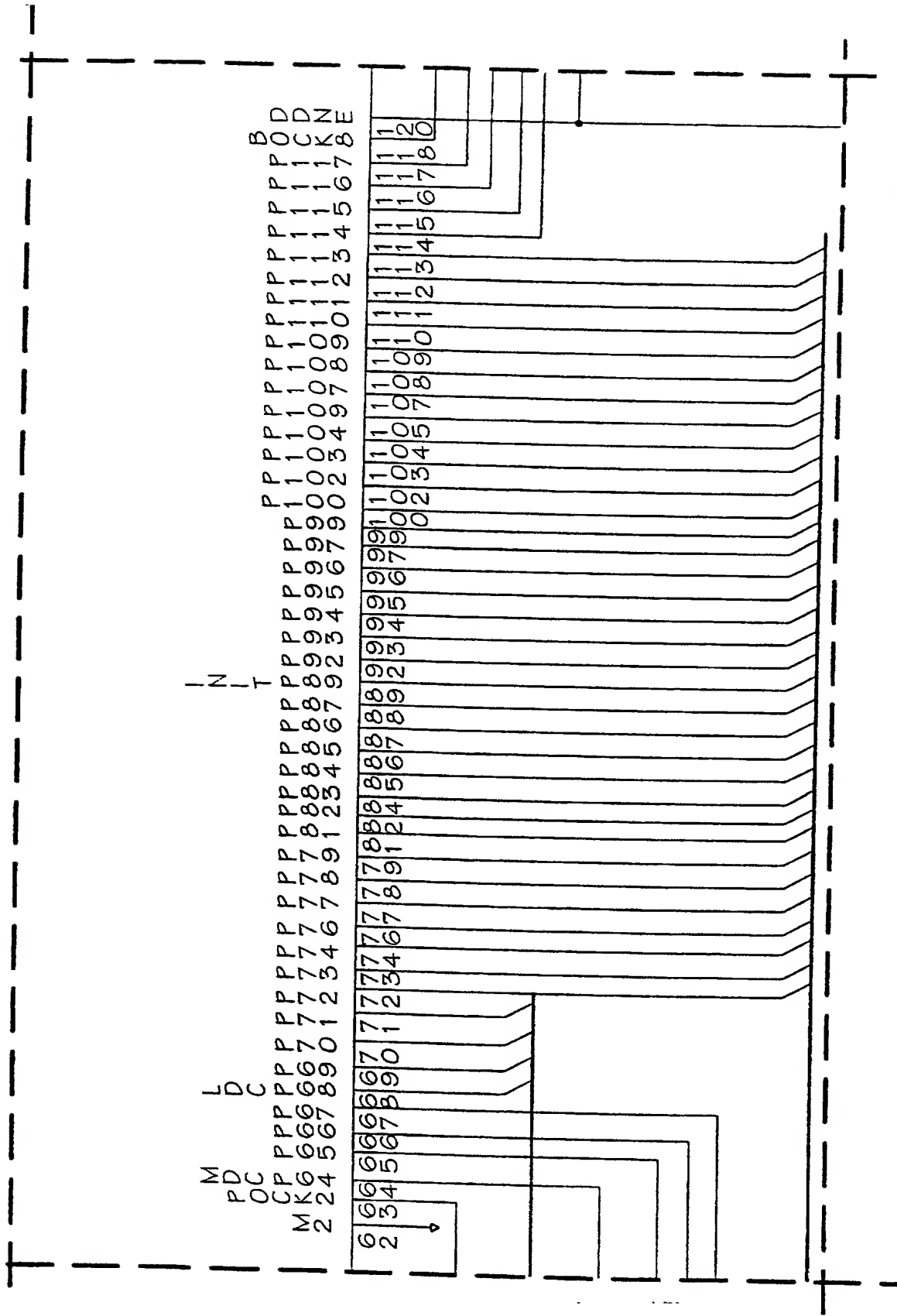


FIG. 62(J)



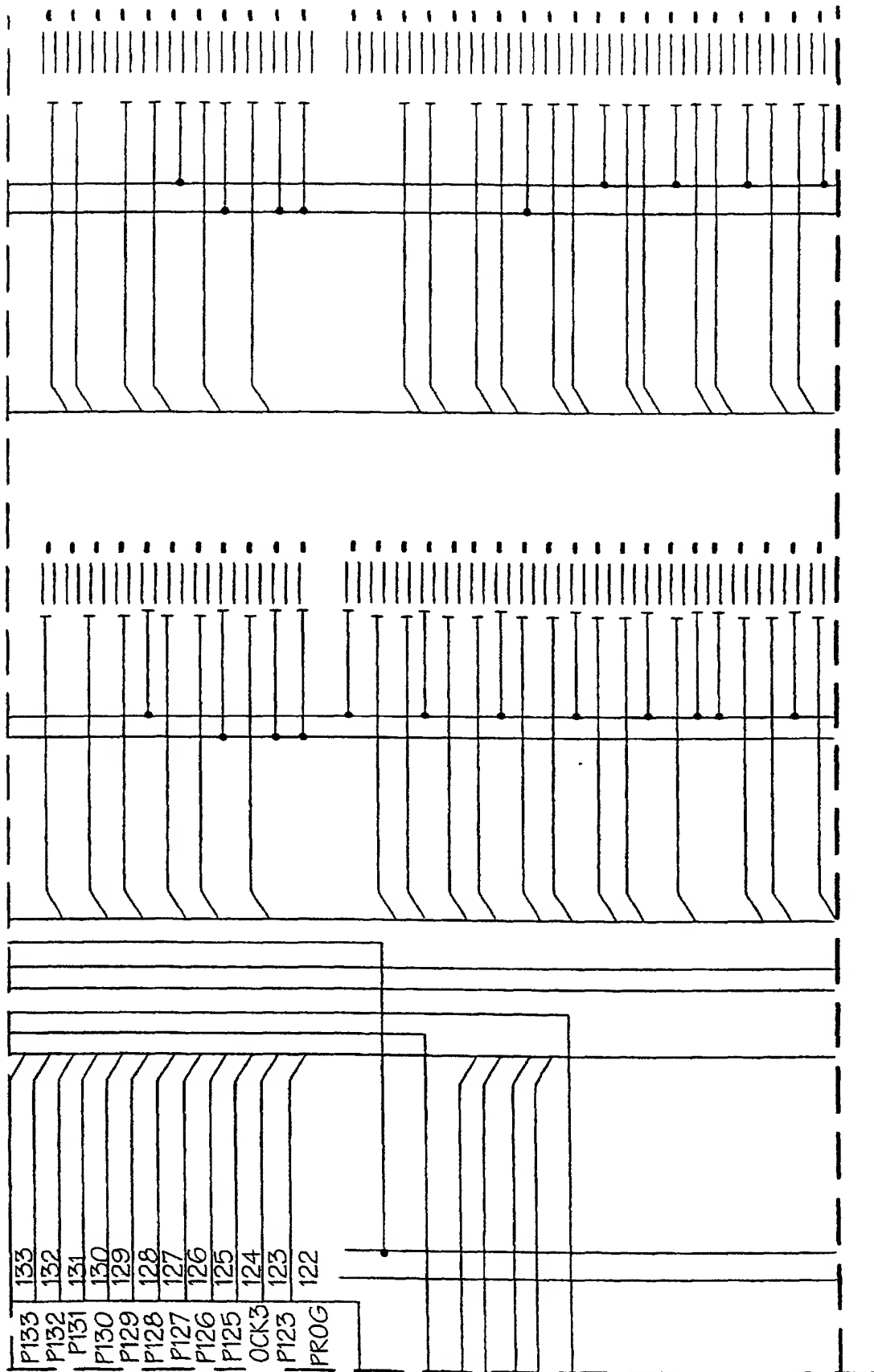
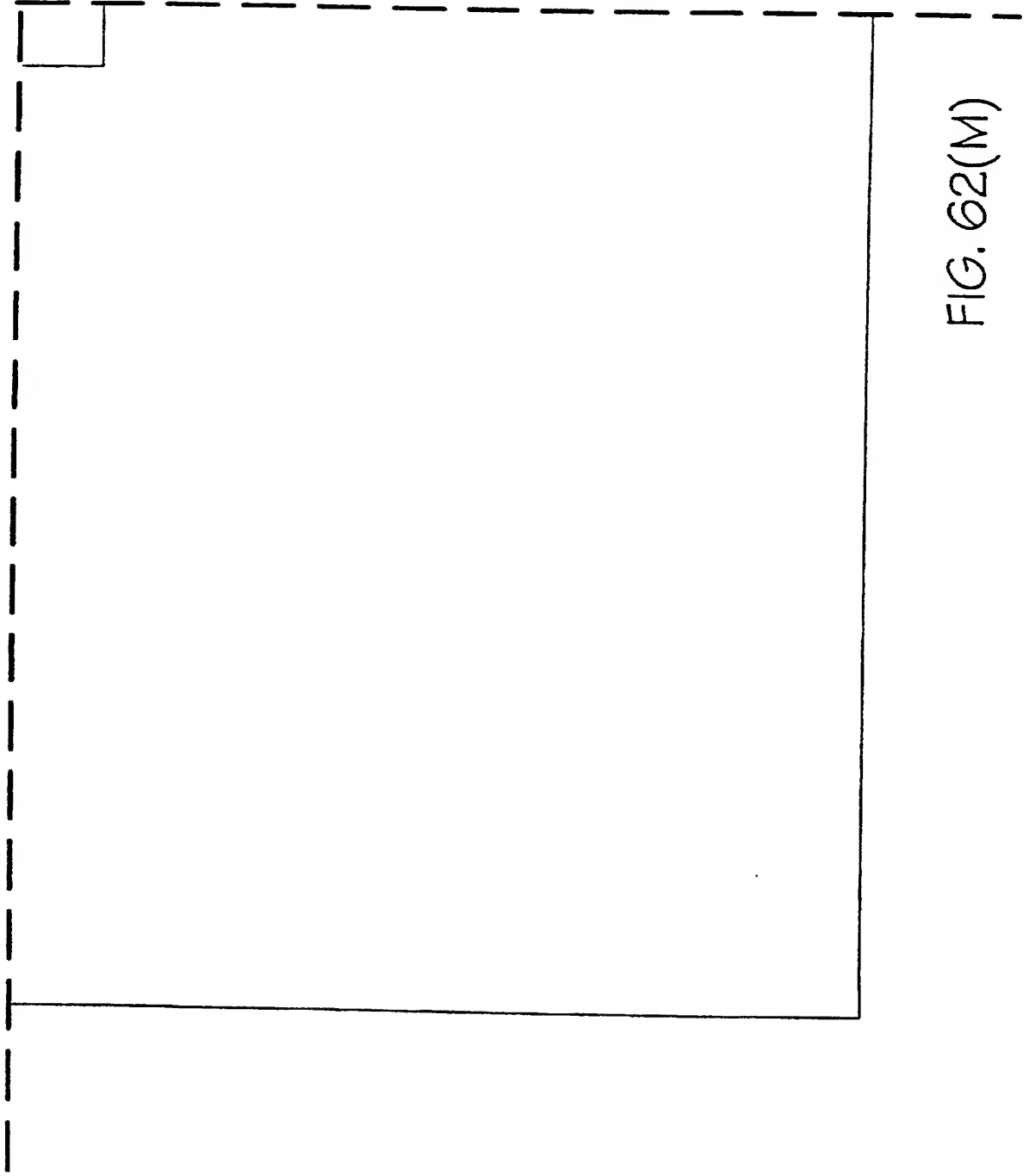


FIG. 62(L)





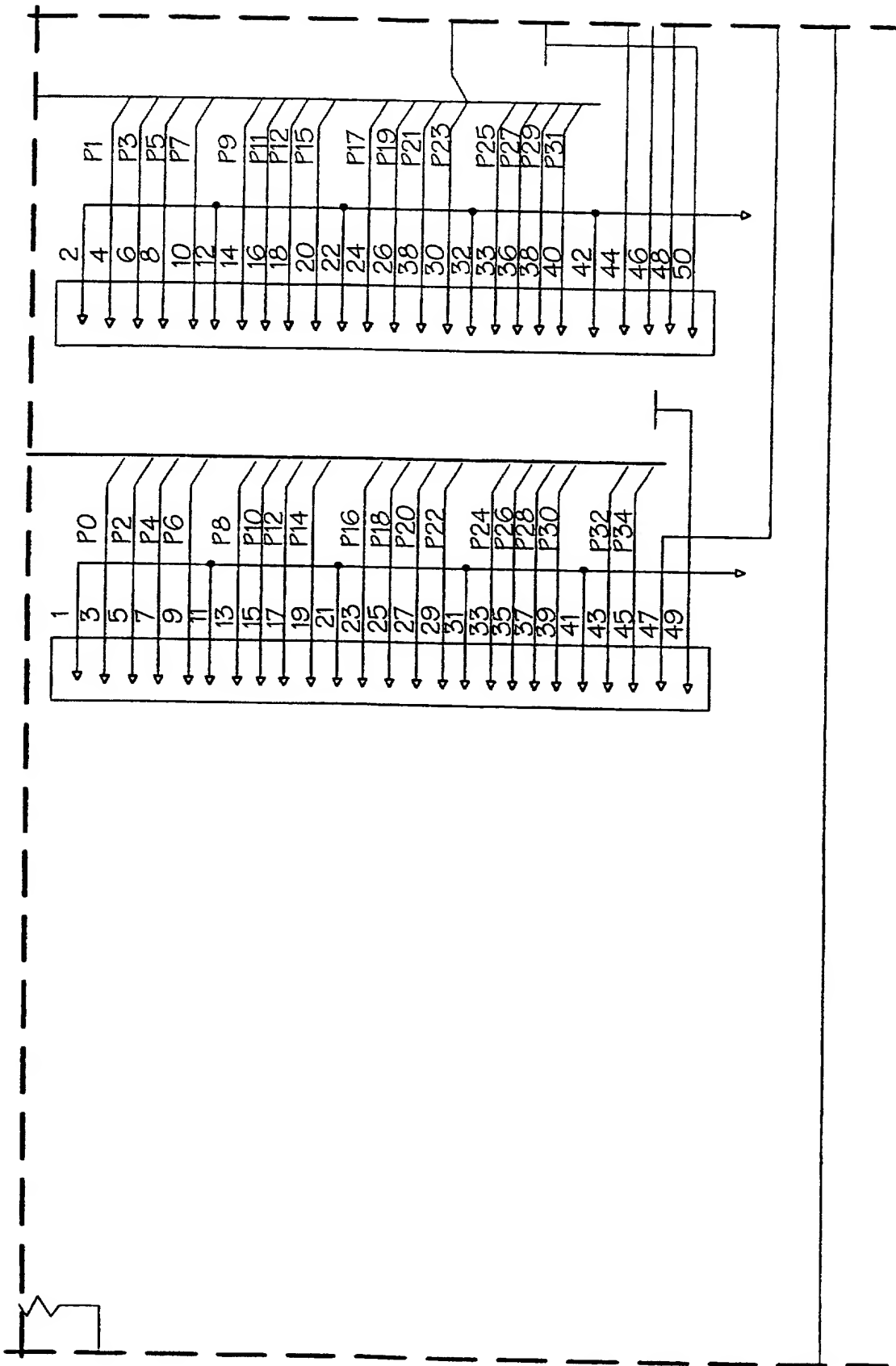


FIG. 62(N)

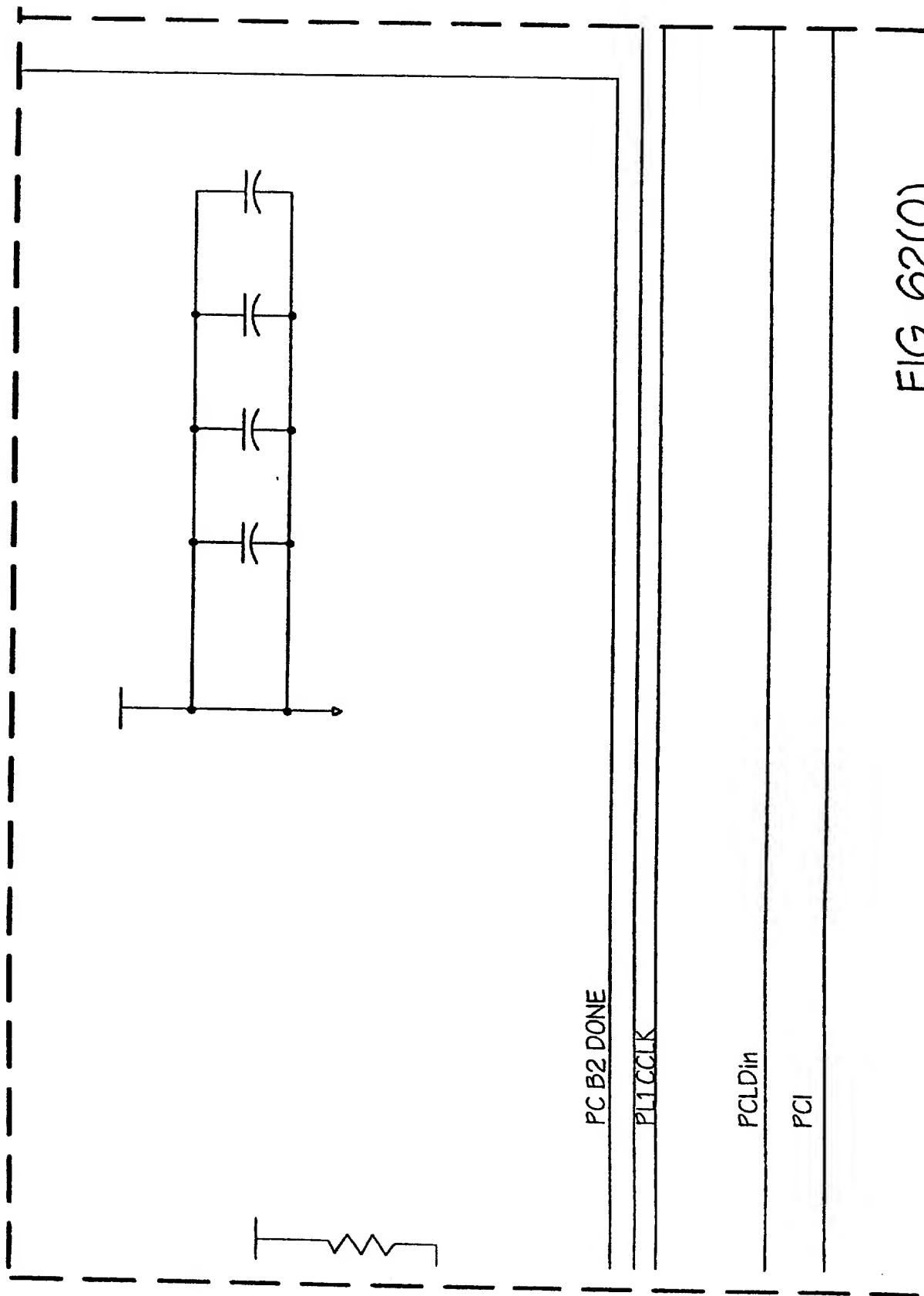
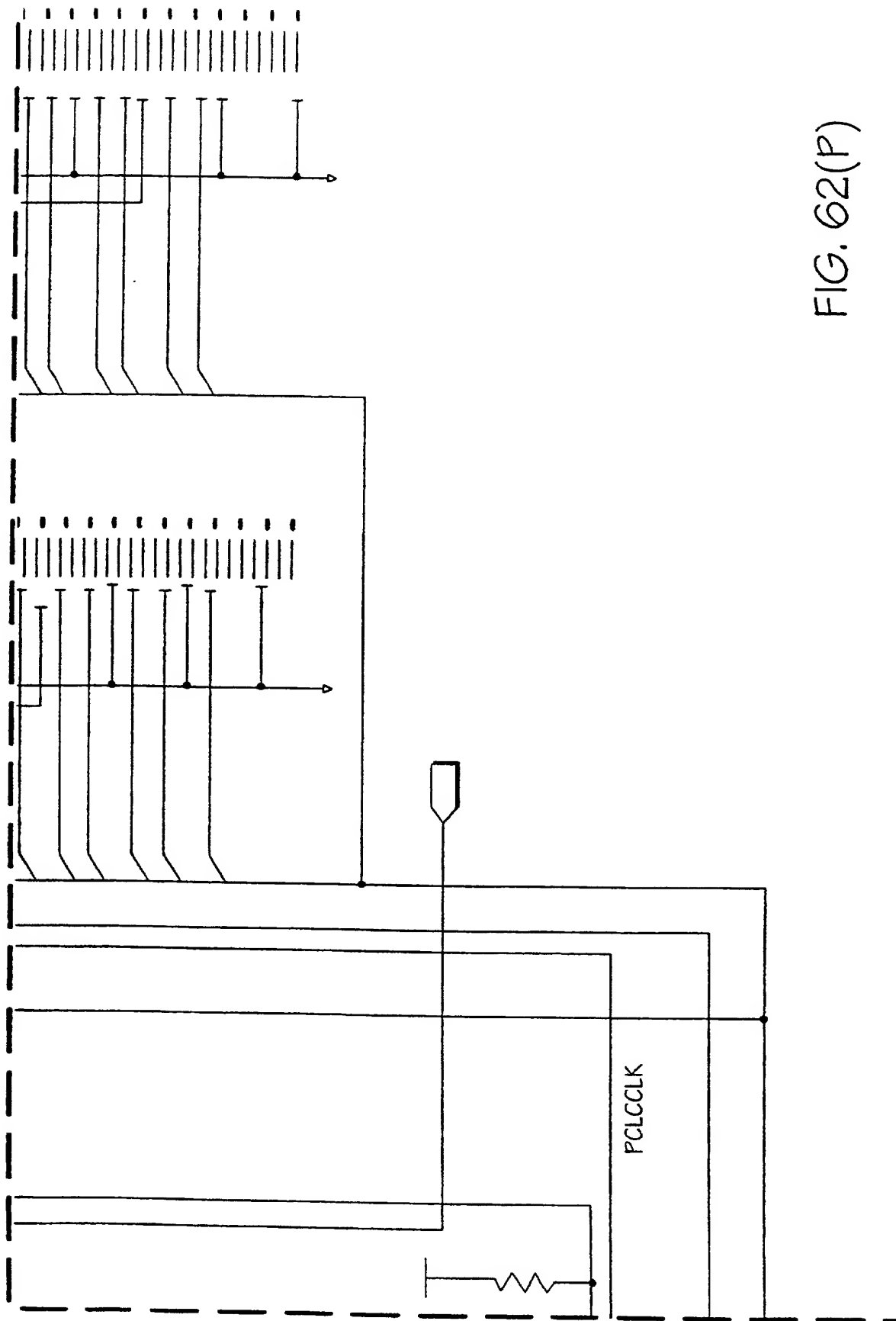


FIG. 62(0)



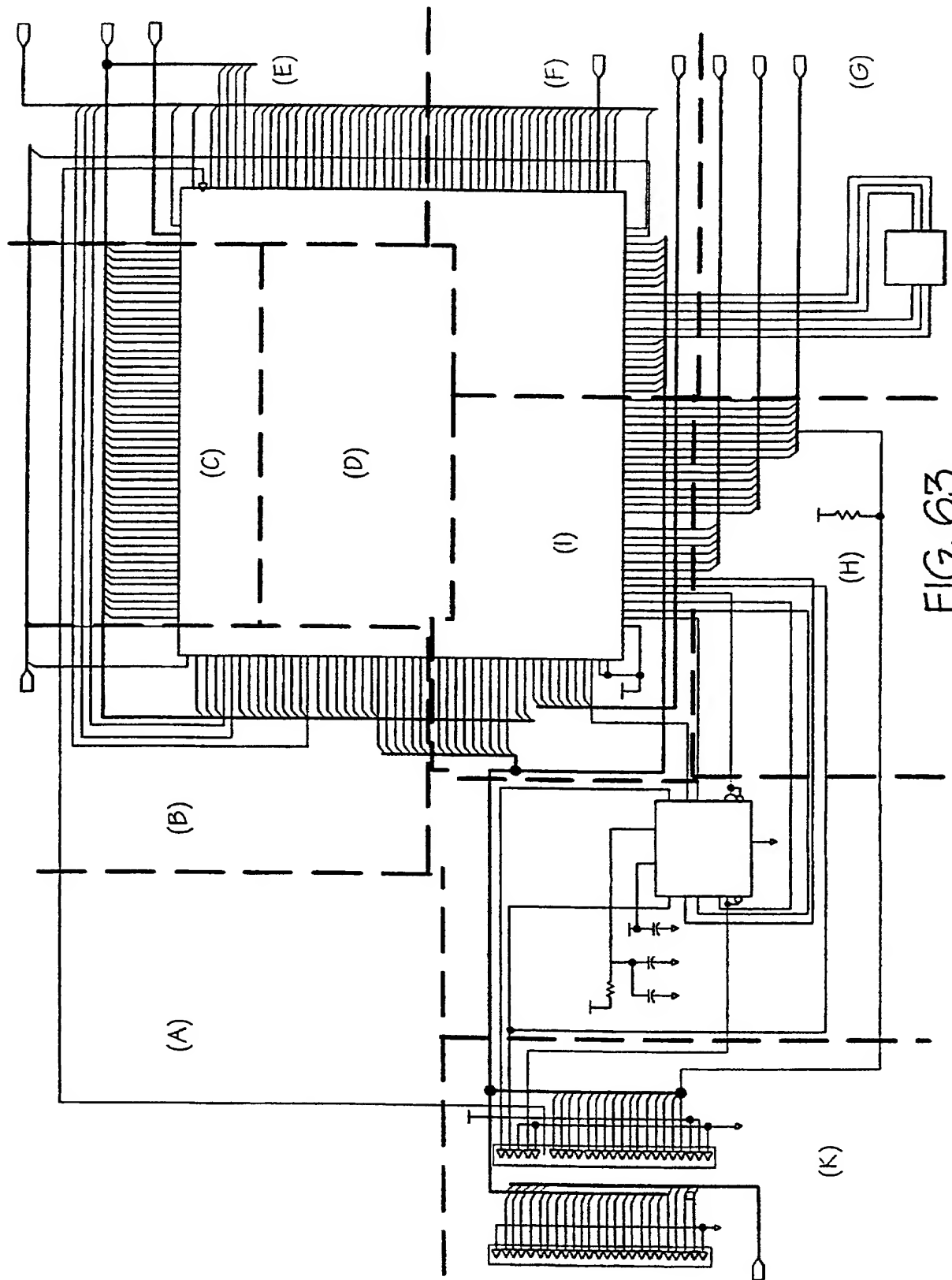


FIG. 63

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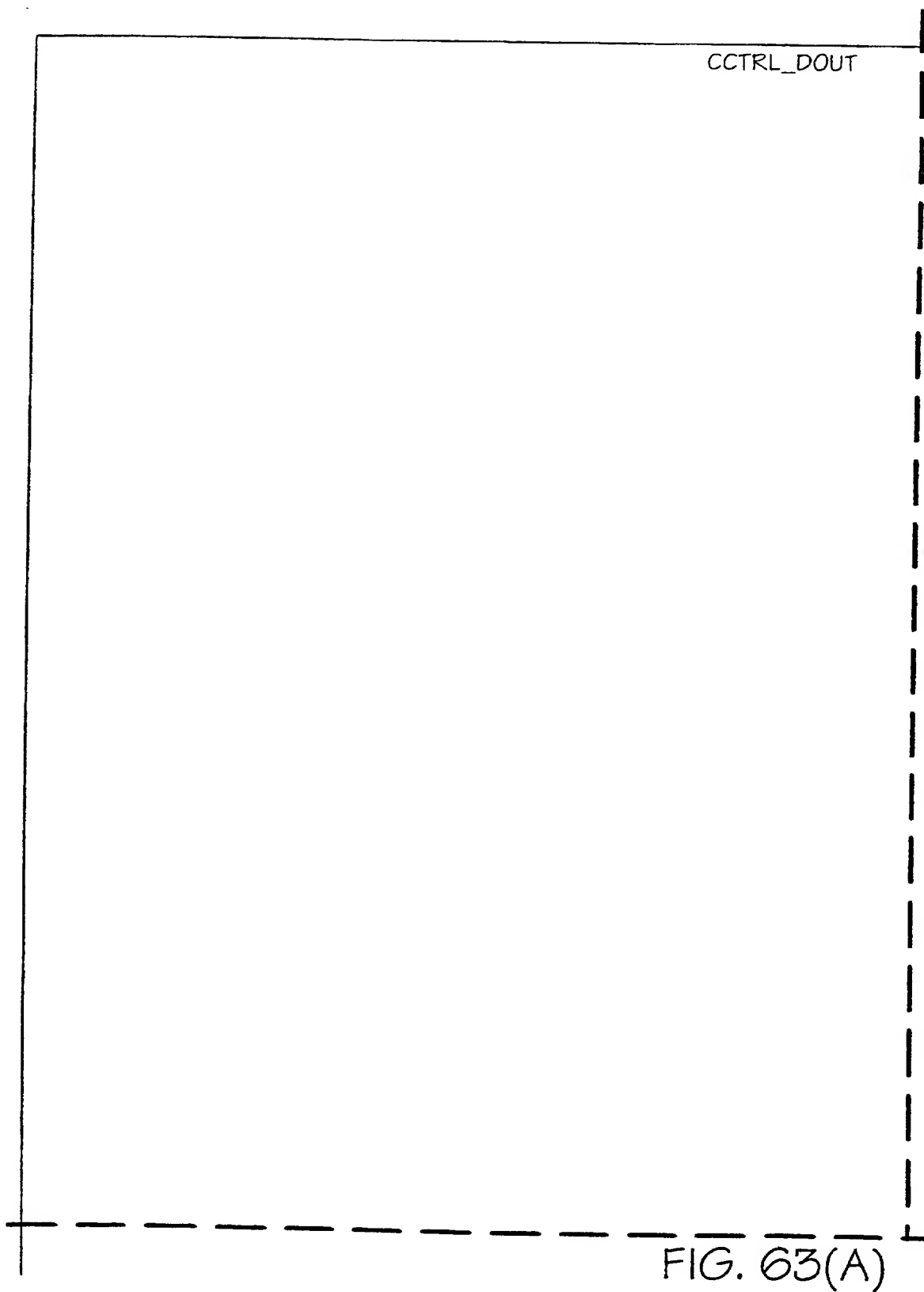


FIG. 63(B)

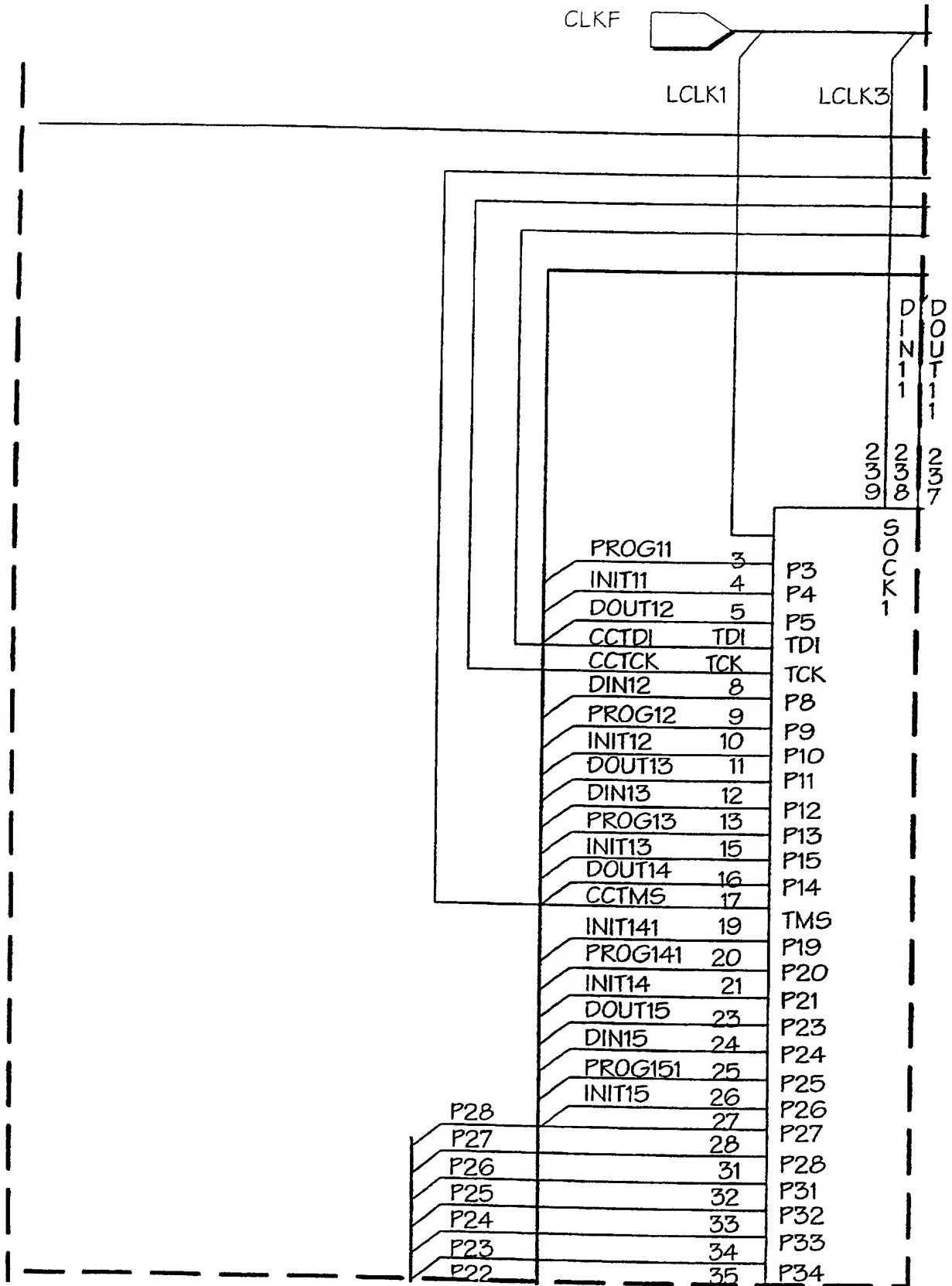


FIG. 63(B)

DOU10	1184	P1185
D-N0	1185	P1186
PROG0	1187	P1188
-N10	1188	P1189
DOU11	1189	P1190
D-N1	1190	P1191
PROG1	1191	P1192
-N11	1192	P1193
DOU12	1193	P1194
D-N2	1194	P1195
PROG2	1195	P1197
-N12	1197	P1198
DOU13	1198	P1199
D-N3	1199	P2200
PROG3	2000	P2202
-N13	2002	P2203
DOU14	2003	P2205
D-N4	2005	P2206
PROG4	2006	P2207
-N14	2007	P2208
DOU15	2008	P2209
D-N5	2009	P2210
PROG5	2100	P2213
-N15	2103	P2214
DOU16	2104	P2215
D-N6	2105	P2216
PROG6	2106	P2217
-N16	2107	P2218
DOU17	2108	P2219
D-N7	2200	P2220
PROG7	2201	P2223
-N17	2203	P2224
DOU18	2204	P2225
D-N8	2205	P2226
PROG8	2206	P2228
-N18	2208	P2229
DOU19	2209	P2230
D-N9	2230	P2231
PROG9	2231	P2232
-N19	2232	P2233
DOU110	2233	P2234
D-N10	2234	P2235
PROG10	2235	P2236
-N110	2236	P2237
DOU111	2237	P2238

FIG. 63(C)

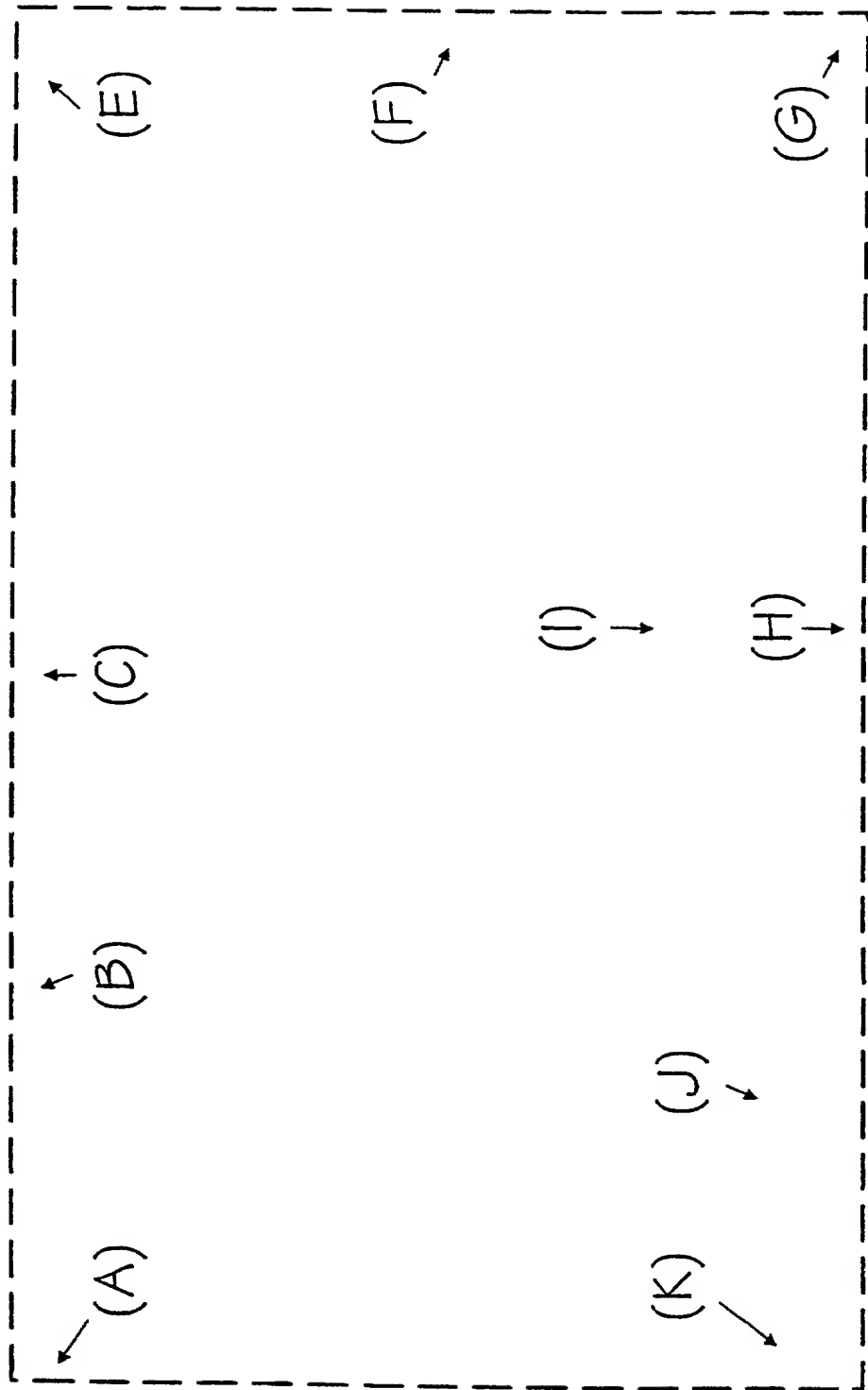


FIG. 63(D)



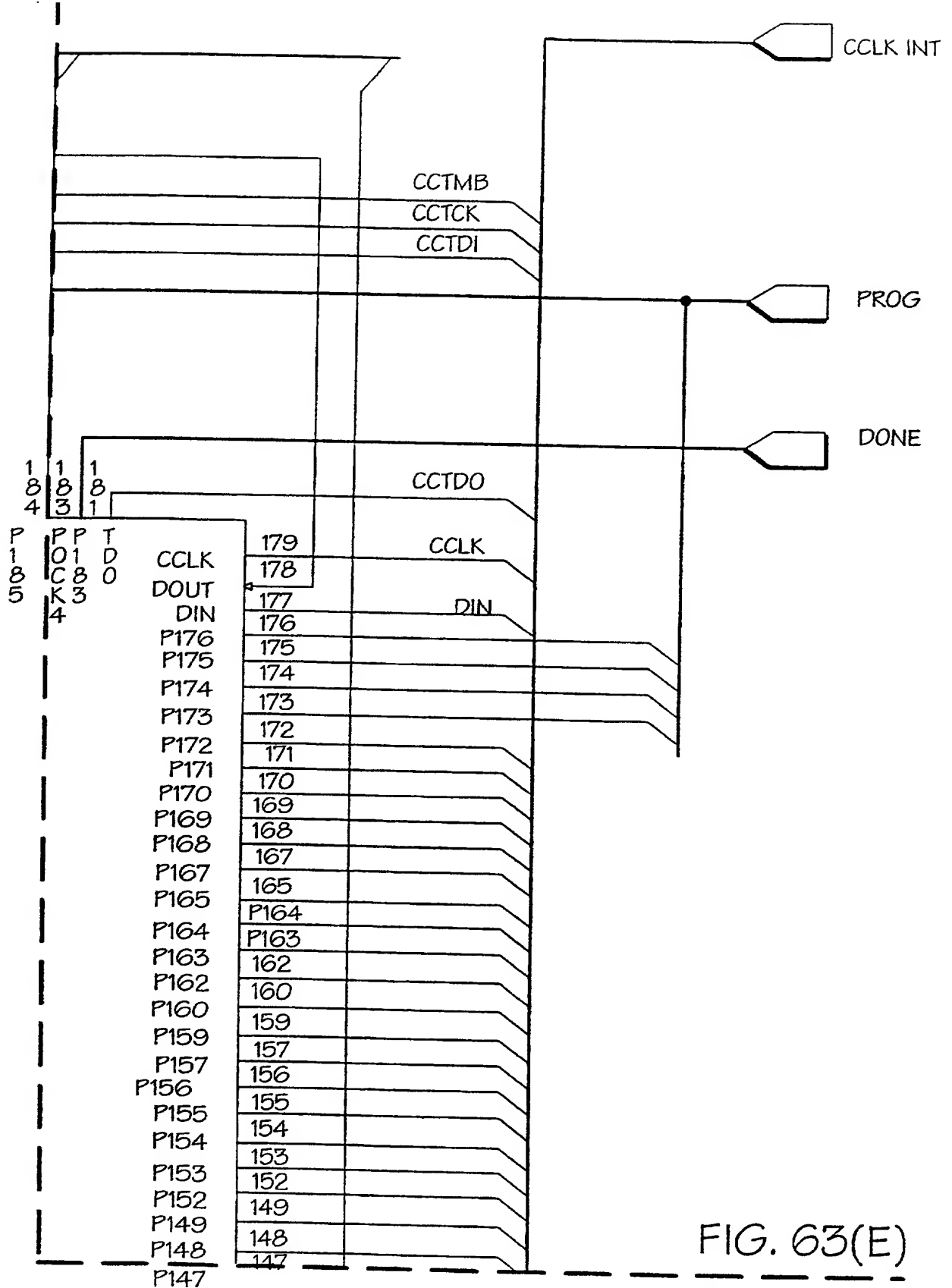


FIG. 63(E)

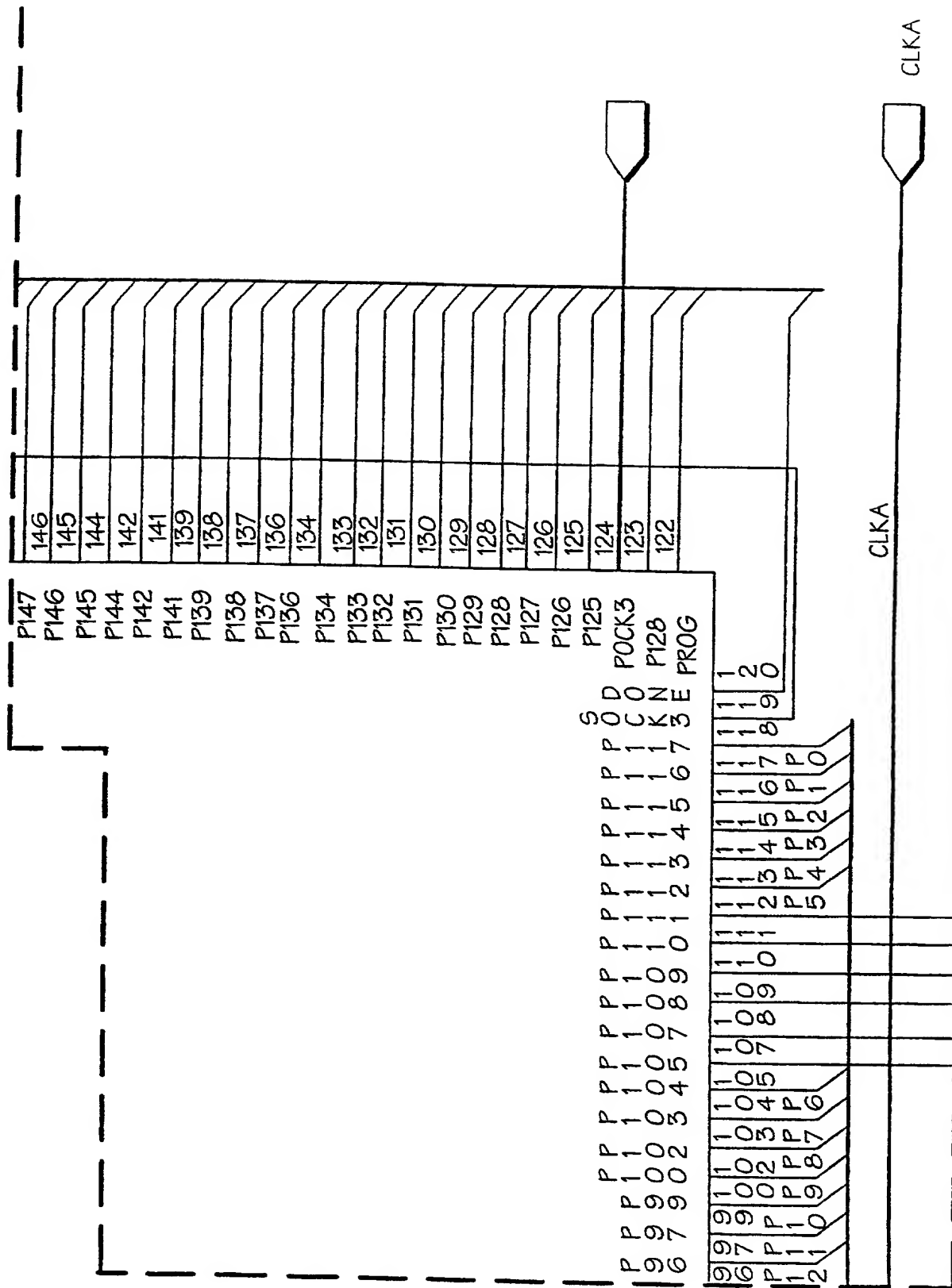


FIG. 63(F)

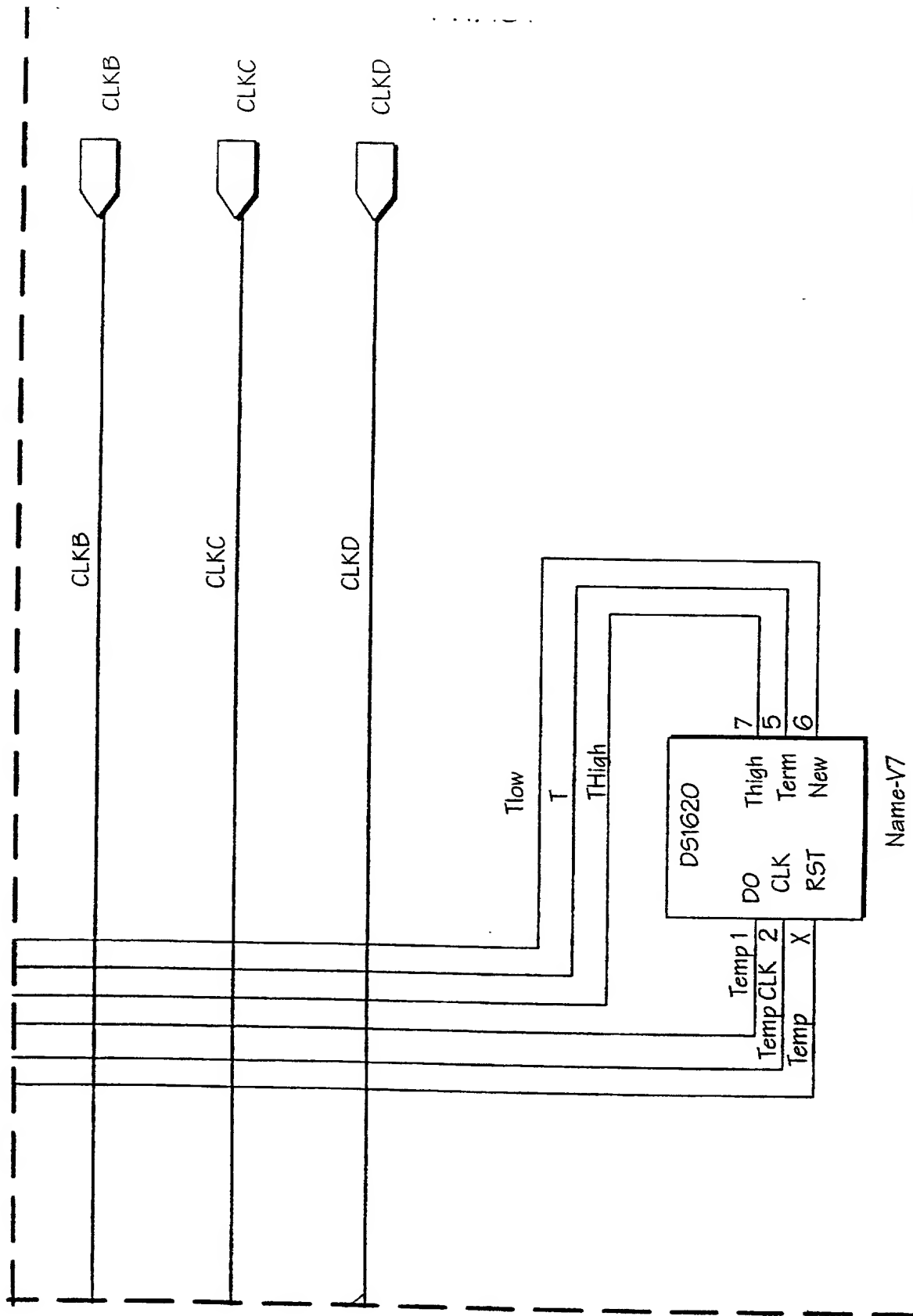


FIG. 63(G)

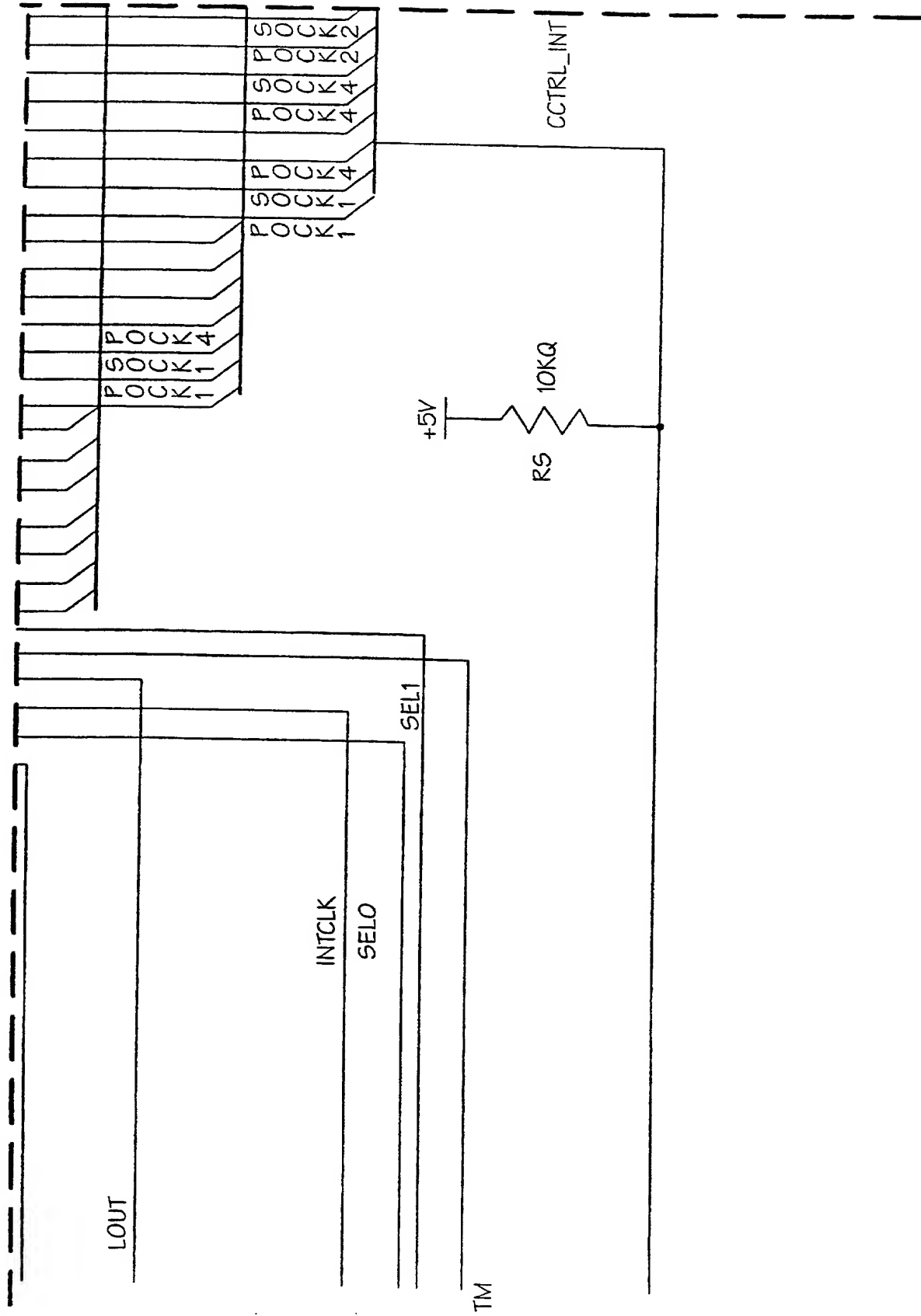


FIG. 63(H)

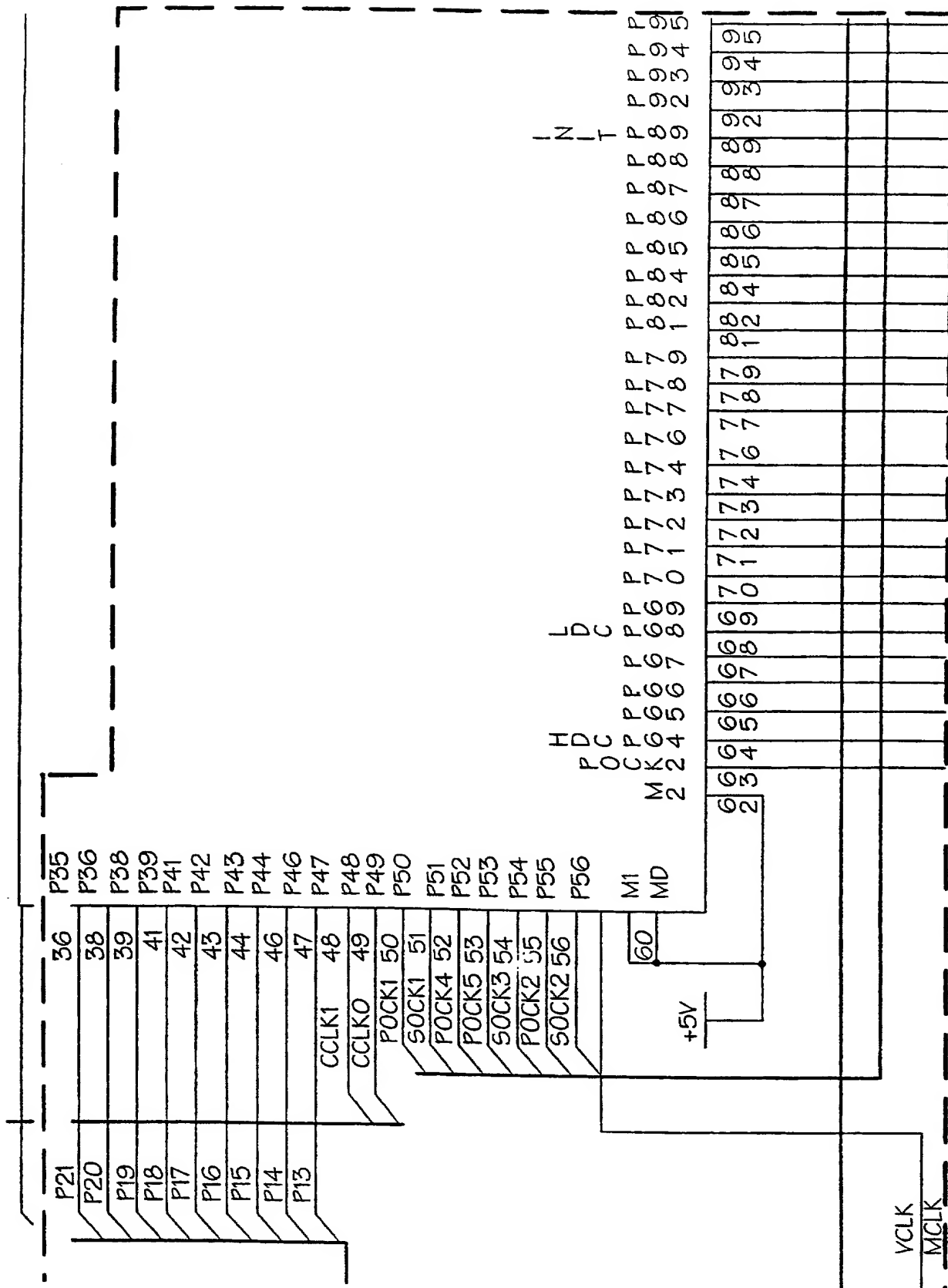


FIG. 63(I)

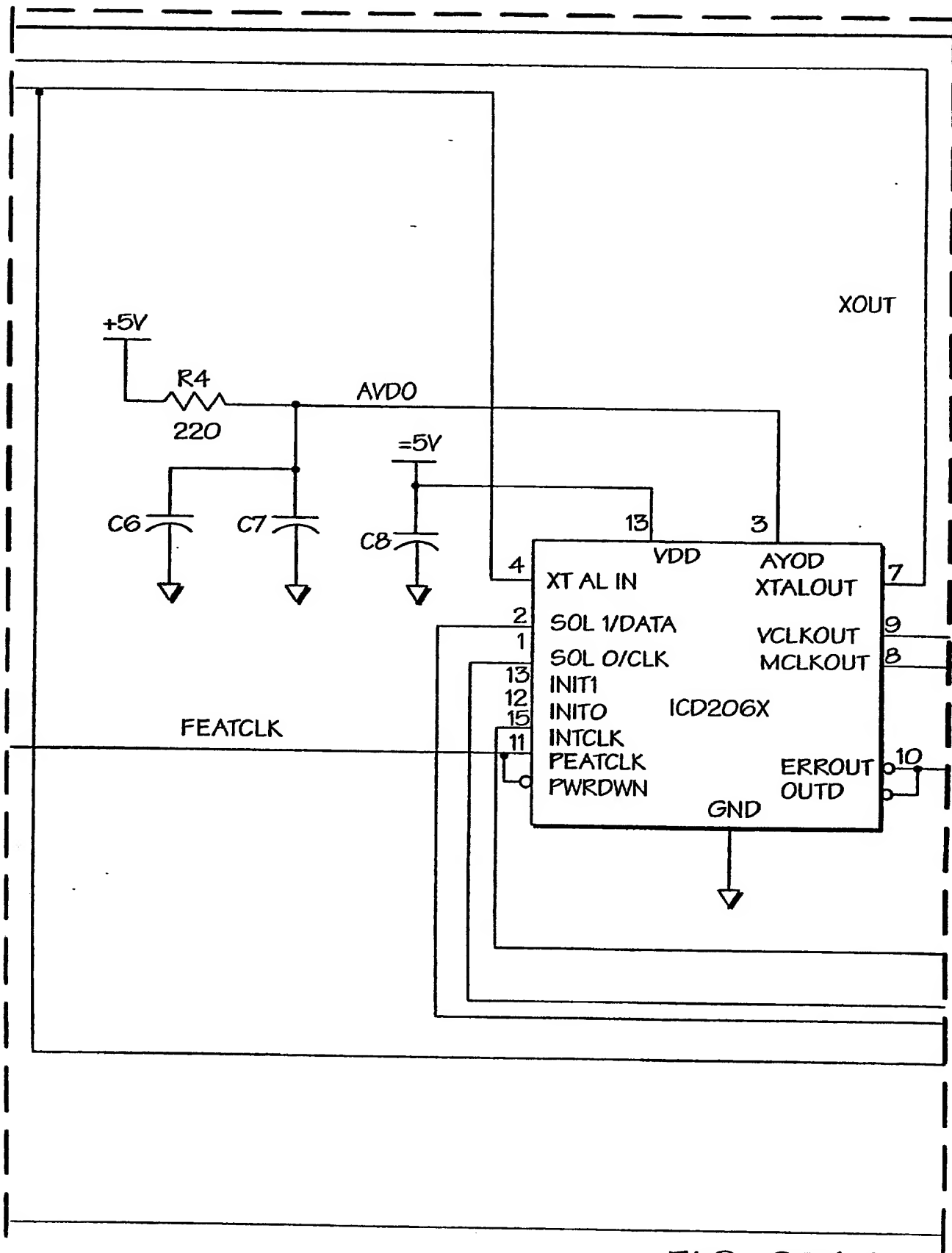


FIG. 63(J)

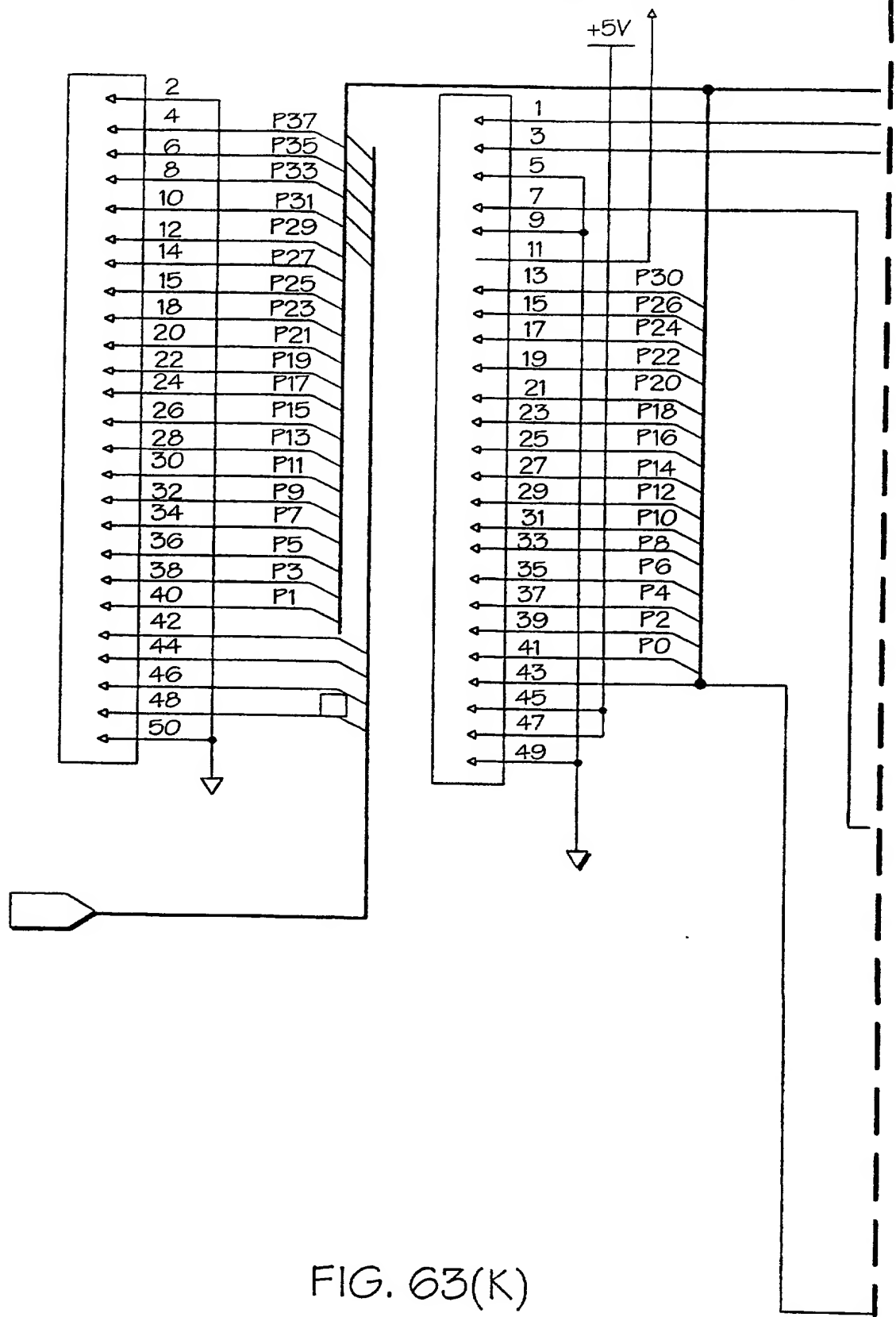


FIG. 63(K)

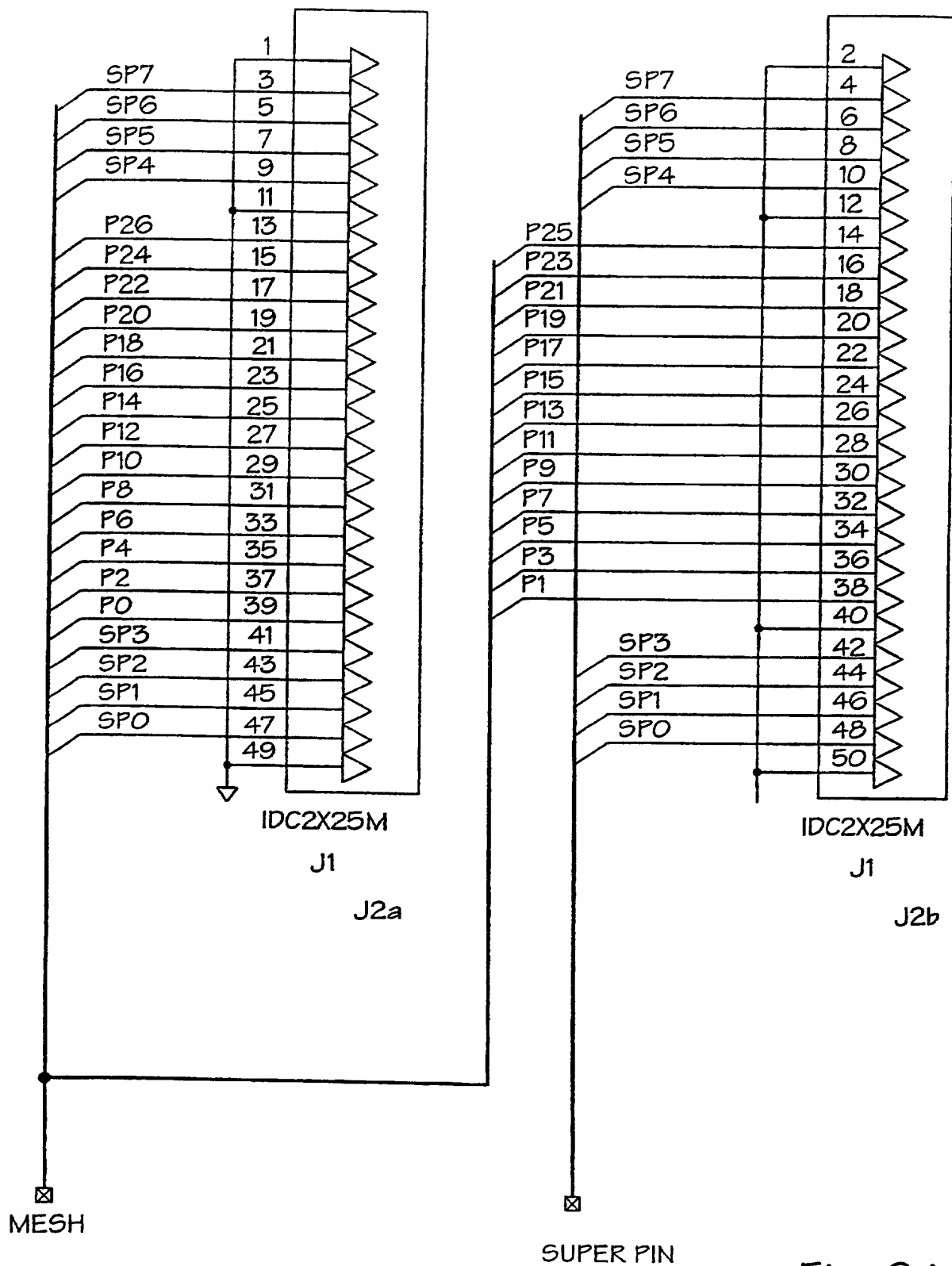


Fig. 64



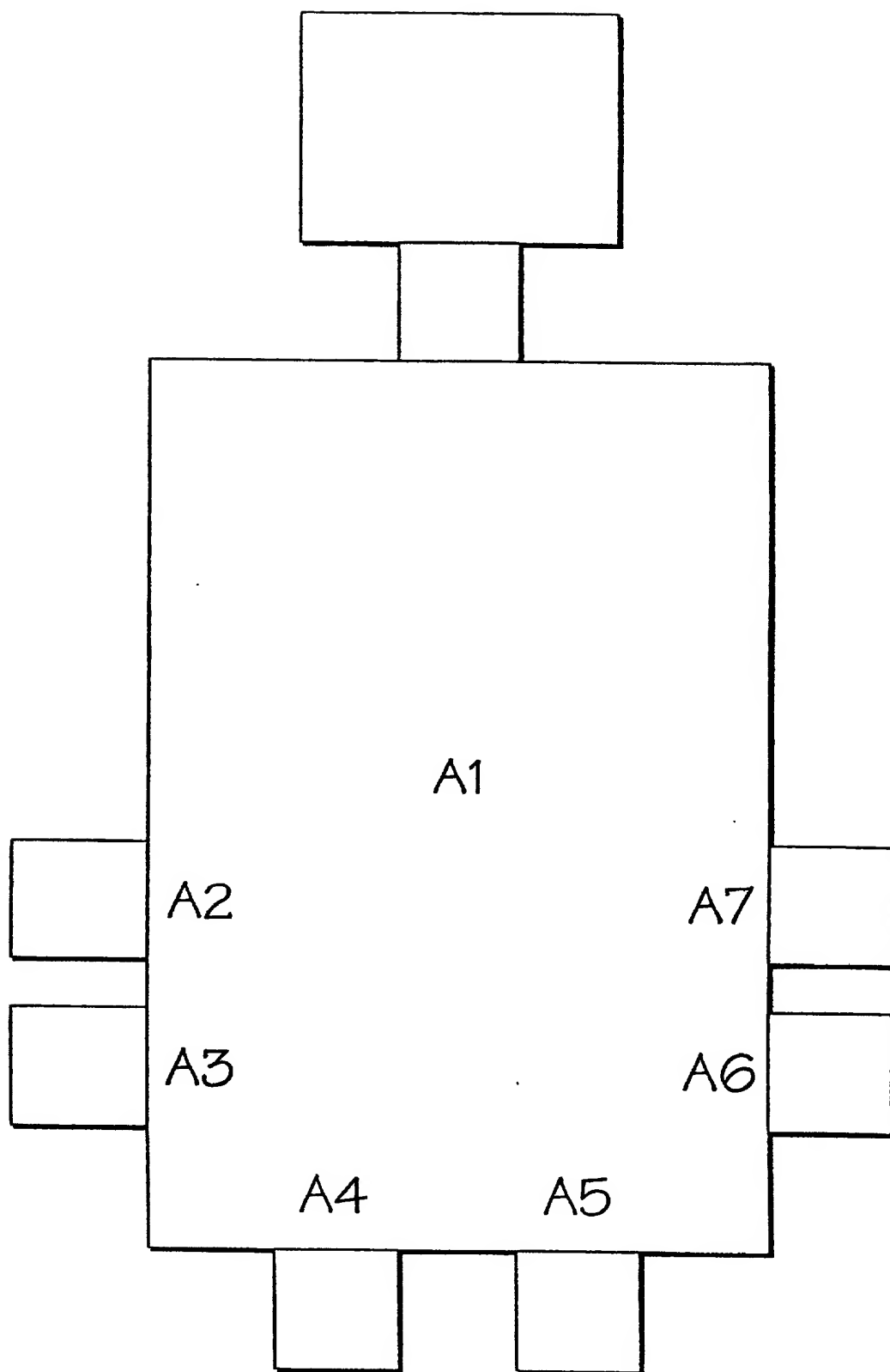


FIG. 65

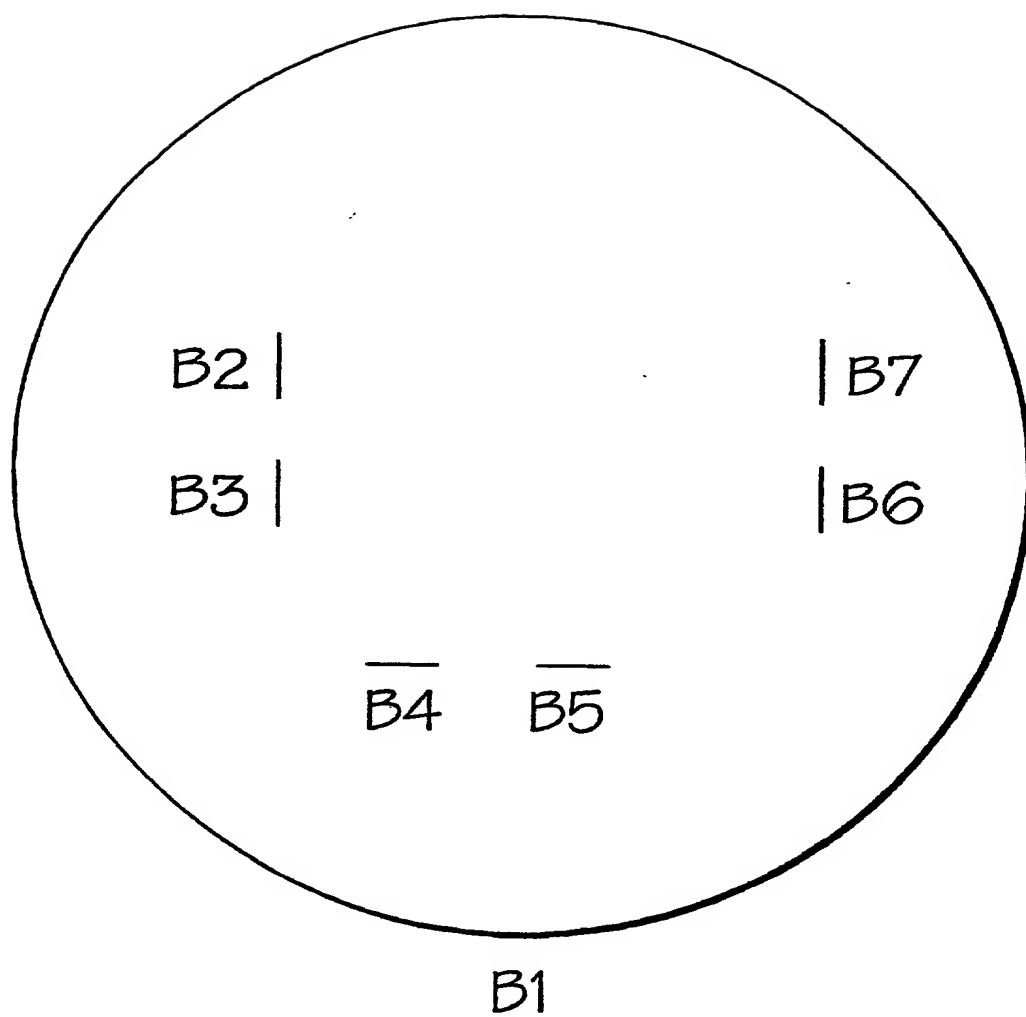


FIG. 66

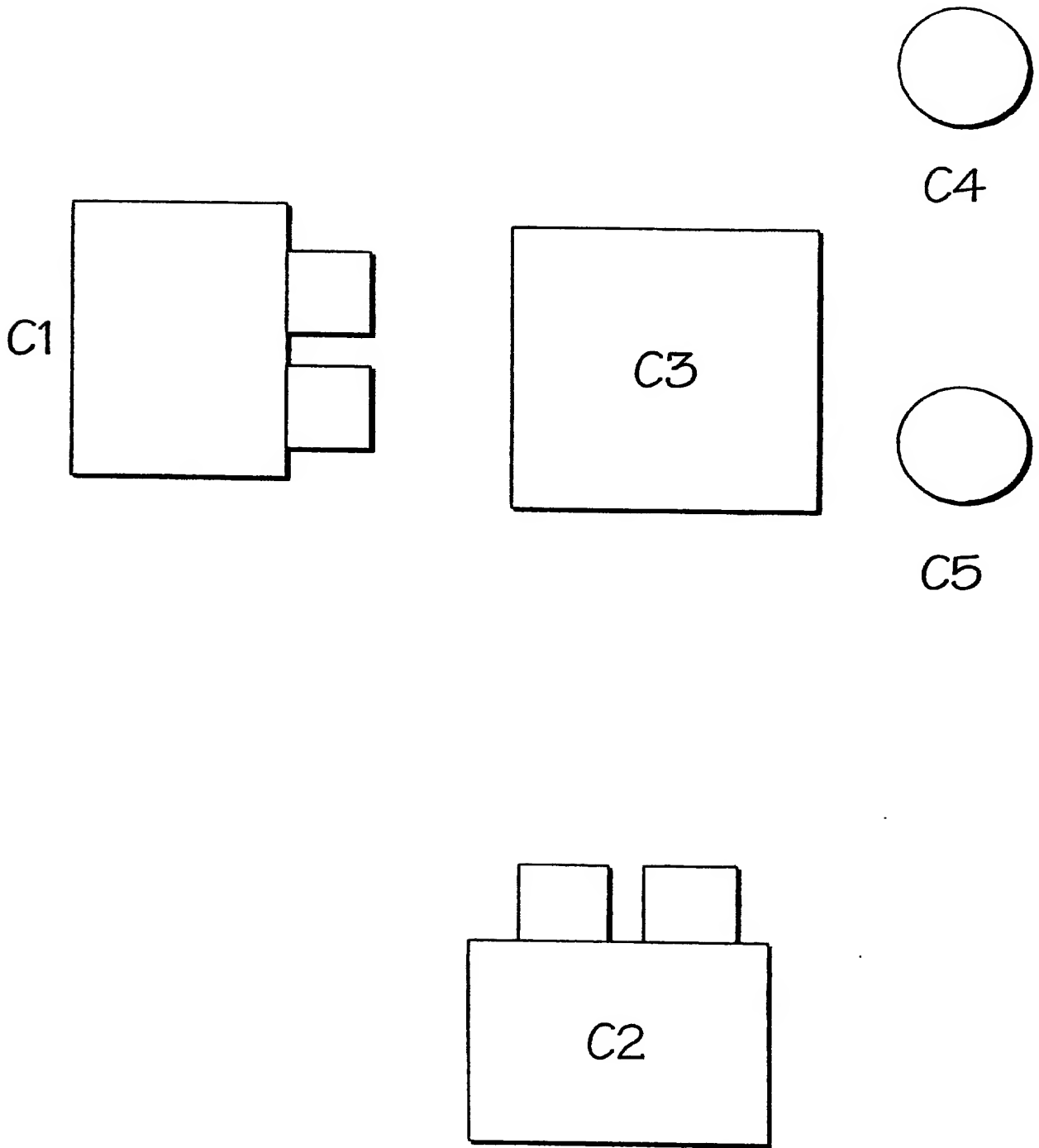


FIG. 67

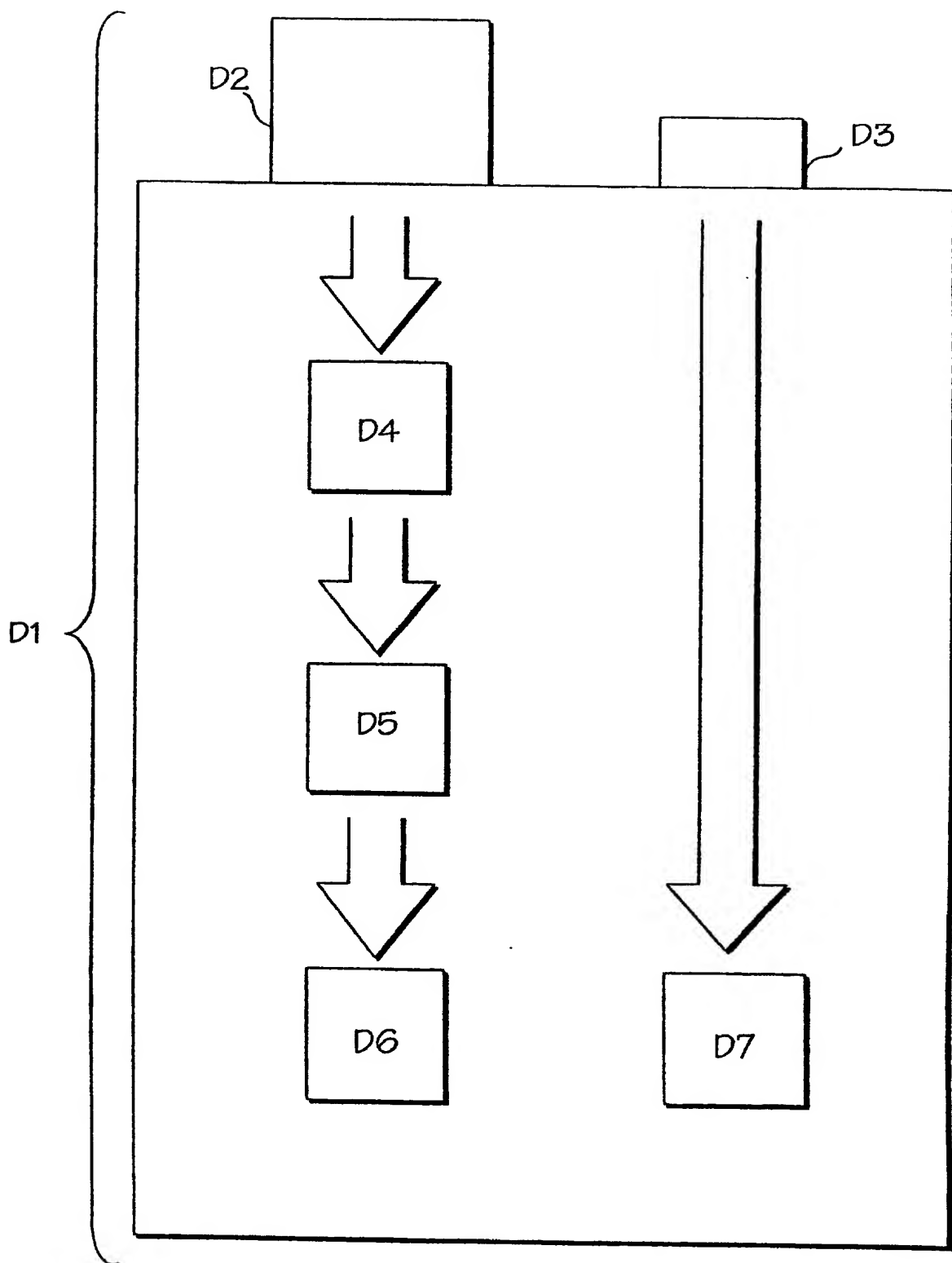


FIG. 68

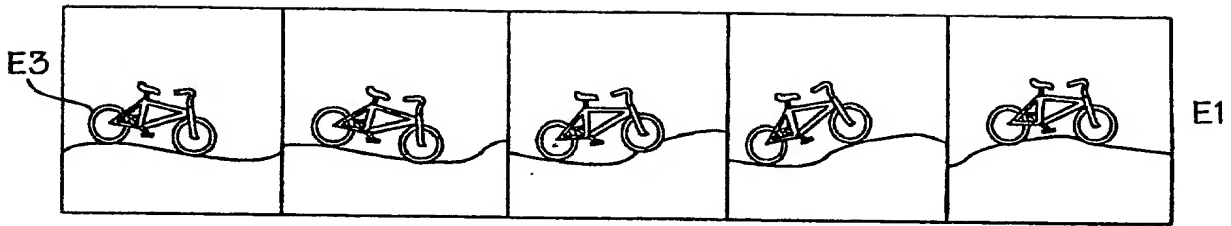


FIG. 69(A)

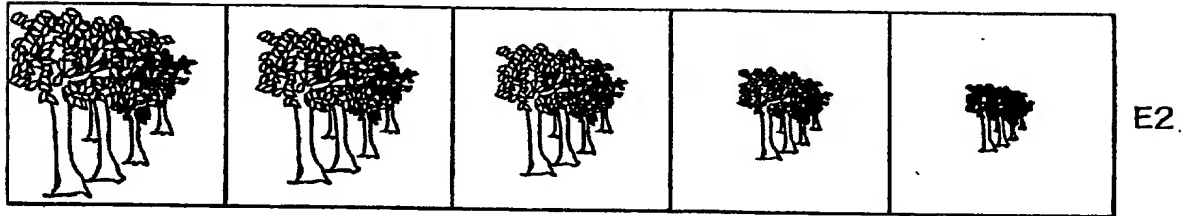


FIG. 69(B)

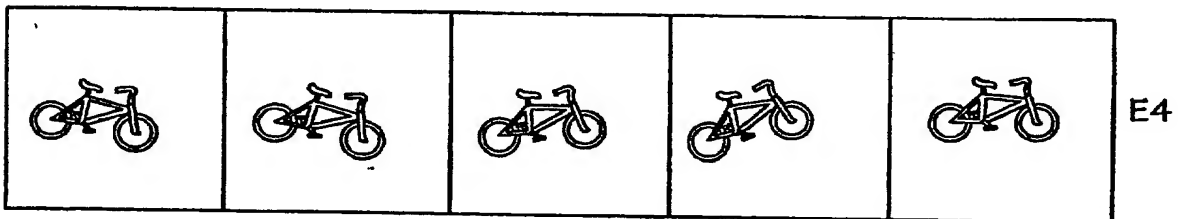


FIG. 69(C)

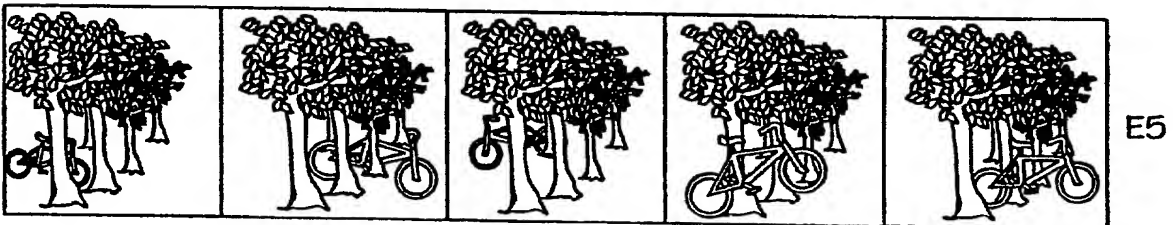


FIG. 69(D)